

# **APPLICATIONS**

■ Used with Gate Turn-Off Thyristors in high current switching applications

# **CONDITIONS - (UNLESS STATED OTHERWISE)**

V <sub>1</sub> = +5V	V <sub>2</sub> = +15V		V <sub>3</sub> = -15V	
Test circuit GTO		DG758BX		
GDU connection to GTO		500mm CO - AX cable type RC5327230		
Test circuit emitter and gate drive emitter		Honeywell sweetspot HFE 4020 - 013		
Test circuit emitter current		30mA		
Test circuit receiver and gate drive receiver		Honeywell sweetspot HFD 3029 - 002		

# **ELECTRICAL CHARACTERISTICS**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I <sub>v1</sub>	+5V PSU current	500Hz, 50% duty cycle	-	-	4.40	A
I <sub>v2</sub>	+15V PSU current	500Hz	-	-	0.48	А
I <sub>V3</sub>	-15V PSU current	500Hz, I <sub>T</sub> = 3000A GTO T <sub>j</sub> = 125°C	-	-	10.0	А
V <sub>1(Min)</sub>	+5V PSU minimum	-	3.8	-	-	V
V <sub>2(Min)</sub>	+15V PSU minimum	-	14.0	-	-	V
V <sub>3(Min)</sub>	-15V PSU minimum	-	14.0	-	-	V
I <sub>FGM</sub>	Peak forward gate current	-	40	-	-	А
I <sub>G(ON)</sub>	On-state gate current	-	-	8	-	A
dl <sub>FG</sub> /dt	Rate of rise of positive gate current	Measured 10 - 75% I <sub>FGM</sub>	-	40	-	A/μs
dl <sub>GQ</sub> /dt	Rate of rise of negative gate current	I <sub>T</sub> = 3000A, 90% I <sub>G(ON)</sub> - 50% I <sub>GQM</sub>	-	40	-	A/μs

# GDU 90-20302

# **Gate Drive Unit**



**KEY PARAMETERS** 

I<sub>FGM</sub>

I<sub>G(ON)</sub> dI<sub>GQ</sub>∕dt

40A

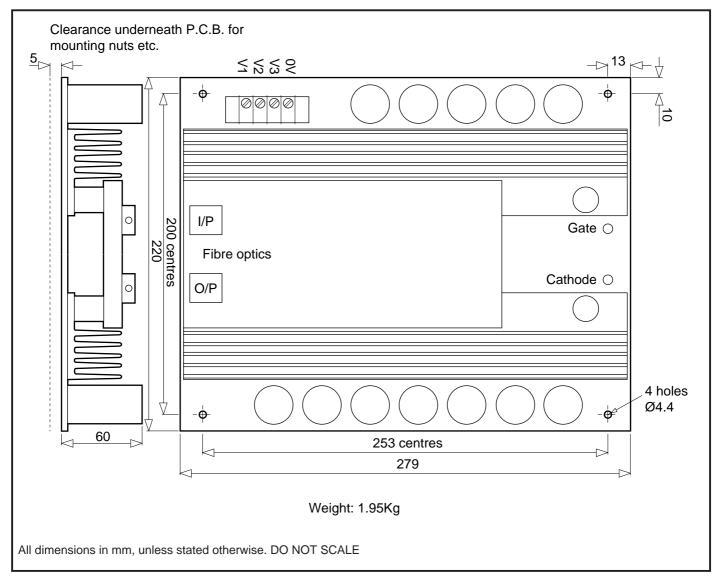
8A

40A/µs

## **TIMING CHARACTERISTICS**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units				
t,*†	No response pulse width of Adjustable by R81 + R82		2	-	3	μs				
t <sub>2</sub>	Delay time emitter current to receiver o/p	-	0.4	-	0.8	μs				
t <sub>3</sub> *†	Turn-on delay emitter current to 10% I <sub>FGM</sub>	-	5.2	-	6.2	μs				
t <sub>4</sub>	I <sub>FGM</sub> pulse width	-	-	16	-	μs				
t <sub>5</sub> *	Minimum on time 10% $I_{FGM}$ to 90% $I_{G(ON)}$	Adjustable by R37	80	-	110	μs				
t <sub>6</sub>	Receiver storage time	-	0.5	-	0.9	μs				
t <sub>7</sub>	Turn-off delay. Emitter current to 90% I <sub>G(ON)</sub>	-	1.5	-	2.3	μs				
t <sub>s</sub> *	Minimum off time 90% $I_{G(ON)}$ to 10% $I_{FGM}$	Adjustable by R38	80	-	110	μs				
t <sub>9</sub>	Delay time Gate volts to o/p emitter current		-	0.1	-	μs				
t <sub>10</sub>	Turn-off delay Gate volts to test receiver o/p	-	-	0.7	-	μs				
t <sub>11</sub>	Storage time Gate volts to o/p emitter current	Measured at low I <sub>GQM</sub>	-	0.1 <sup>1</sup>	-	μs				
t <sub>12</sub>	Turn-on delay Gate volts to test receiver o/p	Measured at low I <sub>GQM</sub>	-	0.8 <sup>1</sup>	-	μs				
* $t_1, t_3, t_5, t_8$ are factory settings. <sup>†</sup> Adjustment of $t_1$ alters $t_3$ . 1. Varies with $I_{GQM}$ due to gate lead impdeance.										
Test circuit emitter current Control card receiver output Gate current Gate current Gate voltage Control card emitter current Test circuit receiver output Test circuit receiver output Control card emitter current Test circuit receiver output Test circuit receiver										

## OUTLINE



#### GDU 90 20302

#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink / clamping systems in line with advances in device types and the voltage and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group continues to offer high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the up to date CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete solution (PACs).

#### **DEVICE CLAMPS**

Disc devices require the correct clamping force to ensure their safe operation. The PACs range offers a varied selection of preloaded clamps to suit all of our manufactured devices. This include cube clamps for single side cooling of 'T' 22mm

Clamps are available for single or double side cooling, with high insulation versions for high voltage assemblies.

Please refer to our application note on device clamping, AN4839

#### HEATSINKS

Power Assembly has it's own proprietary range of extruded aluminium heatsinks. They have been designed to optimise the performance or our semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request

For further information on device clamps, heatsinks and assemblies, please contact your nearest Sales Representative or the factory.



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