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HAT2140H

Silicon N Channel Power MOS FET Power Switching



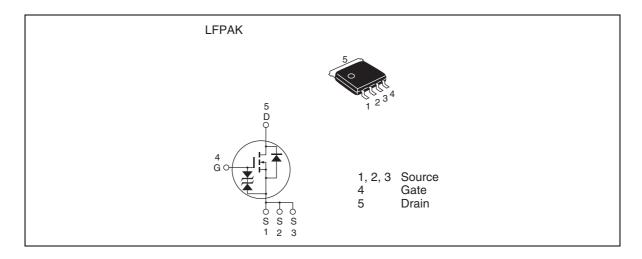
ADE-208-1581B(Z)

Preliminary 3rd. Edition Aug. 2002

Features

- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 12.5 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V)}$

Outline



HAT2140H

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	25	A
Drain peak current	Note1 D(pulse)	100	A
Body-drain diode reverse drain current	I _{DR}	25	A
Avalanche current	Note 3	25	A
Avalanche energy	E _{AR} Note 3	62.5	mJ
Channel dissipation	Pch Note2	30	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to + 150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Tc=25°C
- 3. Value at Tch = 25°C, Rg \geq 50 Ω

Electrical Characteristics

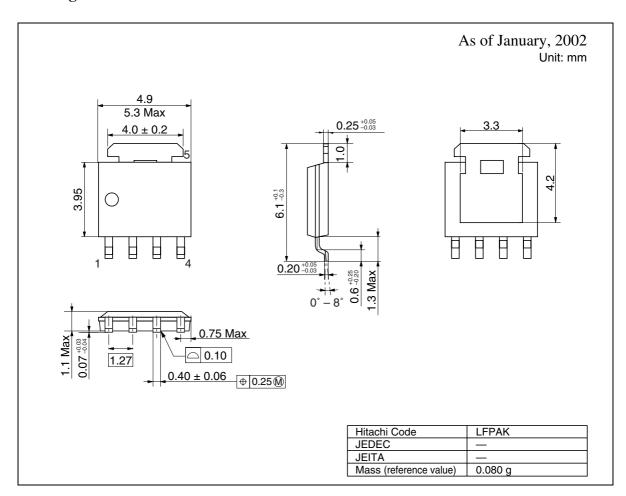
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{_{(BR)DSS}}$	100	_	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{_{\text{GS(off)}}}$	2.0	_	3.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	$R_{\scriptscriptstyle DS(on)}$	_	12.5	16.0	mΩ	$I_{D} = 12.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	_	13.5	18.0	mΩ	$I_D = 12.5 \text{ A}, V_{GS} = 7 \text{ V}^{Note4}$
Forward transfer admittance	ly _{fs} l	27	45	_	S	$I_{D} = 12.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	6500	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	480	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	210	_	pF	f = 1 MHz
Total gate charge	Qg	_	105	_	nc	V _{DD} = 50 V
Gate to source charge	Qgs	_	20	_	nc	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Qgd	_	22	_	nc	I _D = 25 A
Turn-on delay time	t _{d(on)}	_	25	_	ns	$V_{GS} = 10 \text{ V}, I_{D} = 12.5 \text{ A}$
Rise time	t _r	_	24	_	ns	$V_{DD} \cong 30 \text{ V}$
Turn-off delay time	t _{d(off)}	_	100	_	ns	$R_L = 2.4 \Omega$
Fall time	t _f	_	12	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V _{DF}	_	0.83	1.08	V	$IF = 25 A, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery time	t _{rr}	_	55	_	ns	IF = 25 A, $V_{GS} = 0$ diF/ dt = 100 A/ μ s

Notes: 4. Pulse test

HAT2140H

Package Dimensions



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Sales Offices

TACH

Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Öhte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL http://www.hitachisemiconductor.com/

For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1> (408) 433-1990 Maidenhead

Hitachi Europe Ltd. Electronic Components Group Whitebrook Park Lower Cookham Road Fax: <1>(408) 433-0223 Berkshire SL6 8YA, United Kingdom Fax: <65>-6538-6933/6538-3877 Tel: <44> (1628) 585000

> Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen Postfach 201, D-85619 Feldkirchen Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Fax: <44> (1628) 585200

Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00 Singapore 049318 Tel: <65>-6538-6533/6538-8577

URL: http://semiconductor.hitachi.com.sg Tel: <852>-2735-9218 Hitachi Asia Ltd.

(Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP URL: http://www.hitachi.com.tw

7/F., North Tower World Finance Centre Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong

Fax: <852>-2730-0281

Hitachi Asia (Hong Kong) Ltd.

Group III (Electronic Components)

URL: http://semiconductor.hitachi.com.hk

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