HSB278S

Silicon Schottky Barrier Diode for High Speed Switching

HITACHI

ADE-208-1383 (Z) Rev.0 Mar. 2001

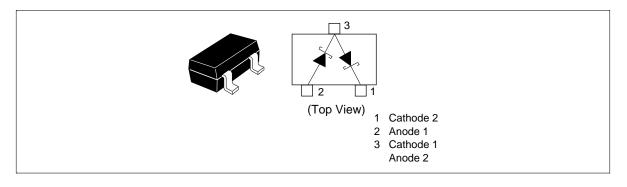
Features

- Low forward voltage, Low capacitance.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HSB278S	S2	CMPAK

Outline





HSB278S

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit	
Repetitive peak reverse voltage	V_{RRM}	30	V	
Reverse voltage	V_R	30	V	
Non-Repetitive peak forward surge current	I _{FSM} *1 *2	200	mA	
Peak forward current	I _{FM} *2	150	mA	
Average rectified current	I _O *2	30	mA	
Junction temperature	Tj	125	°C	
Storage temperature	Tstg	-55 to +125	°C	

Notes: 1. 10 msec sine wave 1 pulse

2. Per one device.

Electrical Characteristics (Ta = 25°C) *1

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F1}	_	_	0.30	V	I _F = 1 mA
	V_{F2}	_	_	0.95		I _F = 30 mA
Reverse current	I _R	_	_	700	nA	V _R = 10 V
Capacitance	С	_	_	1.50	pF	V _R = 1 V, f = 1 MHz
ESD-Capability *1		100			V	C = 200 pF, $R_L = 0 \Omega$, Both forward and reverse direction 1 pulse.

Notes: 1. Per one device.

2. Failure criterion ; $I_{\textrm{R}} > 1.4~\mu\textrm{A}$ at $V_{\textrm{R}}$ = 10 V

Main Characteristic

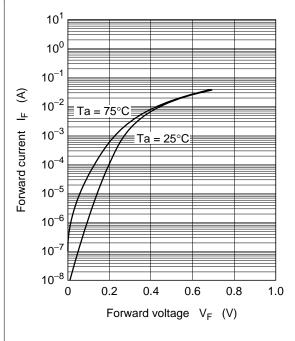
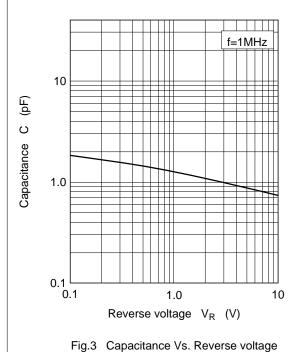


Fig.1 Forward current Vs. Forward voltage



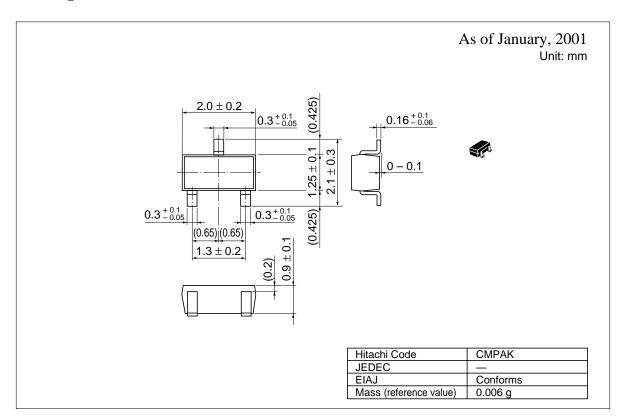
 10^{-4} Ta = 75°C

Ta = 25°C 10^{-8} 10^{-8} 10^{-8} 10^{-8} 10^{-8} Reverse voltage V_R (V)

Fig.2 Reverse current Vs. Reverse voltage

HSB278S

Package Dimensions



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