



Low Profile, High Current Inductor



US PATENTED 6,204,744, several foreign patents, and other patents pending

STANDARD ELECTRICAL SPECIFICATIONS				
Lo INDUCTANCE μH ± 20% @100KHz, .25V, 0A	DCR mΩ TYPICAL 25°C	DCR mΩ MAX 25°C	HEAT RATING CURRENT DC AMPS ³ TYPICAL	SATURATION CURRENT DC AMPS ⁴ TYPICAL
0.10	0.47	0.50	60	120
0.15	0.53	0.60	55	118
0.22	0.63	0.70	53	112
0.30	0.70	0.80	48	72
0.33	0.83	0.90	46	65
0.40	0.90	1.0	44	64
0.47	1.0	1.2	41	63
0.56	1.2	1.4	37	62
0.68	1.4	1.6	35	60
0.82	1.6	1.9	33	50
1.0	1.7	2.0	32	49
1.2	2.1	2.5	30	48
1.5	2.5	3.0	27	45
2.2	3.5	4.2	22	40
3.3	5.7	6.8	18	35
4.7	9.3	11.2	13.5	30

- NOTES:**
- All test data is referenced to 25°C ambient.
 - Operating Temperature Range - 55°C to + 125°C
 - DC current (A) that will cause an approximate ΔT of 40°C.
 - DC current (A) that will cause Lo to drop approximately 20%
 - The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

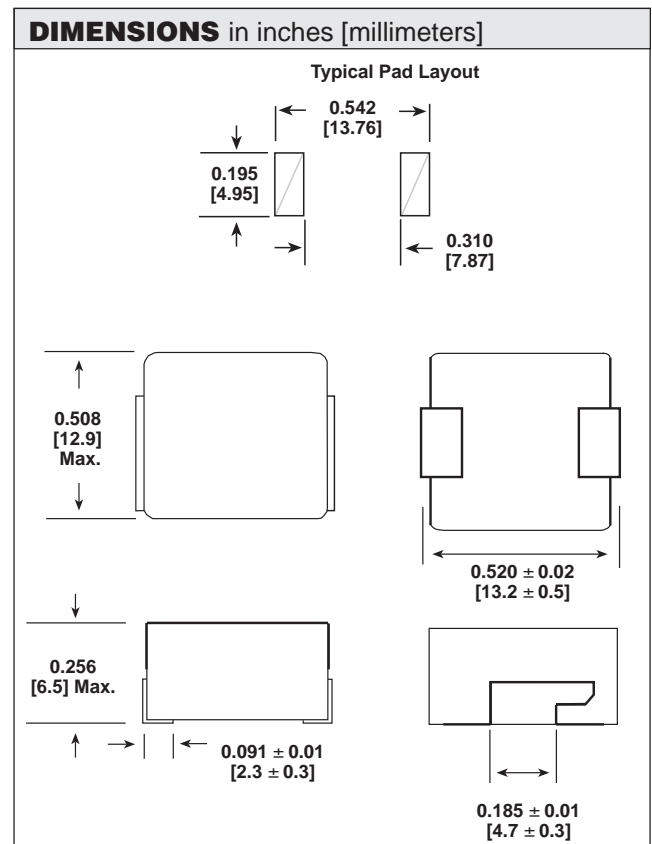
DESCRIPTION		
IHLP-5050FD-01	1.0μH	± 20%
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE

FEATURES

- Shielded construction.
- Frequency range up to 5.0MHz.
- Lowest DCR/μH, in this package size.
- Handles high transient current spikes without saturation.
- Ultra low buzz noise, due to composite construction.
- 100% lead (Pb) free.

APPLICATIONS

- Notebook/Desktop/Server applications.
- High current POL converters.
- Low profile, high current power supplies.
- Battery powered devices.
- DC/DC converters in distributed power systems.
- DC/DC converter for Field Programmable Gate Array (FPGA).



SAP PART NUMBERING GUIDELINES (INTERNAL)

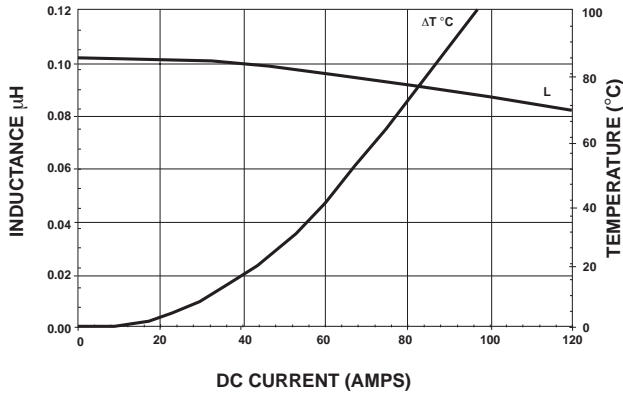
I	H	L	P	5	0	5	0	F	D	R	Z	1	R	0	M	0	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES				

See the end of this data book for conversion tables

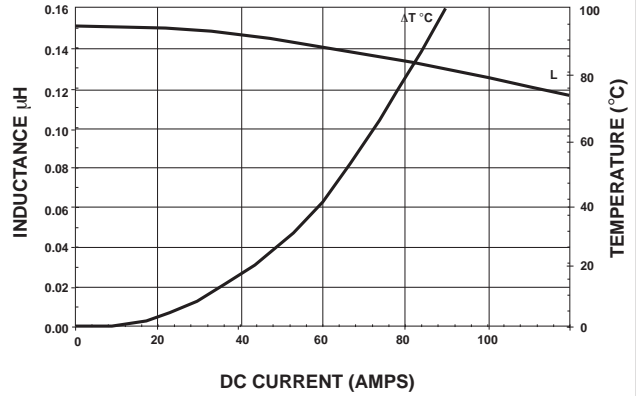


PERFORMANCE GRAPHS

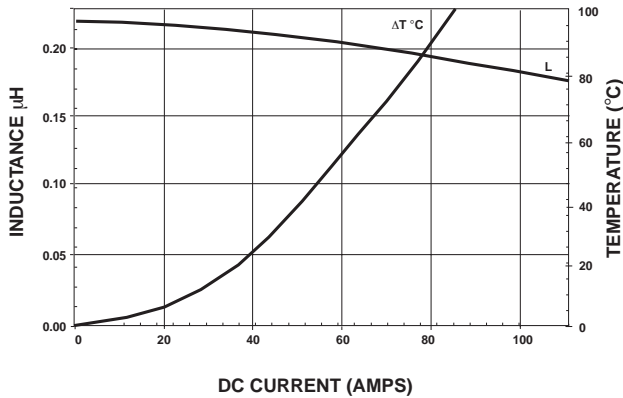
IHLP-5050FD-01 0.10 μ H



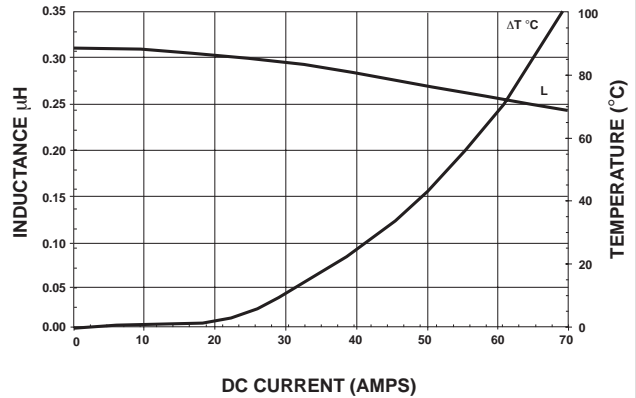
IHLP-5050FD-01 0.15 μ H



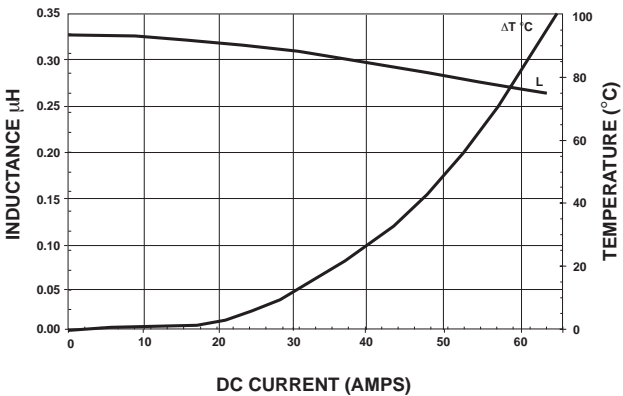
IHLP-5050FD-01 0.22 μ H



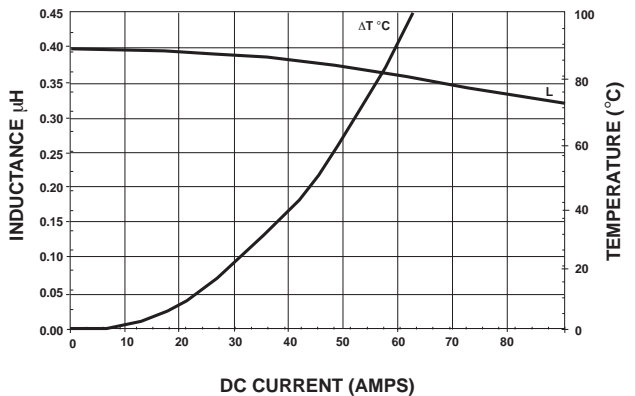
IHLP-5050FD-01 0.30 μ H



IHLP-5050FD-01 0.33 μ H



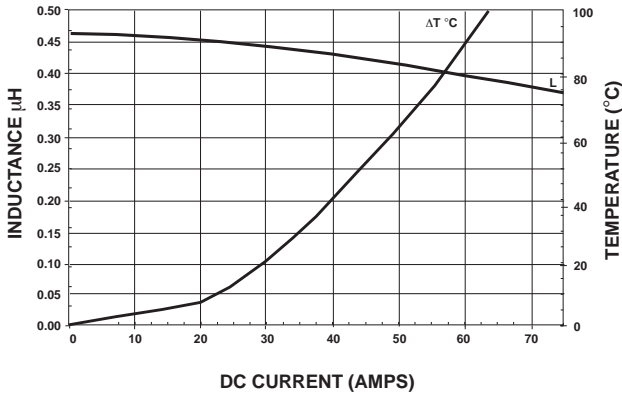
IHLP-5050FD-01 0.40 μ H



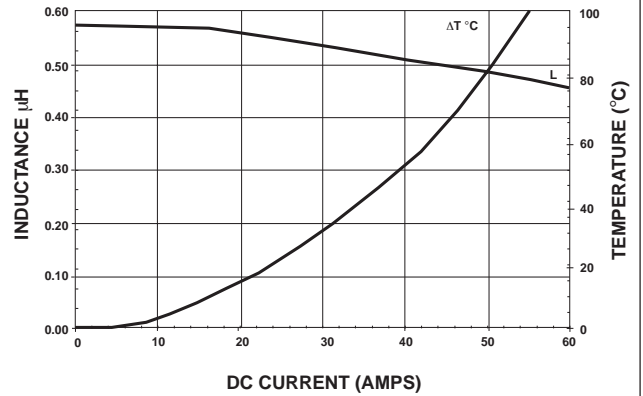


PERFORMANCE GRAPHS

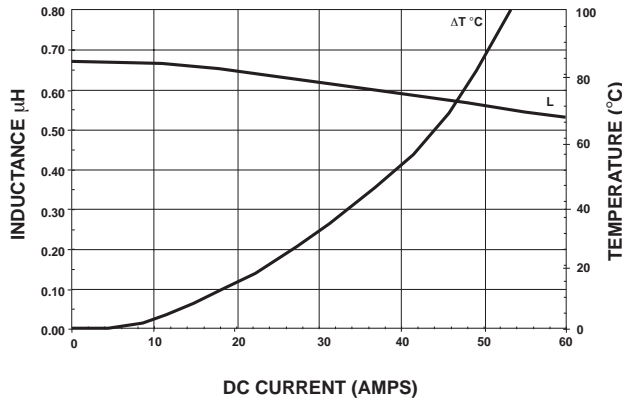
IHLP-5050FD-01 0.47 μ H



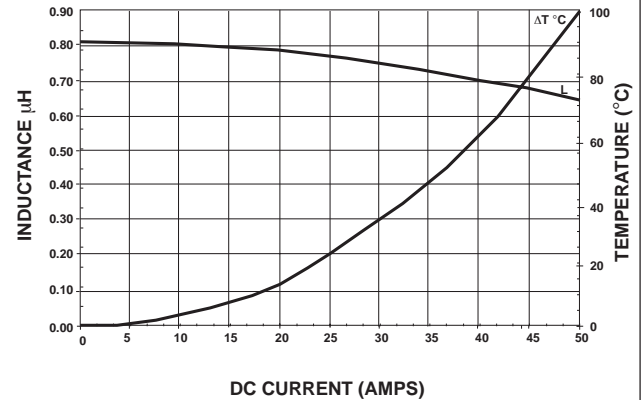
IHLP-5050FD-01 0.56 μ H



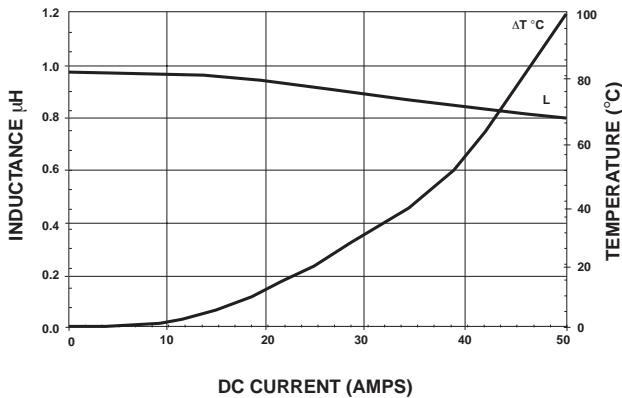
IHLP-5050FD-01 0.68 μ H



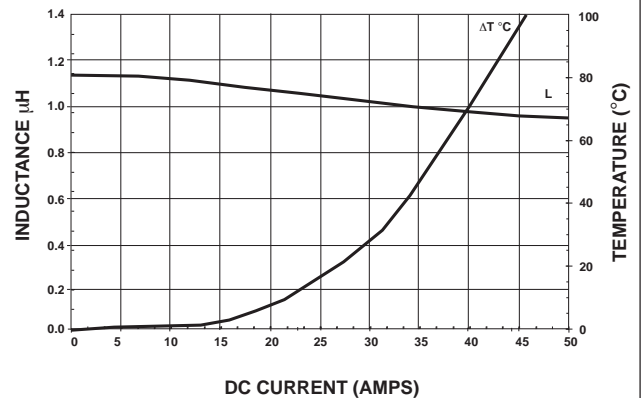
IHLP-5050FD-01 0.82 μ H



IHLP-5050FD-01 1.0 μ H

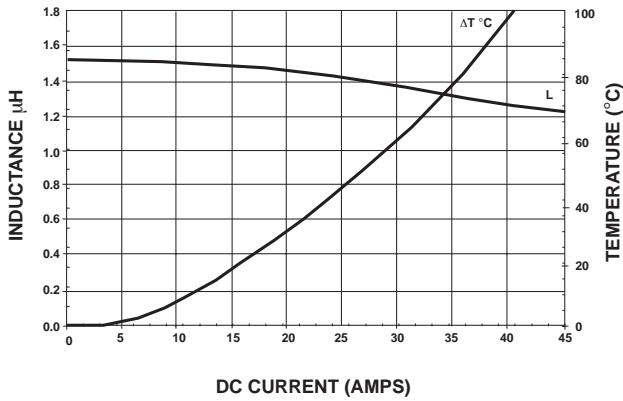


IHLP-5050FD-01 1.2 μ H

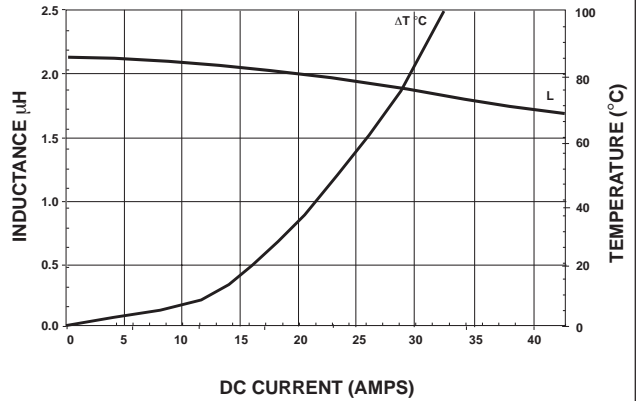


PERFORMANCE GRAPHS

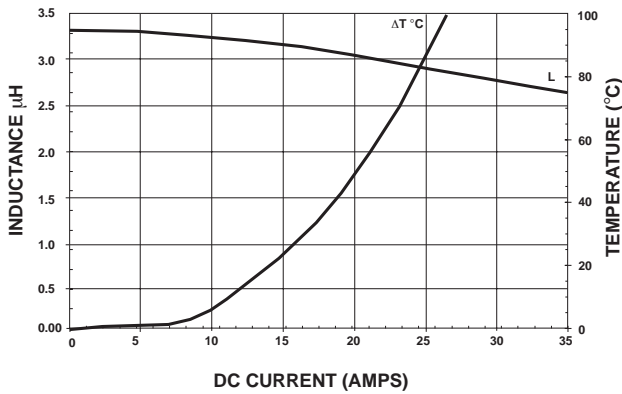
IHLP-5050FD-01 1.5 μ H



IHLP-5050FD-01 2.2 μ H



IHLP-5050FD-01 3.3 μ H



IHLP-5050FD-01 4.7 μ H

