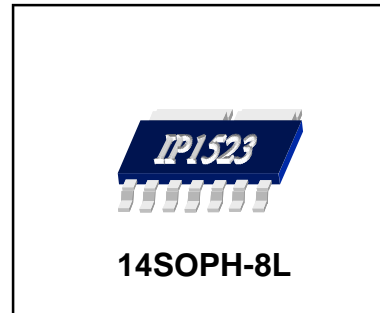


**DESCRIPTION**

The IP1523 is a dual output voltage series regulator with output on/off control function. The nominal output voltages are 3.3V and 2.0V, respectively. The 3.3V regulator can provide the output current up to 550mA. While the 2.0V regulator can provide up to 500mA. When the IP1523 becomes off, all the output voltages are shut down.



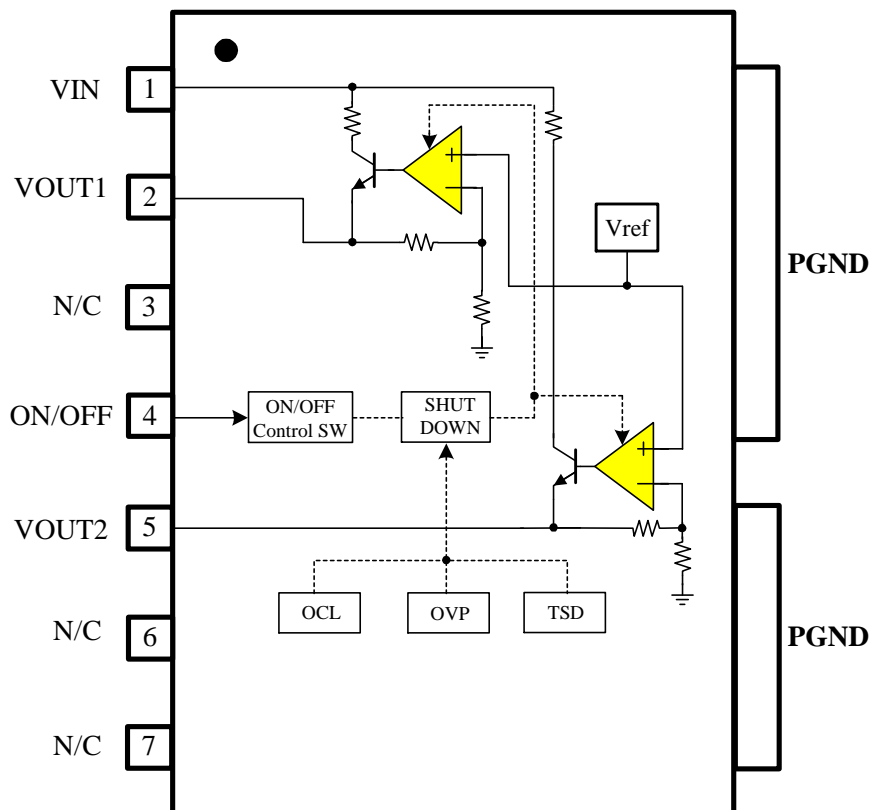
**FEATURES**

- 1-Fixed 3.3V Regulator with Internal NPN TR.
- 1-Fixed 2.0V Regulator with Internal NPN TR.
- Built-in TSD circuit.
- Built-in Current Limit Circuit.
- Built-in ON/OFF Control Circuit.
- Built-in Over Voltage Protection Circuit.
- Outputs Trimmed to +/-3% Tolerance

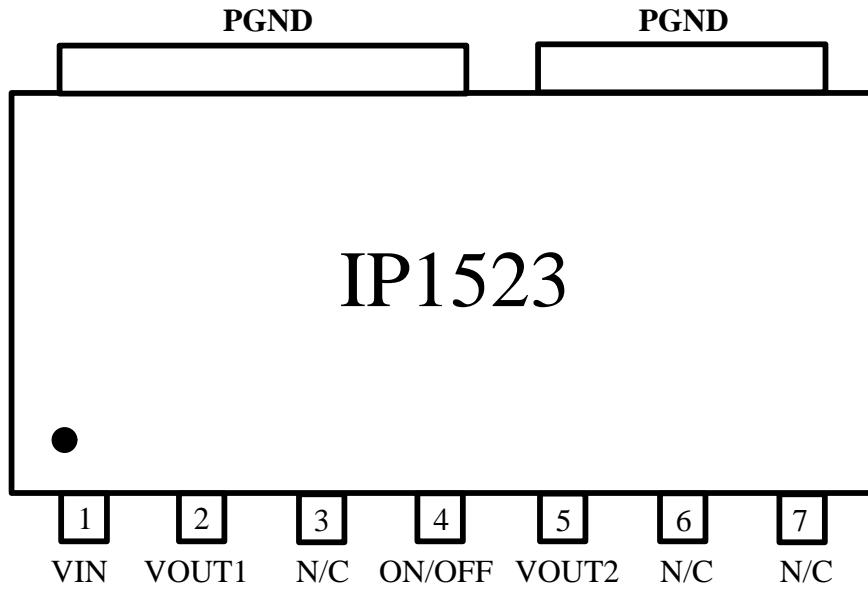
**ORDERING INFORMATION**

Device	Package	Operating Temp
IP1523	14SOPH-8L	-35°C ~ +85°C

**BLOCK DIAGRAM**



**PIN CONNECTIONS**



**PIN DESCRIPTIONS**

NO	SYMBOL	I/O	DESCRIPTION
1	VIN	I	Input Supply Voltage
2	VOUT1	O	Reg +3.3V Output
3	N/C	-	No Connection
4	ON/OFF	I	ON/OFF Control
5	VOUT2	O	Reg +2.0V Output
6	N/C	-	No Connection
7	N/C	-	No Connection
8	PGND	-	Power Ground

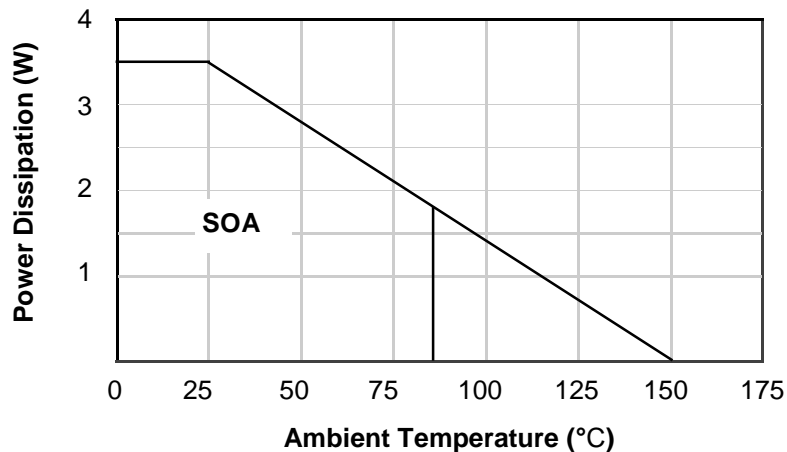
**ABSOLUTE MAXIMUM RATINGS(TA=25°C)**

CHARACTERISTICS	SYMBOL	VALUE	UNIT
Maximum supply voltage	Vinmax	10	V
ON/OFF voltage	Vonoff	10	V
Power dissipation	Pd	3.5*	W
Operating temperature	Topr	-35 ~ +85	°C
Storage temperature	Tstr	-55 ~ +150	°C
Maximum output current	Iomax	700	mA

Note>

1. When mounted on 100mm X 100mm X 1mm PCB (Phenolic resin material).
2. Power dissipation reduces 28mW/°C for using above Ta=25°C
3. Do not exceed Pd and SOA.

**POWER DISSIPATION CURVE**



**RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTICS	SYMBOL	VALUE	UNIT
Supply voltage	Vcc	4.5 ~ 6.0	V
Input voltage	Vin	4.5 ~ 6.0	V

**ELECTRICAL CHARACTERISTICS**

( Vin= 5.25V, Co=10uF, Ta = 25°C, unless otherwise specified.)

Characteristics	Symbol	Condition	Min.	Typ.	Max.	Units
Quiescent current	Icc	Vcc=5.25V, No-Load	-	7	12	mA
OVP Detecting Voltage	Vovp	Vcc=Variable	6.5	-	-	V
<b>3.3V REGULATOR PART</b>						
Output Voltage	Vout1	Io=10mA	3.2	3.3	3.4	V
Line Regulation	dVline1	Vin=4.5~6V, Io=10mA	-	0.035	0.2	%
Load Regulation	dVload 1	Vin=5.25V, 10mA < Io < 500mA	-	0.4	0.8	%
Dropout voltage	Vdrop1	Io=500mA	-	1.1	1.3	V
Ripple rejection	RR1	F=120Hz, Co=10uF Tantalum (Vin-Vout)=3V, Io=200mA	60	70	-	dB
Current limit <sup>*Note</sup>	Ilimit1	Vin-Vout=2.5V	550	-	-	mA
Temperature stability	Temp1	-	-	0.5	-	%
Output noise voltage 1	Vn1	Io=300mA	-	100	-	uV
<b>2.0V REGULATOR PART</b>						
Output Voltage	Vout2	Io=10mA	1.94	2.0	2.06	V
Line Regulation	dVline2	Vin=4.5~6V, Io=10mA	-	0.035	0.2	%
Load Regulation	dVload2	Vin=5.25V, 10mA < Io < 300mA	-	0.4	0.8	%
Dropout voltage	Vdrop2	Io=300mA	-	1.7	2.1	V
Ripple rejection	RR2	F=120Hz, Co=10uF Tantalum (Vin-Vout)=3V, Io=100mA	60	70	-	dB
Current limit <sup>*Note</sup>	Ilimit2	Vin-Vout=3.0V	500	-	-	mA
Temperature stability	Temp2	-	-	0.5	-	%
Output noise voltage 2	Vn2	Io=150mA	-	100	-	uV

**ELECTRICAL CHARACTERISTICS (Continued)**

( Vin= 5.25V, Co=10uF, Ta = 25°C, unless otherwise specified.)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Units
<b>ON/OFF CONTROL PART</b>						
On voltage	Von	Vout=Enabled	-	-	0.8	V
Off voltage	Voff	Vout=Disabled	2.0	-	-	V

\* Note

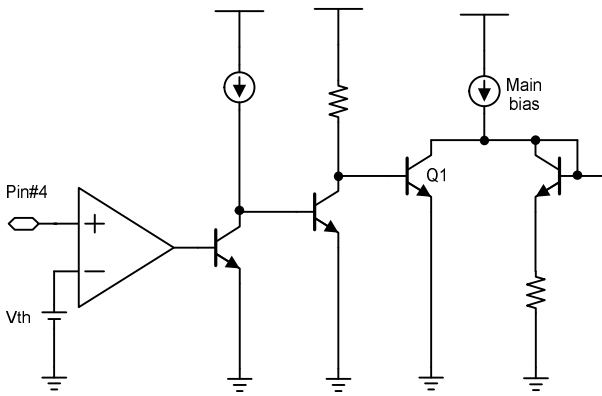
Don't exceed following the current limit.

( Iout1 : 550 mA, Iout2 : 500 mA )

**APPLICATION SUMMARY**

**- ON / OFF Control Function**

If you want to control the output of the IP1523, use pin #4 as follows:

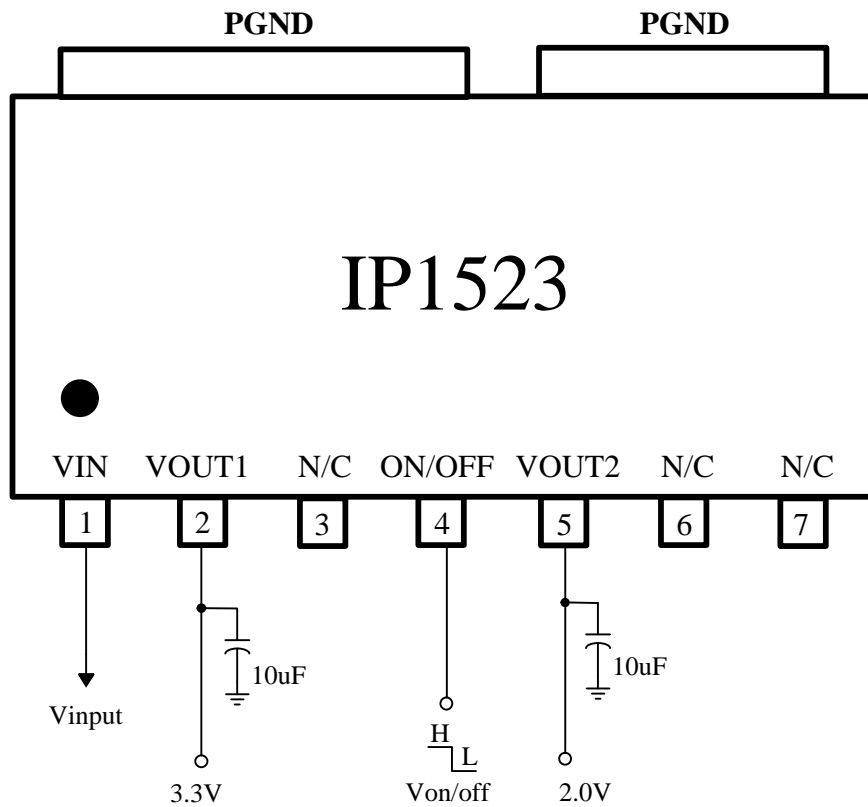


ON/OFF control function

Pin#4	IP1523
HIGH	off
LOW	on

If the voltage of pin#4 becomes larger than the threshold voltage  $V_{th}$ , the main bias current and hence, the output voltages will be shut down.

TYPICAL APPLICATION CIRCUIT

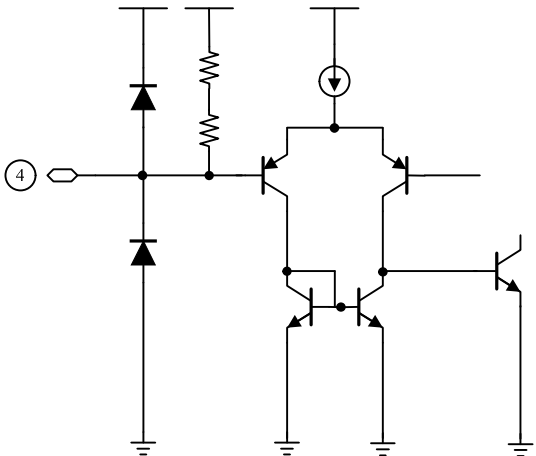


**INTERNAL CIRCUIT**

Pin no	Pin name	Internal circuit
2	Regulator output1	<p>The diagram shows the internal circuit for Regulator output1 (Pin 2). It features a differential pair of transistors. The output of one transistor is connected to a resistor network that includes a resistor connected to ground and another resistor connected to the output pin (2). A diode is also connected to the output pin (2) and ground.</p>
5	Regulator Output2	<p>The diagram shows the internal circuit for Regulator Output2 (Pin 5). It features a differential pair of transistors. The output of one transistor is connected to a resistor network that includes a resistor connected to ground and another resistor connected to the output pin (5). A diode is also connected to the output pin (5) and ground.</p>

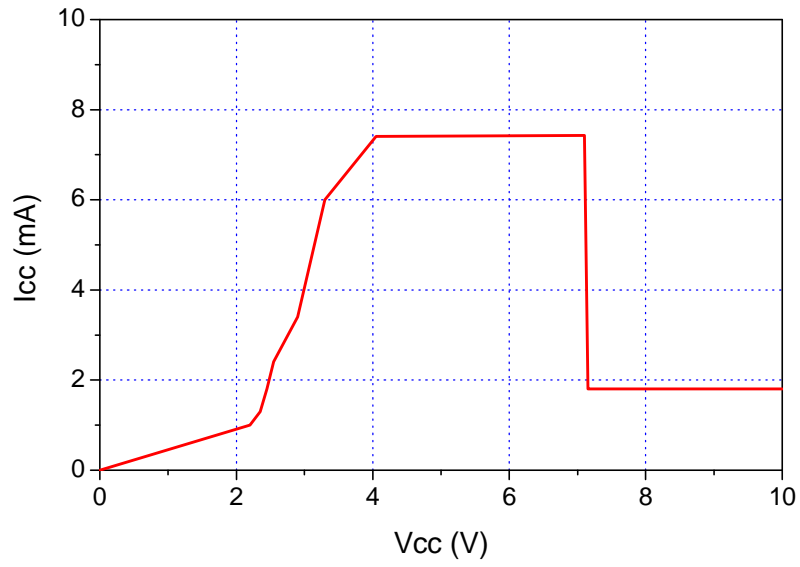


**INTERNAL CIRCUIT (Continued)**

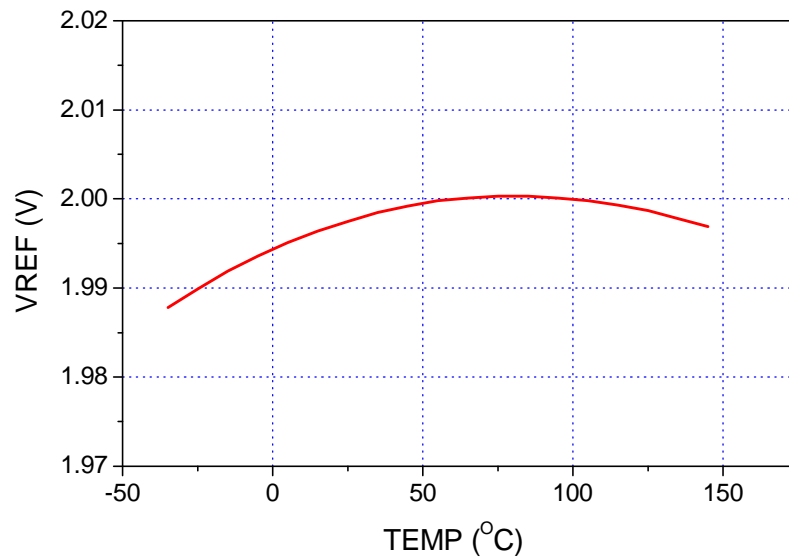
Pin no	Pin name	Internal circuit
4	ON / OFF	

ELECTRICAL CHARACTERISTICS CURVES

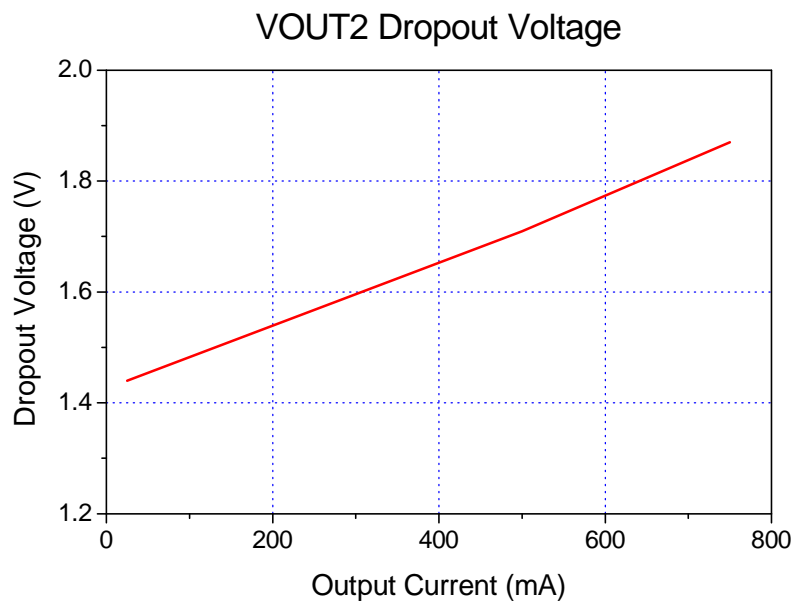
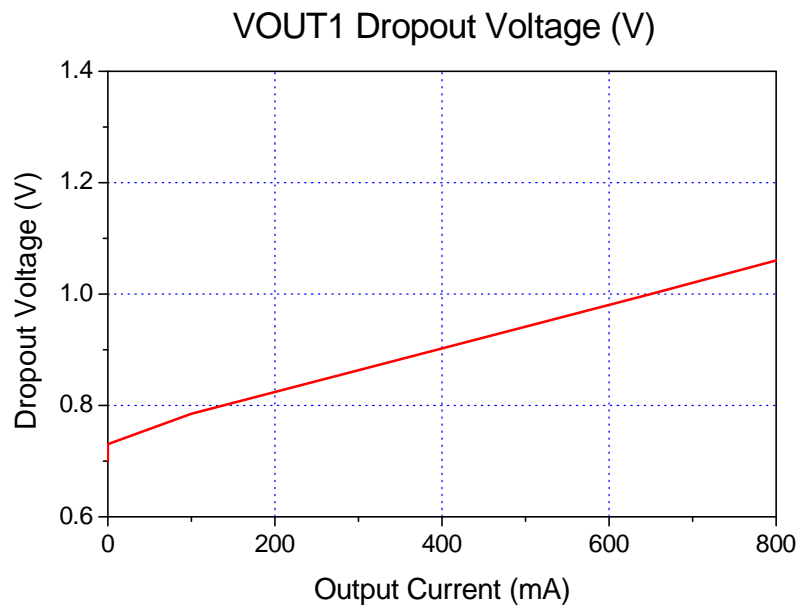
QUIESCENT CURRENT



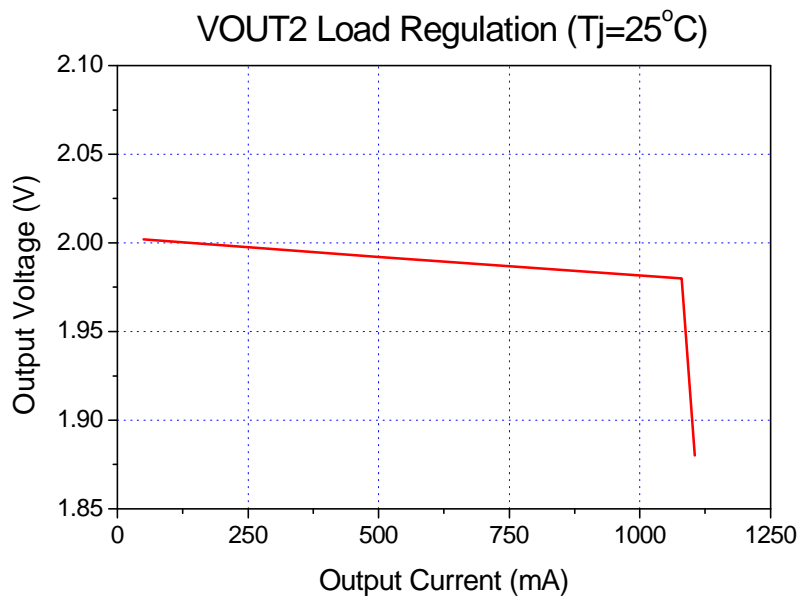
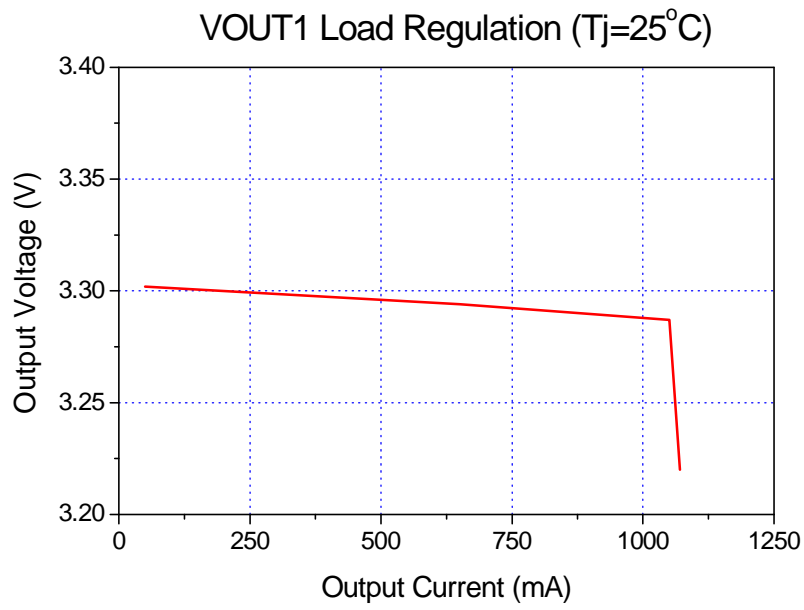
VREF TEMPERATURE STABILITY



ELECTRICAL CHARACTERISTICS CURVES (Continued)

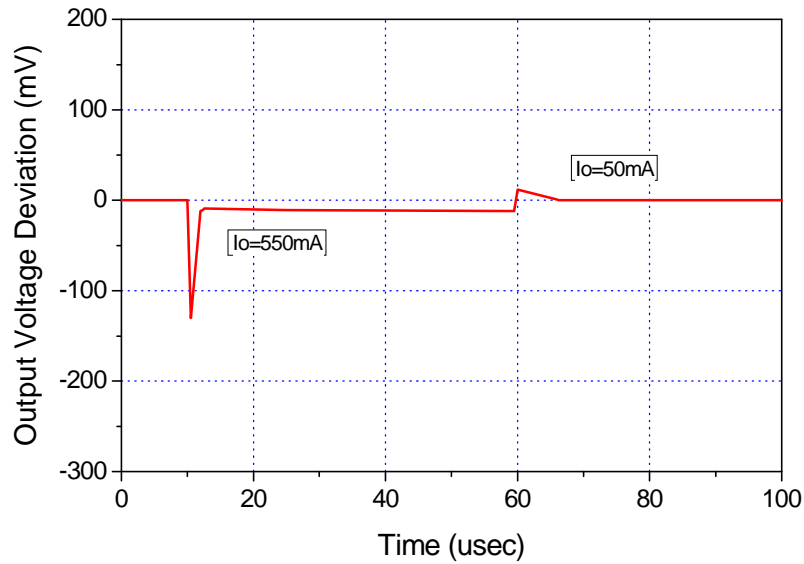


ELECTRICAL CHARACTERISTICS CURVES (Continued)

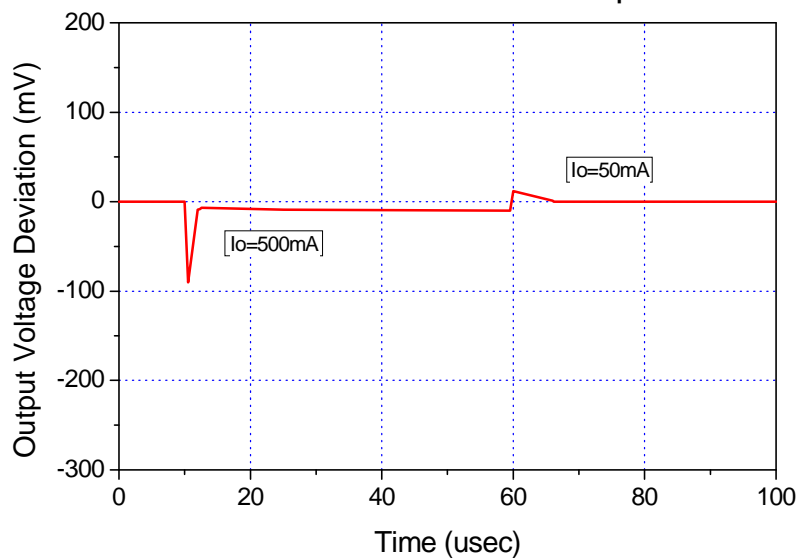


ELECTRICAL CHARACTERISTICS CURVES (Continued)

VOUT1 Load Transient Response



VOUT2 Load Transient Response



**PACKAGE DIMENSION**

**14SOPH-8L**

