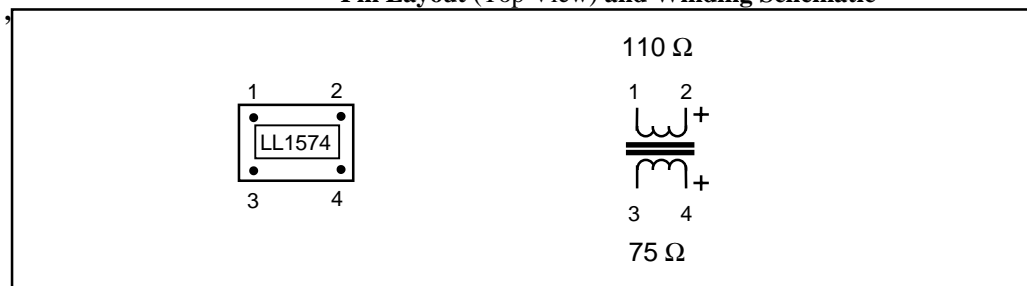


AES - DATS conversion transformer LL1574

LL1574 is a pulse transformer designed for impedance matching between 110 Ω and 75 Ω systems. The transformer has a large amorphous metal core which results in low copper resistance, high signal tolerance and low internal capacitance.

| | |
|---|-------------|
| Turns ratio: | 1 : 1.2 |
| Impedance ratio | 75 : 110 |
| Dims: (Length x Width x Height above PCB (mm)) | 15 x 9 x 11 |

Pin Layout (Top View) and Winding Schematic



| | |
|---|-------------------------|
| Spacing between pins: | 5.08 mm (0.2") |
| Spacing between rows of pins: | 10.16 mm (0.4") |
| Rec. PCB hole diameter: | 1.5 mm |
| Weight | 2 grams |
| Core | Amorphous core material |
| Static resistance of primary (Pins 1 - 2): | 1.0 Ω |
| Static resistance of secondary(Pins 3 - 4): | 1.1 Ω |
| Maximum primary signal • time before saturation: | 160 μVs at 8 volts p-p. |
| Maximum no load current at above conditions: | $\hat{I} = 3$ mA |
| Primary main inductance (tuned at 10 kHz, 2 V): | 40mH |
| Primary leakage inductance: | 1.3 μH |
| Total coupling capacitance: | < 15 pF |
| Winding capacitance: | < 1 pF |
| Isolation between windings: | 2 kV |
| Source impedance: | 0 -- 500 Ω |
| Optimum load impedance: | 200 Ω |

Application example:

Interface between 110 ohms AES/EBU and 75 ohms DATS/AES3id networks

