M4001 & M4002 Series

9x14 mm, 5.0 or 3.3 Volt, Sinewave, VCSO

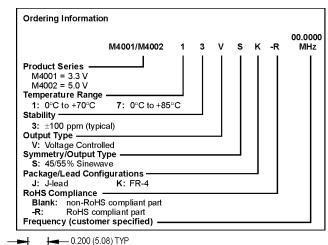




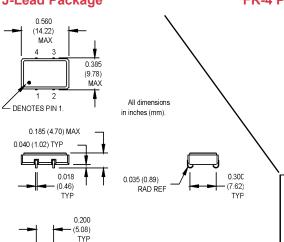




- Low Jitter <1 ps from 12 kHz to 20 MHz
- Ideal for clock smoothing application for OC-48 and OC-192



J-Lead Package

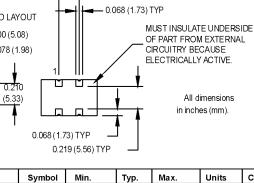


FR-4 Package SUGGESTED SOLDER PAD LAYOUT 0.200 (5.08) 0.078 (1.98) Ŧ

+

0.120 (3.05)

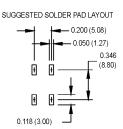
PARAMETER



0.570 (14.48)MAX 4 3 0.365 (9.27) MAX 0.260 (6.60) MAX

Condition/Notes

TYF



Electrical Specifications	Frequency Range	F	622.08		2488	MHz	See Note 1
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+125	°C	
	Frequency Stability	∆F/F	(See Ordering Information)				
	Aging						
	1st Year						
	Thereafter (per year)						
	Pullability/APR		50			ppm	See Note 2
	Control Voltage	Vc	0		3.3	V	M4001
			0		5.0	V	M4002
	Linearity				15	%	Positive Monotonic Slope
	Modulation Bandwidth	fm	500			kHz	-3dB Bandwidth
	Input Impedance	Zin	50k			Ohms	
	Input Voltage	Vdd	3.135	3.3	3.465	V	M4001
			4.5	5.0	5.5	٧	M4002
	Input Current	ldd		50	80	mA	M4001
				70	100	mA	M4002
	Output Type						Sinewave
	Load			50		Ohms	See Note 3
	Symmetry (Duty Cycle)		45/55		55/45	%	@ 0 VDC
	Output Power	Po	+2	+5	+8	dBm	M4001
			+4	+7	+10	dBm	M4002
	Start up Time						
	Sub-Harmonic Levels				-20	dBc	
	Non-Harmonic Levels				-60	dBc	
1	Phase Jitter	фЈ					

Pin Connections

PIN	FUNCTION			
1	Control Voltage			
2	Gro und			
3	RF Out			
4	+Vdd			

1. Consult factory for extended temperature operation and exact frequency availability.

100 Hz

10 Hz

APR specification inclusive of initial calibration, deviation over temperature, shock, vibration, supply voltage, and aging
See load circuit diagram #3.

0.10

1 kHz

0.3

10 kHz

ps RMS

100 kHz

-152

@ 1244.16 MHz

@ 1244.16 MHz

Phase Noise (Typical)

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.

Integrated 12 kHz - 20 MHz

Offset from carrier

dBc/Hz



MtronPTI Lead Free Solder Profile

