MMBD717LT1

Preferred Device

Common Anode Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.28 V (Typ) @ $I_F = 1$ mAdc
- Pb–Free Package is Available

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	20	V
Forward Power Dissipation @ $T_A = 25^{\circ}C$ Derate above 25°C	P _F	200 1.6	mW mW/°C
Operating Junction Temperature Range	Τ _J	-55 to +150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS ($T_A = 125^{\circ}C$ unless otherwise noted)

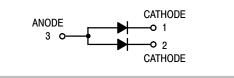
Rating	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage ($I_R = 10 \ \mu A$)	V _{(BR)R}	20	-	-	V
Total Capacitance $(V_R = 1.0 \text{ V}, \text{ f} = 1.0 \text{ MHz})$	CT	-	2.0	2.5	P _F
Reverse Leakage ($V_R = 10 V$) (For each individual diode while the second diode is unbiased)	I _R	-	0.05	1.0	μAdc
Forward Voltage (I _F = 1.0 mAdc)	V _F	-	0.28	0.37	Vdc



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20 VOLT SCHOTTKY BARRIER DETECTOR AND SWITCHING DIODES







MARKING DIAGRAM



B3 = Device Code

M = Date Code*

= Pb–Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or overbar may vary depending upon manufacturing location.

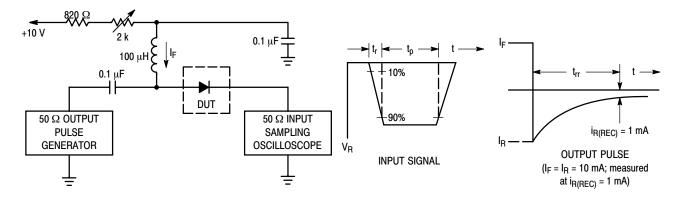
ORDERING INFORMATION

Device	Package	Shipping [†]
MMBD717LT1	SC-70	3,000 / Tape & Reel
MMBD717LT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

MMBD717LT1



NOTES: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA. 3. t_p » t_{rr}



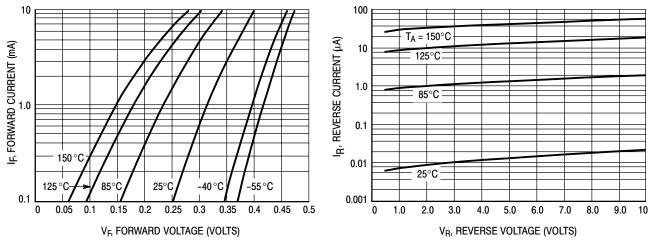
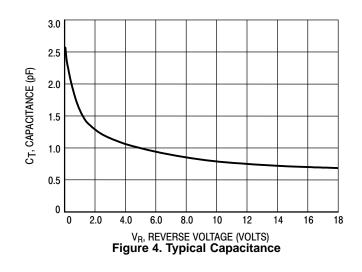


Figure 2. Typical Forward Voltage

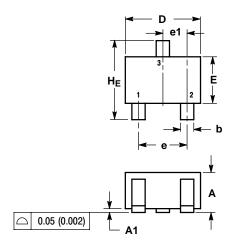
Figure 3. Reverse Current versus Reverse Voltage

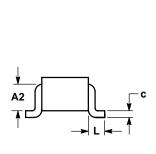


MMBD717LT1

PACKAGE DIMENSIONS

SC-70 / SOT-323 CASE 419-04 ISSUE M





NOTES 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

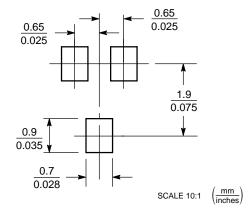
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.7 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
Е	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.425 REF 0.017 REF					
HE	2.00	2.10	2.40	0.079	0.083	0.095

2. CATHODE 3. ANODE

PIN 1. CATHODE

STYLE 4:

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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