

NJ42 Process

Silicon Junction Field-Effect Transistor

- General Purpose Amplifier
- High Breakdown Voltage

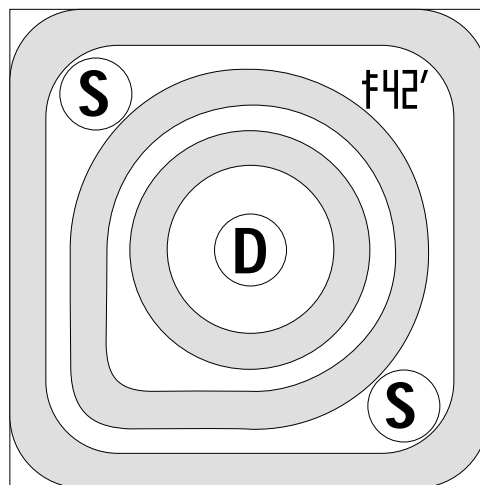
Absolute maximum ratings at TA = 25 °C

Gate Current, I _g	10 mA
Operating Junction Temperature, T _j	+150°C
Storage Temperature, T _s	- 65°C to +175°C

Devices in this Databook based on the NJ42 Process.

Datasheet

2N6449, 2N6450
IFN6449, IFN6450



Die Size = 0.032" X 0.032"
All Bond Pads = 0.004", Dia.
Substrate is also Gate.

At 25°C free air temperature:

Static Electrical Characteristics

		NJ42 Process						
		Min	Typ	Max	Unit	Test Conditions		
Gate Source Breakdown Voltage	V _{(BR)GSS}	- 300	- 400		V	I _G = 1 μA, V _{DS} = 0V		
Reverse Gate Leakage Current	I _{GSS}		- 1	- 10	nA	V _{GS} = - 150V, V _{DS} = 0V		
Drain Saturation Current (Pulsed)	I _{DSS}	2		10	mA	V _{DS} = 30V, V _{GS} = 0V		
Gate Source Cutoff Voltage	V _{GS(OFF)}	- 2		- 12	V	V _{DS} = 30V, I _D = 1 nA		

Dynamic Electrical Characteristics

Forward Transconductance	g _{fs}		800		μS	V _{DS} = 30V, V _{GS} = 0V	f = 1 kHz
Input Capacitance	C _{iss}		6	10	pF	V _{DS} = 30V, V _{GS} = 0V	f = 1 MHz
Feedback Capacitance	C _{rss}		2	5	pF	V _{DS} = 30V, V _{GS} = 0V	f = 1 MHz
Equivalent Noise Voltage	e _N		10		nV/√HZ	V _{DS} = 15V, V _{GS} = 0V	f = 1 kHz



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