

MOS FIELD EFFECT TRANSISTOR NP50P04KDG

SWITCHING P-CHANNEL POWER MOSFET

DESCRIPTION

The NP50P04KDG is P-channel MOS Field Effect Transistor designed for high current switching applications.

<R> ORDERING INFORMATION

PART NUMBER	LEAD PLATING	PACKING	PACKAGE	
NP50P04KDG-E1-AY Note		Tara 000 a/aal	TO-263 (MP-25ZK)	
NP50P04KDG-E2-AY Note	Pure Sn (Tin)	Tape 800 p/reel		

Note Pb-free (This product does not contain Pb in external electrode.)

FEATURES

• Super low on-state resistance

 $R_{DS(on)1} = 10 \text{ m}\Omega \text{ MAX}. \text{ (V}_{GS} = -10 \text{ V}, I_{D} = -25 \text{ A})$

 $R_{DS(on)2} = 15 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.5 \text{ V, Ip} = -25 \text{ A)}$

Low input capacitance

Ciss = 5100 pF TYP.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

	- (- ,		
Drain to Source Voltage (V _{GS} = 0 V)	VDSS	-40	V
Gate to Source Voltage (VDS = 0 V)	Vgss	∓20	V
Drain Current (DC) (Tc = 25°C)	I _{D(DC)} ∓50		Α
Drain Current (pulse) Note1	D(pulse)	∓150	Α
Total Power Dissipation (Tc = 25°C)	P _{T1}	90	W
Total Power Dissipation (T _A = 25°C)	P _{T2}	1.8	W
Channel Temperature	Tch	175	°C
Storage Temperature	T _{stg}	-55 to +175	°C
Single Avalanche Current Note2	Ias	37	Α
Single Avalanche Energy Note2	Eas	136	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. Starting T_{ch} = 25°C, V_{DD} = -30 V, R_G = 25 Ω , V_{GS} = $-20 \rightarrow 0$ V

THERMAL RESISTANCE

Channel to Case Thermal Resistance Rth(ch-C) 1.67 °C/W
Channel to Ambient Thermal Resistance Rth(ch-A) 83.3 °C/W

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.

(TO-263)



Document No. D18688EJ3V0DS00 (3rd edition) Date Published May 2007 NS CP(K) Printed in Japan



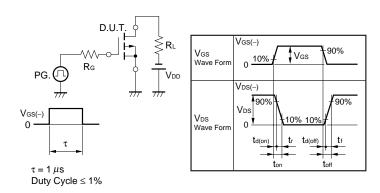
ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = -40 V, V _{GS} = 0 V			-10	μΑ
Gate Leakage Current	Igss	V _{GS} = ∓20 V, V _{DS} = 0 V			∓100	nA
Gate to Source Threshold Voltage	V _{GS(th)}	V _{DS} = -10 V, I _D = -1 mA	-1.0	-1.6	-2.5	V
Forward Transfer Admittance Note	y fs	V _{DS} = -10 V, I _D = -25 A	15	30		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = -10 V, I _D = -25 A		7.9	10	mΩ
	RDS(on)2	V _{GS} = -4.5 V, I _D = -25 A		9.8	15	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V,		5100		pF
Output Capacitance	Coss	V _{GS} = 0 V,		790		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		440		pF
Turn-on Delay Time	t _{d(on)}	$V_{DD} = -20 \text{ V}, I_D = -25 \text{ A},$		20		ns
Rise Time	tr	V _{GS} = -10 V,		45		ns
Turn-off Delay Time	t _{d(off)}	R _G = 0 Ω		405		ns
Fall Time	tr			230		ns
Total Gate Charge	Q _G	V _{DD} = -32 V,		100		nC
Gate to Source Charge	Qgs	V _{GS} = -10 V,		13		nC
Gate to Drain Charge	Q _{GD}	I _D = -50 A		42		nC
Body Diode Forward Voltage Note	V _{F(S-D)}	I _F = -50 A, V _{GS} = 0 V		0.95	1.5	V
Reverse Recovery Time	trr	I _F = -50 A, V _{GS} = 0 V,		48		ns
Reverse Recovery Charge	Qrr	di/dt = –100 A/μs		66		nC

Note Pulsed test PW \leq 350 μ s, Duty Cycle \leq 2%

TEST CIRCUIT 1 AVALANCHE CAPABILITY

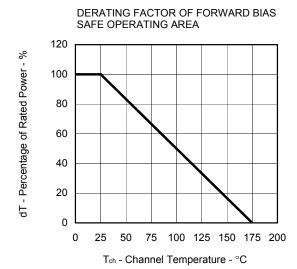
TEST CIRCUIT 2 SWITCHING TIME

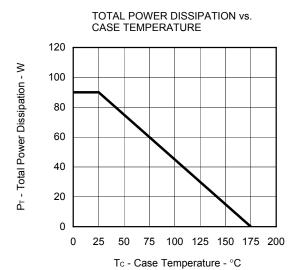


TEST CIRCUIT 3 GATE CHARGE

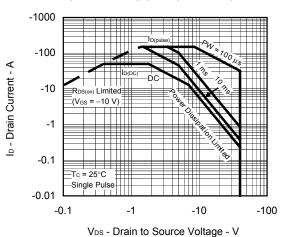
$$\begin{array}{c|c} D.U.T. & \\ \hline \\ IG = -2 \text{ mA} \\ \hline \\ PG. & \\ \hline \\ \end{array} \begin{array}{c} RL \\ \hline \\ VDD \\ \hline \end{array}$$

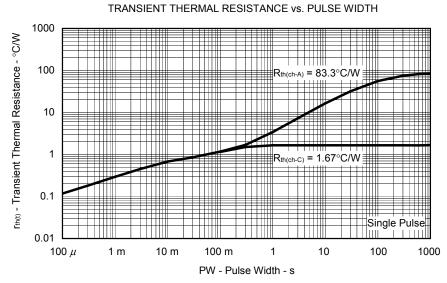
TYPICAL CHARACTERISTICS (TA = 25°C)





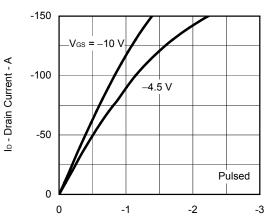
FORWARD BIAS SAFE OPERATING AREA





3

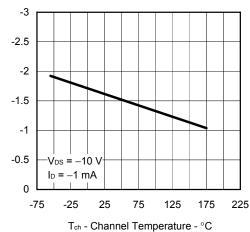




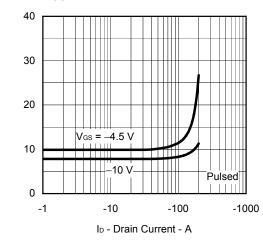
TO COURSE TURESUOUR VOLTAGE



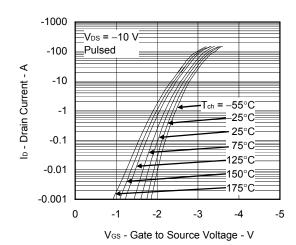
VDS - Drain to Source Voltage - V



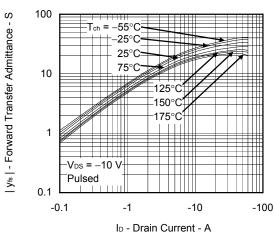
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



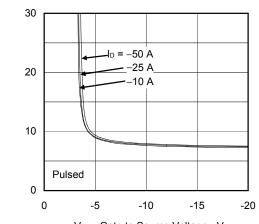
FORWARD TRANSFER CHARACTERISTICS



FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

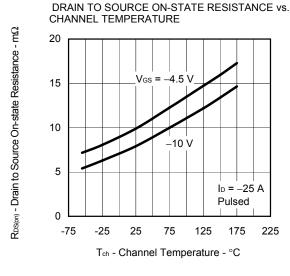


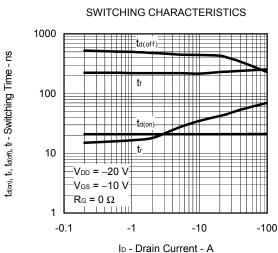
V_{GS} - Gate to Source Voltage - V

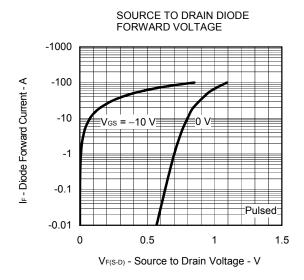
R_{DS(m)} - Drain to Source On-state Resistance - mΩ

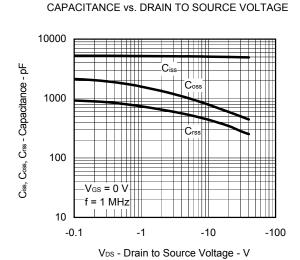
 $\mathsf{V}_{\mathsf{GS}(\mathtt{fh})}$ - Gate to Source Threshold Voltage - V

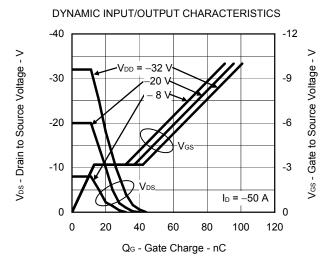
R_{DS(m)} - Drain to Source On-state Resistance - mΩ

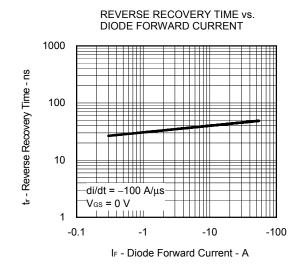






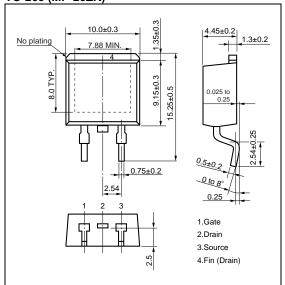




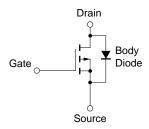


PACKAGE DRAWING (Unit: mm)

TO-263 (MP-25ZK)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

6

NP50P04KDG

- The information in this document is current as of May, 2007. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets or data books, etc., for the most up-to-date specifications of NEC Electronics products. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without the prior
 written consent of NEC Electronics. NEC Electronics assumes no responsibility for any errors that may
 appear in this document.
- NEC Electronics does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from the use of NEC Electronics products listed in this document or any other liability arising from the use of such products. No license, express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC Electronics or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative purposes in semiconductor product operation and application examples. The incorporation of these circuits, software and information in the design of a customer's equipment shall be done under the full responsibility of the customer. NEC Electronics assumes no responsibility for any losses incurred by customers or third parties arising from the use of these circuits, software and information.
- While NEC Electronics endeavors to enhance the quality, reliability and safety of NEC Electronics products, customers agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize risks of damage to property or injury (including death) to persons arising from defects in NEC Electronics products, customers must incorporate sufficient safety measures in their design, such as redundancy, fire-containment and anti-failure features.
- NEC Electronics products are classified into the following three quality grades: "Standard", "Special" and "Specific".
 - The "Specific" quality grade applies only to NEC Electronics products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of an NEC Electronics product depend on its quality grade, as indicated below. Customers must check the quality grade of each NEC Electronics product before using it in a particular application.
 - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots.
 - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).
 - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

(Note)

- (1) "NEC Electronics" as used in this statement means NEC Electronics Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC Electronics products" means any product developed or manufactured by or for NEC Electronics (as defined above).