Preliminary



- Quartz SAW Stabilized and Filtered "Diff Sine" Technology
- Fundamental-Mode Oscillation at 780.881 MHz
- Voltage Tunable for Phase Lock Loop Operations

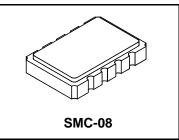
The output of this device is generated and filtered by narrowband quartz SAW elements at 780.881 MHz. The configuration of this clock is intended to provide a pure signal for optical timing applications in noisy signal environments. The Q/Qbar differential output swing of ± 1 volt about 0 vdc has symmetry better than $\pm 1\%$ into loads from 40 ohms to 70 ohms; determined by customer application. The long term frequency accuracy is set by an external reference source allowing this device to complete a Phase Lock Loop design without the usual noise and jitter problems associated with PLL's.

Absolute Maximum Ratings

Rating	Value	Units
DC Suppy Voltage	0 to 5.5	VDC
Tune Voltage	0 to 6	VDC
Case Temperature	-55 to 100	°C

780.881 MHz Optical Timing Clock

OP4015B



Electrical Characteristics

Characteristic		Sym	Notes	Minimum	Typical	Maximum	Units
Operating Frequency	Frequency Absolute Frequency	f _O	1, 9		780.881		MHz
	Tune Range		2	±25			ppm
	Tune Voltage		1	0		+3	VDC
	Tuning Linearity		1, 8		±3%		
	Tuning Sensitivity	df/dv	2, 10	140		300	ppm/V
	Modulation Bandwidth			125	265		kHz
Q and Q Output	Voltage into 50 Ω (VSWR≤1.2)	Vo	1,3	0.60		1.1	V _{P-P}
	Operating Load VSWR		1,3			2:1	
	Symmetry		3, 4, 5	49		51	%
	Harmonic Spurious		3, 4, 6			-30	dBc
	Nonharmonic Spurious		3, 4, 6, 7			-60	dBc
Phase Noise	dBc/Hz@100Hz offset				-70		dBc/Hz
	1kHz offset				-100		dBc/Hz
	10k offset				-120		
	Noise Floor				-150		
Q and Q Jitter	RMS Jitter (12 kHz to 80 MHz)		3, 4, 6, 7		.20		ps
	No Noise on V _{CC}		3, 4, 6, 7		10		PS _{P-P}
	200 mV _{P-P} from 1MHz to ½ f _O on		3		12		PS _{P-P}
Input Impedence (Tunii	ng Port)			1			KΩ
Output DC Resistance (between Q & \overline{Q})			1, 3	50			KΩ
DC Power Supply	Operating Voltage	V _{CC}	1, 3	3.13	3.3, 5.0	5.25	VDC
	Operating Current	I _{CC}	1, 3			70	mA
Operating Case Temperature		T _C	1, 3	-40°C		+85°C	°C
Lid Symbolization (YY=Year, WW=Week)		-		RFM OP4015	B YYWW		

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CAUTION: Electrostatic Sensitive Device. Observe precautions for handling. COCOM CAUTION: Approval by the U.S. Department of Commerce is required prior to export of this device.

Notes:

- Unless otherwise noted, all specifications include any combination of load VSWR, Vcc, and temperature, with Q and Q terminated into 50 ohm loads to ground (see typical test circuit).
- 2. Useful tuning range is in excess of what is required over temp, aging, pushing, pulling & accuracy.
- The design, manufacturing process, and specifications of this device are subject to change without notice.
 Only under the nominal conditions of 50 Ω load impedance with VSWR ≤ 1.2 and nominal power supply voltage.
- Symmetry is defined as the pulse width (in percent of total period) measured at the 50% points of Q or Q (see timing definitions).

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Linearity is a function of the percentage variation from a permitted linear deviation versus the amount of frequency tune range (see linearity definition).

9. One or more of the following United States patents apply: 4,616,197; 4,670,681; 4,760,352.

E-mail: info@rfm.com http://www.rfm.com OP4015B-041003

V_{tune}

BLOCK DIAGRAM

SAW Oscillato

Vcc

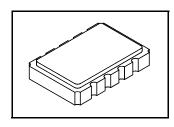
Buffer Amplifie

Q

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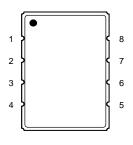
780.881 MHz Optical Timing Clock

SMC-8 8-Terminal Surface Mount Case



ELECTRICAL CONNECTIONS

Terminal Number	Connection	
1	V _{CC}	
2	Ground	
3	Enable/Disable	
4	Q Output	
5	Q Output	
6	Ground	
7	Cround	
8	TUNE Input	
LID	Ground	



TOP VIEW

Dimen- sion	mr	n	Inches		
	MIN	MAX	MIN	MAX	
A	13.46	13.97	0.530	0.550	
В	9.14	9.66	0.360	0.380	
С	1.93 Nominal		0.076 Nominal		
D	3.56 Nominal		0.141 Nominal		
E	2.24 Nominal		0.088 Nominal		
F	1.27 Nominal		0.050 Nominal		
G	2.54 Nominal		0.100 Nominal		
н	3.05 Nominal		0.120 Nominal		
J	1.93 Nominal		0.076 Nominal		
к	5.54 Nominal		0.218 Nominal		
L	4.32 Nominal		0.170 Nominal		
М	4.83 Nominal		0.190 Nominal		
N	0.50 Nominal 0.020 Nominal			Iominal	

