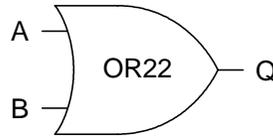


OR22 is a 2-input OR gate with 2x drive strength.

Truth Table

A	B	Q
L	L	L
L	H	H
H	L	H
H	H	H



Capacitance

	Ci (pF)
A	0.038
B	0.036

Area

0.54 mils²

Power

3.68 μW/MHz

Delay [ns] = tpd.. = f(SL, L) with SL = Input Slope [ns] ; L = Output Load [pF]
 Output Slope [ns] = op_sl.. = f(L) with L = Output Load [pF]

AC Characteristics : Tj = 25°C VDD = 3.3V Typical Process

AC Characteristics

Characteristics	Symbol	SL = 0.1			SL = 2.0		
		L = 0.2	L = 1.4	L = 2.0	L = 0.2	L = 1.4	L = 2.0
Delay A to Q	tpdar	0.51	1.87	2.53	0.70	2.01	2.70
	tpdaf	0.51	1.60	2.18	0.75	1.83	2.39
Delay B to Q	tpdbr	0.53	1.88	2.61	0.78	2.10	2.79
	tpdbf	0.54	1.62	2.18	0.68	1.77	2.32
Output Slope A to Q	op_slar	0.92	5.12	7.52	0.85	5.20	7.42
	op_slaf	0.68	3.77	5.03	0.70	3.45	5.01
Output Slope B to Q	op_slbr	0.92	5.28	7.27	0.88	5.22	7.45
	op_slbf	0.68	3.50	5.08	0.68	3.47	4.97