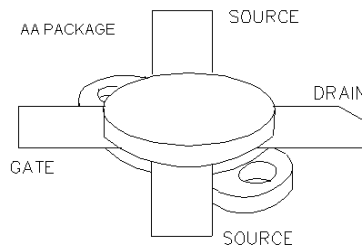




**General Description**

Silicon VDMOS and LDMOS transistors designed specifically for broadband RF applications. Suitable for Military Radios, Cellular and Paging Amplifier Base Stations, Broadcast FM/AM, MRI, Laser Driver and others.

"Polyfet"<sup>TM</sup> process features low feedback and output capacitances resulting in high  $F_t$  transistors with high input impedance and high efficiency.



**SILICON GATE ENHANCEMENT MODE**

**RF POWER VDMOS TRANSISTOR**

**35.0 Watts Single Ended**

**Package Style AA**

**HIGH EFFICIENCY, LINEAR  
HIGH GAIN, LOW NOISE**

**ABSOLUTE MAXIMUM RATINGS ( T = 25 °C )**

Total Device Dissipation	Junction to Case Thermal Resistance	Maximum Junction Temperature	Storage Temperature	DC Drain Current	Drain to Gate Voltage	Drain to Source Voltage	Gate to Source Voltage
60 Watts	2.80 °C/W	200 °C	-65 °C to 150 °C	2.5 A	125V	125V	20 V

**RF CHARACTERISTICS ( 35.0 WATTS OUTPUT )**

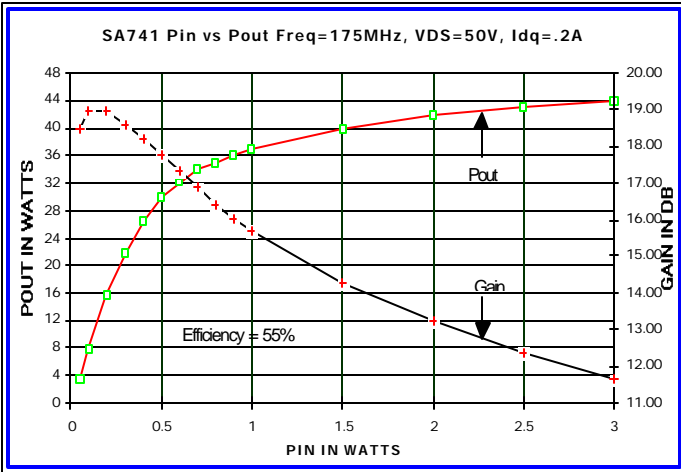
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Gps	Common Source Power Gain	16			dB	$I_{dq} = 0.20 \text{ A}$ , $V_{ds} = 50.0 \text{ V}$ , $F = 175 \text{ MHz}$
$\eta$	Drain Efficiency		55		%	$I_{dq} = 0.20 \text{ A}$ , $V_{ds} = 50.0 \text{ V}$ , $F = 175 \text{ MHz}$
VSWR	Load Mismatch Tolerance			20:1	Relative	$I_{dq} = 0.20 \text{ A}$ , $V_{ds} = 50.0 \text{ V}$ , $F = 175 \text{ MHz}$

**ELECTRICAL CHARACTERISTICS ( EACH SIDE )**

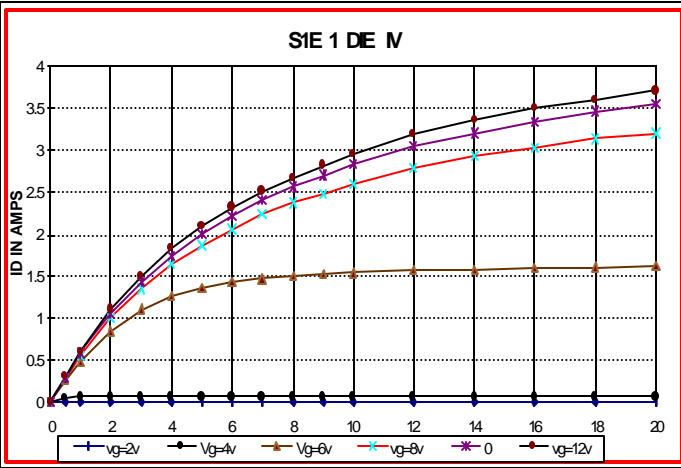
SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Bvdss	Drain Breakdown Voltage	125			V	$I_{ds} = 10.00 \text{ mA}$ , $V_{gs} = 0\text{V}$
Idss	Zero Bias Drain Current			1.0	mA	$V_{ds} = 50.0 \text{ V}$ , $V_{gs} = 0\text{V}$
Igss	Gate Leakage Current			1	uA	$V_{ds} = 0\text{V}$ $V_{gs} = 30\text{V}$
Vgs	Gate Bias for Drain Current	1		7	V	$I_{ds} = 0.05 \text{ A}$ , $V_{gs} = V_{ds}$
gM	Forward Transconductance		0.8		Mho	$V_{ds} = 10\text{V}$ , $V_{gs} = 5\text{V}$
Rdson	Saturation Resistance		2.50		Ohm	$V_{gs} = 20\text{V}$ , $I_{ds} = 1.00 \text{ A}$
I <sub>dsat</sub>	Saturation Current		3.50		Amp	$V_{gs} = 20\text{V}$ , $V_{ds} = 10\text{V}$
Ciss	Common Source Input Capacitance		48.0		pF	$V_{ds} = 50.0 \text{ V}$ $V_{gs} = 0\text{V}$ , $F = 1 \text{ MHz}$
Crss	Common Source Feedback Capacitance		0.2		pF	$V_{ds} = 50.0 \text{ V}$ $V_{gs} = 0\text{V}$ , $F = 1 \text{ MHz}$
Coss	Common Source Output Capacitance		17.0		pF	$V_{ds} = 50.0 \text{ V}$ $V_{gs} = 0\text{V}$ , $F = 1 \text{ MHz}$

# SA741

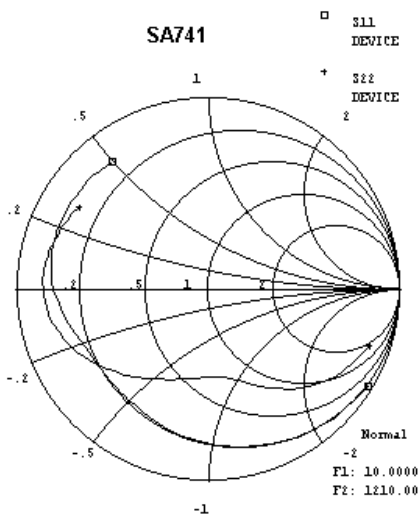
POUT VS PIN GRAPH



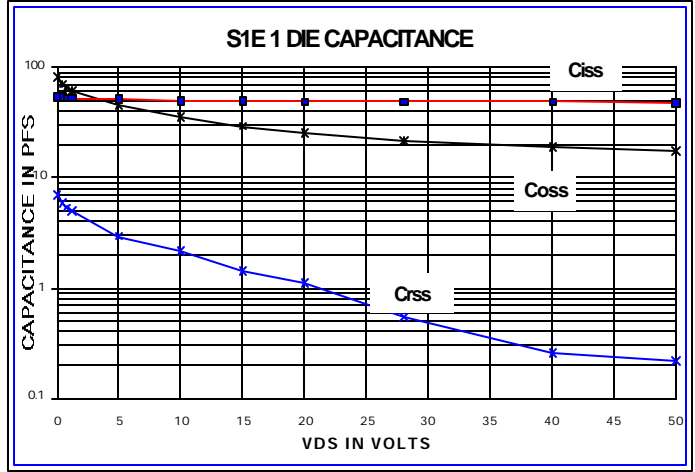
IV CURVE



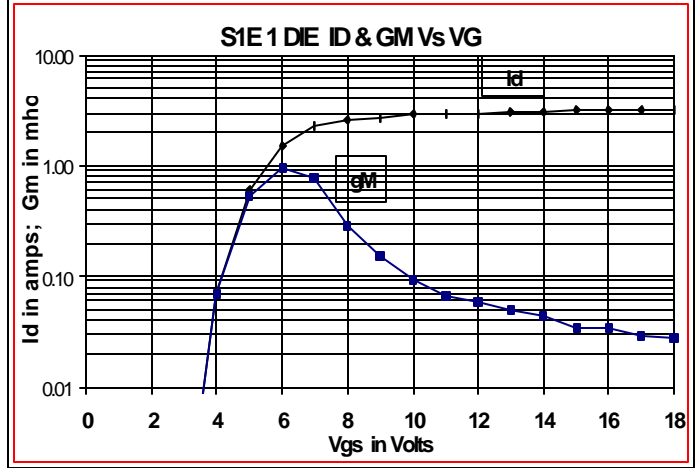
S11 & S22 SMITH CHART



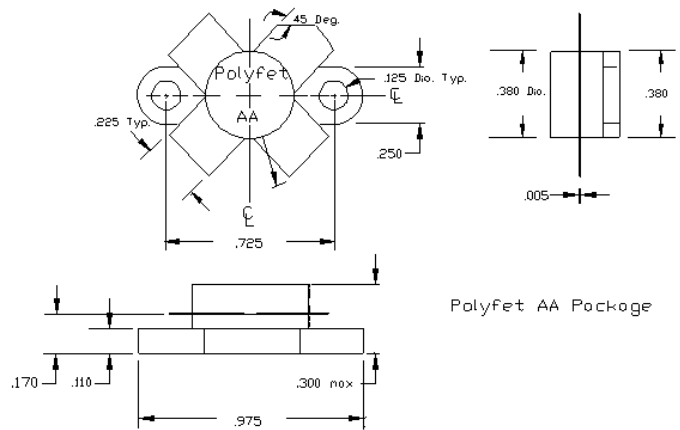
CAPACITANCE VS VOLTAGE



ID & GM VS VGS



PACKAGE DIMENSIONS IN INCHES



Tolerance .XX +/-0.01 .XXX +/- .005 inches