Silicon-Based Technology Corp.

Small-Signal Schottky Barrier Diodes

SBT107WS Series

SBT107WS series are Schottky Barrier Diodes fabricated by a series of proprietary Schottky barrier patents and technologies (SBT®) developed by Silicon-Based Technology Corporation, which exhibit high-performance characteristics for modern switching, conversion and protection applications with high speed and low power consumptions. The package types as described in this data sheet are set forth in routine production; other packages are available upon special orders.

Features and Advantages:

- Low forward voltage drop(V_F)
- Low reverse leakage current (I_R)
- Very small conduction power loss
- Very small switching power loss
- Very high switching speed
- Very high reliability

■ Electrical Characteristics: (@T_A=25°C unless otherwise specified)

Characteristic	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	30	-	-	V	I _R =100μA
Forward Voltage (Note 2)	V _{FM}	-	280 320 450 550	- - 500 600	mV	$@I_F=2.0mA$ $@I_F=15mA$ $@I_F=50mA$ $@I_F=100mA$
Reverse Current (Note 2)	I _{RM}	-	-	1.0	μA	V _R =25V
Total Capacitance	Ст	-	7	-	pF	V _R =0V, f=1.0MH _Z

Tel: 886-3-5777897 Fax: 886-3-5779832



■ Maximum Ratings: (@T_A=25℃ unless otherwise specified)

Characteristic	Symbol	SBT107WS	Unit
Peak Repetitive Reverse Voltage	V_{RRM}		
Working Peak Reverse Voltage	V_{RWM}	30	V
DC Blocking Voltage	V_R		
RMS Reverse Voltage	$V_{R(RMS)}$	21	V
Forward Continuous Current (Note 1)	I _{FM}	100	mA
Non-Repetitive Peak Forward Surge Current @t≤10µs	I _{FSM}	750	mA
Power Dissipation (Note 1)	P_d	250	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{eJA}	500	€\M
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +125	C

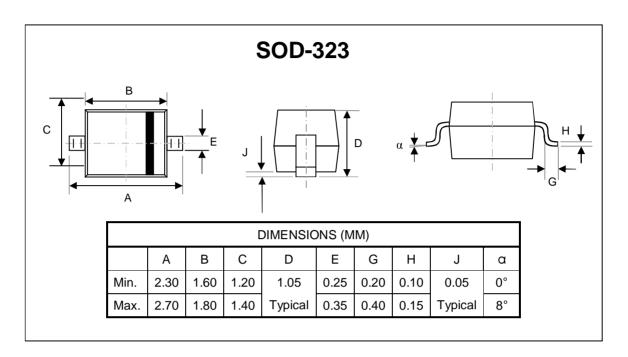
Notes: 1. Part mounted on FR4 PC Board with recommended pad layout, which can be found on our website at http://www.sbt.com.tw.

2. No purposefully added lead.

■ Package Data:

- Case: Molded Plastic Material (UL Flammability Classification 94V-0)
- Terminals: Solderable Plated Terminals (MIL-STD-202, Method 208)
- Lead Free Plating (Matte Tin Finish)
- Polarity: See device configurations below
- Approx. Weight: 0.004 grams.
- Package outline and dimensions (see below)





■ Ordering Information (Note 3)

Part Number	Marking Code	Packaging Type	Shipping	
Fait Number	Marking Code	rackaging Type	7" Tape & Real	
SBT107WS	SBTJ7S	SOD-323	3K	

Notes: 3. Website at http://www.sbt.com.tw

- 4. Bulk package in a box form is also available upon request.
- Day code marking is YM, in which Y represents year (For example: 2005 is marked by 5);
 M represents month in a year (For example: March is marked by C; November is marked by K).