

## SIGC54T60R3

## IGBT<sup>3</sup> Chip

#### **FEATURES:**

- 600V Trench & Field Stop technology
- low V<sub>CE(sat)</sub>
- low turn-off losses
- short tail current
- positive temperature coefficient
- easy paralleling

#### This chip is used for:

• power module



# Applications:drives

Chip Type	V <sub>CE</sub>	I <sub>Cn</sub>	Die Size	Package	Ordering Code
SIGC54T60R3	600V	100A	5.97 x 8.97 mm <sup>2</sup>	sawn on foil	Q67050- A4341-A101

#### **MECHANICAL PARAMETER:**

Raster size	5.97 x 8.97			
Emitter pad size	( 2.489 x 1.767 ) x 4 ( 2.789 x 1.995 ) x 4			
Gate pad size	1.615 x 0.817			
Area total / active	53.6 / 40	$mm^2$		
Thickness	70	μm		
Wafer size	150	mm		
Flat position	90	deg		
Max. possible chips per wafer	245 pcs			
Passivation frontside	Photoimide			
Emitter metallization	3200 nm AlSiCu			
Collector metallization	1400 nm Ni Ag -system suitable for epoxy and soft solder die bonding			
Die bond	electrically conductive glue or solder			
Wire bond	Al, <500μm			
Reject ink dot size	Ø 0.65mm; max 1.2mm			
Recommended storage environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C			



## SIGC54T60R3

#### **MAXIMUM RATINGS:**

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T <sub>j</sub> =25 °C	V <sub>CE</sub>	600	V
DC collector current, limited by T <sub>jmax</sub>	I <sub>C</sub>	1)	Α
Pulsed collector current, t <sub>p</sub> limited by T <sub>jmax</sub>	I <sub>cpuls</sub>	300	А
Gate emitter voltage	$V_{GE}$	±20	V
Operating junction and storage temperature	$T_{\rm j},~T_{\rm stg}$	-40 +175	°C
SC data, V <sub>GE</sub> = 15V, V <sub>CC</sub> = 360V, Tvj = 150°C	<i>t</i> p	5	μs

<sup>1)</sup> depending on thermal properties of assembly

### STATIC CHARACTERISTICS (tested on chip), $T_{\rm j}$ =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
Tarameter	Cymbol	Conditions	min.	typ.	max.	
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	$V_{GE}$ =0 $V$ , $I_{C}$ = 4 $mA$	600			
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	V <sub>GE</sub> =15V, I <sub>C</sub> =100A	1.05	1.45	1.85	V
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_C$ =1600 $\mu$ A , $V_{GE}$ = $V_{CE}$	tbd	5.8	tbd	
Zero gate voltage collector current	I <sub>CES</sub>	$V_{CE}$ =600V , $V_{GE}$ =0V			270	μA
Gate-emitter leakage current	I <sub>GES</sub>	V <sub>CE</sub> =0V , V <sub>GE</sub> =20V			600	nA
Integrated gate resistor	$R_{Gint}$			2		Ω

#### **ELECTRICAL CHARACTERISTICS** (verified by design/characterization):

Parameter	Symbol	Conditions	Value			Unit
raiametei	Symbol		min.	typ.	max.	Oilit
Input capacitance	Ciss	V <sub>CE</sub> =25V,		tbd		nF
Output capacitance	Coss	$V_{GE}=0V$ ,		tbd		
Reverse transfer capacitance	C <sub>rss</sub>	f=1MHz		tbd		

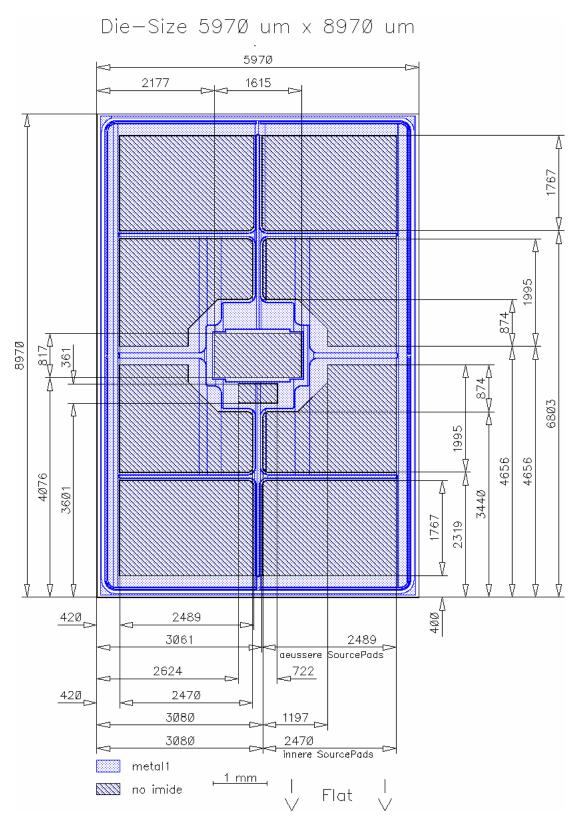
### SWITCHING CHARACTERISTICS (verified by design/characterization), inductive load

Parameter	Symbol	Conditions	Value 2)			Unit
	Symbol Conditions		min.	typ.	max.	Onne
Turn-on delay time	$t_{d(on)}$	<i>T</i> <sub>j</sub> =125°C		tbd		ns
Rise time	t <sub>r</sub>	$V_{\rm CC} = 300  \text{V},$		tbd		
Turn-off delay time	$t_{d(off)}$	I <sub>C</sub> =100A, V <sub>GE</sub> =-15/15V,		tbd		
Fall time	$t_{f}$	$R_{\rm G}$ = tbd $\Omega$	_	tbd		

<sup>&</sup>lt;sup>2)</sup> values also influenced by parasitic L- and C- in measurement and package.



#### **CHIP DRAWING:**





### SIGC54T60R3

#### **FURTHER ELECTRICAL CHARACTERISTICS:**

This chip data sheet refers to the device data sheet	tbd					
DESCRIPTION:						
AQL 0,65 for visual inspection according to failure catalog						
Electrostatic Discharge Sensitive Device according to MIL-STD 883						
Test-Normen Villach/Prüffeld						

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