## OVERVIEW

The SM8120A is a high efficiency step-up DC/DC converter. Due to high voltage CMOS process realizing 24 V output supply as maximum value, white LED of 2-4 lights connected in series can be lighted. By connecting in series, current variation among LED is eliminated. Current value sent to white LED can be set by external resistors. In addition, brightness can also be adjusted by control to FB pin or CE pin.

## FEATURES

- Boost-up control using PFM
- White LED of 2-4 lights (connected in series) lighted
- Output current value can be set by external resistors ( $51 \Omega: 9.8 \mathrm{~mA}, 33 \Omega: 15.2 \mathrm{~mA}, 24 \Omega: 20.8 \mathrm{~mA}$ )
- Brightness adjustable by control to FB pin or CE pin
- Current variation among LED decreased by high precision
- High efficient drive by step-up model
- Supply voltage range: 2.4 to 5.5 V
- Maximum output voltage: 24 V
- Quiescent current: $80 \mu \mathrm{~A}$ (typ)
- Standby current: $1.0 \mu \mathrm{~A}$ (max)
- $\mathrm{R}_{\mathrm{ON}}$ (Switching MOS-Tr): $2 \Omega$ (typ)
- Maximum switching frequency: 500 kHz (typ)
- Output current detection accuracy: $\pm 2 \%$
- Small package: SOT23-5


## APPLICATIONS

- Cellular phone
- Pager
- Digital still camera
- Handy terminal
- PDAs
- Portable games
- White LED drive
- LCD bias supply
- Flash memory supply


## ORDERING INFORMATION

| Device | Package |
| :---: | :---: |
| SM8120AH | SOT23-5 |

## PINOUT

## (Top view)



## PACKAGE DIMENSIONS

(Unit: mm)


## BLOCK DIAGRAM



## PIN DESCRIPTION

| Number | Name | I/O | Description |
| :---: | :---: | :---: | :--- |
| 1 | VDD | - | Power supply |
| 2 | VSS | - | GND |
| 3 | SW | 0 | Coil switching |
| 4 | FB | I | Feed back (Output current detection) |
| 5 | CE | $\mathrm{Ip}^{1}$ | Chip enable (High active) |

[^0]
## SPECIFICATIONS

## Absolute Maximum Ratings

| Parameter | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Supply voltage range | $\mathrm{V}_{\mathrm{DD}}$ | -0.3 to 6.5 | V |
| Input voltage range | $\mathrm{V}_{\mathrm{IN}}$ | $\mathrm{V}_{\mathrm{SS}}-0.3$ to $\mathrm{V}_{\mathrm{DD}}+0.3$ | V |
| SW output voltage range | $\mathrm{V}_{\mathrm{SW}}$ | -0.3 to 27 | V |
| SW input current | $\mathrm{I}_{\mathrm{SW}}$ | 250 | mA |
| Power dissipation | $\mathrm{P}_{\mathrm{D}}$ | $250\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$ | mW |
| Operating temperature range | $\mathrm{T}_{\text {opr }}$ | -40 to 85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range | $\mathrm{T}_{\text {stg }}$ | -55 to 125 | ${ }^{\circ} \mathrm{C}$ |

## Electrical Characteristics

$\mathrm{V}_{\mathrm{DD}}=3.6 \mathrm{~V}, \mathrm{~V}_{\mathrm{SS}}=0 \mathrm{~V}, \mathrm{Ta}=25^{\circ} \mathrm{C}$ unless otherwise noted

| Parameter | Pin | Symbol | Condition | Rating |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | min | typ | max |  |
| Supply voltage | VDD | $V_{D D}$ |  | 2.4 | 3.6 | 5.5 | V |
| Standby current | VDD | $I_{\text {STB }}$ | $\mathrm{V}_{\text {CE }}=0 \mathrm{~V}$ | - | - | 1.0 | $\mu \mathrm{A}$ |
| Quiescent current | VDD | $I_{\text {DD }}$ | $\mathrm{V}_{\mathrm{FB}}=1.0 \mathrm{~V}$ | - | 80 | 120 | $\mu \mathrm{A}$ |
| SW-Tr ON resister | SW | $\mathrm{R}_{\mathrm{ON}}$ | $\mathrm{I}_{\mathrm{SW}}=100 \mathrm{~mA}, \mathrm{~V}_{\mathrm{DD}}=3.6 \mathrm{~V}$ | - | 2.0 | 3.0 | $\Omega$ |
| SW-Tr leak current | SW | ILEAK | $\mathrm{V}_{\mathrm{SW}}=\mathrm{V}_{\mathrm{DD}}$ | - | - | 1.0 | $\mu \mathrm{A}$ |
| Maximum switching frequency | SW | fosc | $\mathrm{V}_{\mathrm{FB}}=0 \mathrm{~V}$ | 450 | 500 | 550 | kHz |
| Duty | SW | Duty | $\mathrm{V}_{\mathrm{FB}}=0 \mathrm{~V}$ | 53 | 60 | 67 | \% |
| Input voltage | CE | $\mathrm{V}_{\mathrm{IH}}$ |  | 2.0 | - | - | V |
|  |  | $\mathrm{V}_{\text {IL }}$ |  | - | - | 0.6 | V |
| Input current | CE | $\mathrm{I}_{\text {CE }}$ | $\mathrm{V}_{\mathrm{CE}}=3.6 \mathrm{~V}$ | - | 5.0 | 10 | $\mu \mathrm{A}$ |
|  | FB | $\mathrm{I}_{\text {FB }}$ | $V_{F B}=0.5 \mathrm{~V}$ | -1.0 | - | 1.0 | $\mu \mathrm{A}$ |
| Soft-start time | SW | $\mathrm{T}_{\text {S }}$ |  | - | 500 | - | $\mu \mathrm{s}$ |
| FB voltage | FB | $V_{\text {FB }}$ |  | 0.49 | 0.50 | 0.51 | V |

## OPERATION OVERVIEW



The SM8120A basic structure is a step-up DC/DC converter. The booster control employs Pulse Frequency Modulation (PFM) which controls the frequency ( 500 kHz max.) at constant SW-Tr ON time ( $1.2 \mu \mathrm{~s}$ typ.). The LED current is set by a current-setting resistor R1 connected between pins FB (with stable voltage of 0.5 V typ.) and VSS.

When the switching transistor $\mathrm{SW}-\mathrm{Tr}$ is ON , energy is stored in the inductor L . When $\mathrm{SW}-\mathrm{Tr}$ is rapidly switched OFF, the energy stored in the inductor generates a voltage across the terminals of the inductor. The induced voltage, after being added to the input voltage, turns ON the Schottky barrier diode SBD and the stored energy is transferred to the output capacitor. This sequence of events continues repeatedly, boosting the output voltage.

The SM8120A features a built-in soft-start function. The soft-start time is approximately $500 \mu \mathrm{~s}$ from after the chip enable input CE rising edge. During this interval, the maximum SW-Tr ON time is restricted to $0.6 \mu \mathrm{~s}$.

## Selecting the Current-setting Resistor (R1)

The SM8120A control stabilizes the voltage on pin FB ( 0.5 V typ.). Hence, the current-setting resistor R1 connected between FB and VSS sets the LED current $\mathrm{I}_{\text {LED }}$, where the resistance R 1 is given by the following equation.
$\mathrm{R} 1=0.5 / \mathrm{I}_{\mathrm{LED}}$


## Selecting the Inductor (L)

The recommended inductance for use with the SM8120A is $22 \mu \mathrm{H}$. The inductor DC resistance affects the power efficiency, therefore a low DC resistance inductor is recommended. Note also that the peak inductor current $I_{\text {peak }}$ should not exceed the inductor maximum current rating. In pulsed current mode control, the peak inductor current $\mathrm{I}_{\text {peak }}$ is given by the following equation.
$\mathrm{I}_{\text {peak }}=\left(\mathrm{V}_{\mathrm{IN}} \times \mathrm{T}_{\mathrm{ON}}\right) / \mathrm{L}$
For example, if the input voltage $\mathrm{V}_{\mathrm{IN}}$ is 3.6 V , the inductance L is $22 \mu \mathrm{H}$, and the $\mathrm{SW}-\mathrm{Tr} \mathrm{ON}$ time $\mathrm{T}_{\mathrm{ON}}$ is $1.2 \mu \mathrm{~s}$, then the peak inductor current $\mathrm{I}_{\text {peak }}$ is $\left(3.6 \times 1.2 \times 10^{-6}\right) /\left(2.2 \times 10^{-6}\right)=0.2 \mathrm{~A}=200 \mathrm{~mA}$.

## Selecting the Capacitors ( $\mathrm{C}_{\mathrm{IN}}, \mathrm{C}_{\text {OUT }}$ )

The recommended capacitances for use with the SM 8120 A are $4.7 \mu \mathrm{~F}$ ceramic input capacitor $\mathrm{C}_{\mathrm{IN}}$ and $1.0 \mu \mathrm{~F}$ tantalum output capacitor $\mathrm{C}_{\text {OUT }}$. The input capacitor ESR ratings affect the ripple voltage, therefore capacitors with low ESR rating are recommended. When the output capacitor ESR ratings are too low, it affect the response to the FB pin, therefore tantalum capacitors are recommended. The input capacitor should be mounted close to the SM8120A IC. Note that the capacitor voltage ratings should be selected to provide sufficient margin for the applied input and output voltages.
For example, if a lithium-ion battery ( 2.5 to 4.5 V ) is connected to the input and 3 white LEDs connected in series at the output draw 20 mA , then the maximum input voltage is 4.5 V and the maximum output voltage is $(4.0 \mathrm{~V} \times 3$ LEDs $)+0.5 \mathrm{~V}=12.5 \mathrm{~V}$. Therefore, the input capacitor should have a voltage rating of 6 V , and the output capacitor should have a voltage rating of 16 V .

## Selecting the Rectifier Schottky Barrier Diode (SBD)

The rectifier schottky barrier diode forward-direction voltage drop affects the power efficiency, therefore a Schottky barrier diode with low forward-direction voltage drop is recommended. Note that the diode should be selected to provide sufficient margin for the rated current and reverse-direction withstand voltage.

## Board Layout Notes

The following precautions should be followed for stable device operation.

- The inductor L and Schottky barrier diode SBD should be connected close to the pin SW using thick, short circuit wiring.
- The input capacitor $\mathrm{C}_{\text {IN }}$ should be mounted close to the IC.
- The IC supply voltage $\mathrm{V}_{\mathrm{DD}}$ wiring and inductor supply wiring should be isolated, reducing any common impedances.
- The ground wiring should be connected at a single point, reducing any common impedances.



## LED OPEN-CIRCUIT PROTECTION

When there is no load (LED open-circuit), the FB pin is pulled-down and then switching occurs at maximum frequency. Consequently, the output voltage continues to be boosted and the SW pin voltage may exceed the maximum rating of 27 V . A zener diode can be added so that it acts as the output load when the LED is opencircuit, preventing the SW voltage from rising. The zener diode must be selected so that the zener does not breakdown during normal operation. The zener voltage $\mathrm{V}_{\mathrm{ZD}}$ range is given by the following relationship, where N is the number of LEDs connected in series, $\mathrm{V}_{\mathrm{FMAX}}$ is the maximum LED forward-bias voltage drop, $\mathrm{V}_{\text {OUT MAX }}$ is the SW pin maximum output voltage, $\mathrm{V}_{\mathrm{FB}}$ is the FB pin voltage, and $\mathrm{V}_{\mathrm{SBD}}$ is the Schottky-barrier diode forward-bias voltage drop.
$\left(\mathrm{V}_{\text {F MAX }} \times \mathrm{N}\right) \leq \mathrm{V}_{\text {ZD }} \leq\left(\mathrm{V}_{\text {OUT MAX }}-\mathrm{V}_{\mathrm{FB}}-\mathrm{V}_{\text {SBD }}\right)$
When the load is applied using a connector (SM8120A and LEDs on separate boards), the zener diode should be mounted on the same board as the SM8120A device so that the SW boost prevention function can operate when the load is disconnected.

## Zener Diode (ZD) Only Connection

When the load is removed (LEDs open circuit), the output voltage is determined by the zener voltage, and the output current is determined by the output current-setting resistance. Consequently, the output current when the load is removed is not limited, and thus the input current cannot be controlled.


## Zener Diode (ZD) and Current-Limiting Resistance Connection

When the load is removed (LEDs open circuit), the output voltage is determined by the zener voltage, and the output current is determined by the sum of the output current-setting resistance and the current-limiting resistance. Consequently, the output current is limited when the load is removed, and the input current can be controlled.


## BRIGHTNESS ADJUSTMENT

## Brightness Adjustment using FB Pin

The LED brightness can be adjusted using an input DC control voltage connected through resistor R3 to the FB pin. Alternatively, the brightness can be controlled by a PWM signal by adding a low-pass filter comprising resistor R4 and capacitor C 1 . The PWM signal frequency range is determined by the low-pass filter coefficients. For example, the recommended values for resistor $\mathrm{R} 4(50 \mathrm{k} \Omega)$ and capacitor $\mathrm{C} 1(0.1 \mu \mathrm{~F})$ provide a PWM signal frequency range of 1 kHz to 1 MHz .

## Brightness adjustment using FB pin (DC voltage input)



Brightness adjustment circuit using FB pin (DC voltage input)


DC voltage vs. LED current

When the brightness is controlled by DC voltage $\left(\mathrm{V}_{\mathrm{DC}}\right)$ connected to resistor R 3 , the LED current $\left(\mathrm{I}_{\mathrm{LED}}\right)$ is given by equation 1 .

$$
\begin{equation*}
\mathrm{ILED}=\frac{\mathrm{VFB}-\frac{\mathrm{R} 2 \times(\mathrm{VDC}-\mathrm{VFB})}{\mathrm{R} 3}}{\mathrm{R} 1} \tag{1}
\end{equation*}
$$

If the values $\mathrm{R} 1=30 \Omega, \mathrm{R} 2=20 \mathrm{k} \Omega, \mathrm{R} 3=100 \mathrm{k} \Omega, \mathrm{V}_{\mathrm{FB}}=0.5 \mathrm{~V}$, and $\mathrm{V}_{\mathrm{DC}}=0 \mathrm{~V}$ are inserted in equation 1, the LED current $\mathrm{I}_{\text {LED }}=20 \mathrm{~mA}$, as shown in equation 2 .

$$
\begin{equation*}
\text { ILED }=\frac{0.5-\frac{20,000 \times(0-0.5)}{100,000}}{30}=\frac{0.6}{30}=20 \mathrm{~mA} \tag{2}
\end{equation*}
$$

If the values $\mathrm{R} 1=30 \Omega, \mathrm{R} 2=20 \mathrm{k} \Omega, \mathrm{R} 3=100 \mathrm{k} \Omega, \mathrm{V}_{\mathrm{FB}}=0.5 \mathrm{~V}$, and $\mathrm{V}_{\mathrm{DC}}=3 \mathrm{~V}$ are inserted in equation 1, the LED current $\mathrm{I}_{\text {LED }}=0 \mathrm{~mA}$, as shown in equation 3 .

$$
\begin{equation*}
\text { ILED }=\frac{0.5-\frac{20,000 \times(3-0.5)}{100,000}}{30}=\frac{0}{30}=0 \mathrm{~mA} \tag{3}
\end{equation*}
$$

Taking the above diagram as an example, inserting the values $\mathrm{R} 1=30 \Omega, \mathrm{R} 2=20 \mathrm{k} \Omega, \mathrm{R} 3=100 \mathrm{k} \Omega, \mathrm{V}_{\mathrm{FB}}=0.5 \mathrm{~V}$, and $V_{D C}=0$ to 3 V into equation 1 gives the maximum LED current $\mathrm{I}_{\text {LED }}$ of 20 mA when $\mathrm{V}_{\mathrm{DC}}=0 \mathrm{~V}$ (equation 2) and the minimum LED current $\mathrm{I}_{\mathrm{LED}}$ of 0 mA when $\mathrm{V}_{\mathrm{DC}}=3 \mathrm{~V}$ (equation 3).

## Brightness adjustment using FB pin (PWM signal input)



Brightness adjustment circuit using FB pin (PWM signal input)


PWM signal vs. LED current

When the brightness is controlled by PWM signal ( $\mathrm{V}_{\mathrm{PWM}} \times$ Duty $)$, the LED current $\left(\mathrm{I}_{\text {LED }}\right)$ is given by equation 4.

$$
\begin{equation*}
\mathrm{ILED}=\frac{\mathrm{VFB}-\frac{\mathrm{R} 2 \times(\mathrm{VPWM} \times \mathrm{Duty}-\mathrm{VFB})}{\mathrm{R} 3+\mathrm{R} 4}}{\mathrm{R} 1} \tag{4}
\end{equation*}
$$

If the values $\mathrm{R} 1=30 \Omega, \mathrm{R} 2=20 \mathrm{k} \Omega, \mathrm{R} 3=50 \mathrm{k} \Omega, \mathrm{R} 4=50 \mathrm{k} \Omega, \mathrm{V}_{\mathrm{FB}}=0.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{PWM}}=3 \mathrm{~V}$, and Duty $=0 \%$ are inserted in equation 4 , the LED current $\mathrm{I}_{\text {LED }}=20 \mathrm{~mA}$, as shown in equation 5 .

$$
\begin{equation*}
\operatorname{ILED}=\frac{0.5-\frac{20,000 \times(3 \times 0-0.5)}{50,000+50,000}}{30}=\frac{0.6}{30}=20 \mathrm{~mA} \tag{5}
\end{equation*}
$$

If the values $\mathrm{R} 1=30 \Omega, \mathrm{R} 2=20 \mathrm{k} \Omega, \mathrm{R} 3=50 \mathrm{k} \Omega, \mathrm{R} 4=50 \mathrm{k} \Omega, \mathrm{V}_{\mathrm{FB}}=0.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{PWM}}=3 \mathrm{~V}$, and Duty $=100 \%$ are inserted in equation 4 , the LED current $\mathrm{I}_{\mathrm{LED}}=0 \mathrm{~mA}$, as shown in equation 6 .

$$
\begin{equation*}
\text { ILED }=\frac{0.5-\frac{20,000 \times(3 \times 1-0.5)}{50,000+50,000}}{30}=\frac{0}{30}=0 \mathrm{~mA} \tag{6}
\end{equation*}
$$

Taking the above diagram as an example, inserting the values $\mathrm{R} 1=30 \Omega, \mathrm{R} 2=20 \mathrm{k} \Omega, \mathrm{R} 3=50 \mathrm{k} \Omega, \mathrm{R} 4=50 \mathrm{k} \Omega$, $\mathrm{V}_{\mathrm{FB}}=0.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{PWM}}=3 \mathrm{~V}$, and Duty $=0$ to $100 \%$ into equation 4 gives the maximum LED current $\mathrm{I}_{\mathrm{LED}}$ of 20 mA when Duty $=0 \%$ (equation 5) and the minimum LED current $\mathrm{I}_{\text {LED }}$ of 0 mA when Duty $=100 \%$ (equation 6).

## Brightness Adjustment using CE Pin

The LED average current can be adjusted by controlling the duty of a PWM signal input on the CE pin. When CE goes from LOW to HIGH, the soft start function operates (with $500 \mu$ s constant soft start time) and, therefore, the LED average current ratio for a given PWM signal duty falls with increasing PWM signal frequency. Taking this into consideration, the recommended PWM control signal has a frequency range of 100 to 400 Hz with duty cycle range of 10 to $90 \%$.


When adjusting the brightness using the CE pin, a ripple voltage synchronized to the PWM signal is generated across the output capacitor COUT. The amplitude of the ripple voltage is determined by the number of LEDs and their forward-bias voltage drop characteristics. If a ceramic capacitor is used for the output capacitor $\mathrm{C}_{\text {OUT }}$, an audible noise may be generated due to the ceramic capacitor's piezoelectric effect. The audible noise level depends on the ceramic capacitor (capacitance, bias dependency, withstand voltage etc.), LEDs (number, forward-bias voltage drop etc.), and mounting board (thickness, mounting conditions etc.), and thus should be verified under actual conditions.

Alternatively, a tantalum capacitor or film capacitor with low piezoelectric effect can be used as the output capacitor $\mathrm{C}_{\text {OUT }}$ to minimize the noise level, or the brightness can be adjusted using the FB pin as described earlier. The audible noise generated when using the CE pin is not an inherent phenomena of the SM8120A device, but of the brightness adjustment method employed.


Output voltage with LEDs ON


Output voltage with LEDs
OFF


CE input signal and output ripple voltage

## Current Switching using External Transistors

If only a few brightness steps are required, the LED current can be adjusted by switching the LED current setting resistance using external transistors (Tr).


| Select signal 2 | Select signal 1 | ILED |
| :---: | :---: | :--- |
| Low | Low | 2 mA |
| Low | High | $2+5=7 \mathrm{~mA}$ |
| High | Low | $2+12.5=14.5 \mathrm{~mA}$ |
| High | High | $2+5+12.5=19.5 \mathrm{~mA}$ |

## TYPICAL APPLICATION CIRCUITS

## 2 LEDs


$\mathrm{C}_{\mathrm{IN}}: 2012 \mathrm{Y} 5 \mathrm{VIC475Z}$ (TDK)
$\mathrm{C}_{\text {OUT }}: 16 \mathrm{MCM} 225 \mathrm{MA}$ (Nippon Chemi-con)
L : LQH32CN220K21 (Murata)
SBD : RB551V-30 (ROHM)
LED : NSCW455 (NICHIA)




## 3 LEDs


$\mathrm{C}_{\mathrm{IN}}: 2012 \mathrm{Y} 5 \mathrm{VIC475Z}$ (TDK)
Cout :16MCM225MA (Nippon Chemi-con)
L : LQH32CN220K21 (Murata)
SBD : RB551V-30 (ROHM)
LED : NSCW455 (NICHIA)




## 4 LEDs


$\mathrm{C}_{\mathrm{IN}}: 2012 \mathrm{Y} 5 \mathrm{VIC475Z}$ (TDK)
$\mathrm{C}_{\text {OUT }}$ :16MCM225MA (Nippon Chemi-con)
L : LQH32CN220K21 (Murata)
SBD : RB551V-30 (ROHM)
LED : NSCW455 (NICHIA)




## FOOTPRINT PATTERN

SOT23-5


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[^0]:    1. Input with built-in pull-down resistor
