

SM8120A White LED Driver IC

OVERVIEW

NIPPON PRECISION CIRCUITS INC

The SM8120A is a high efficiency step-up DC/DC converter. Due to high voltage CMOS process realizing 24V output supply as maximum value, white LED of 2–4 lights connected in series can be lighted. By connecting in series, current variation among LED is eliminated. Current value sent to white LED can be set by external resistors. In addition, brightness can also be adjusted by control to FB pin or CE pin.

FEATURES

PINOUT

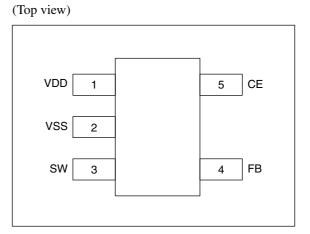
- Boost-up control using PFM
- White LED of 2-4 lights (connected in series) lighted
- Output current value can be set by external resistors (51Ω: 9.8mA, 33Ω: 15.2mA, 24Ω: 20.8mA)
- Brightness adjustable by control to FB pin or CE pin
- Current variation among LED decreased by high precision
- High efficient drive by step-up model
- Supply voltage range: 2.4 to 5.5V
- Maximum output voltage: 24V
- Quiescent current: 80µA (typ)
- Standby current: 1.0µA (max)
- R_{ON} (Switching MOS-Tr): 2Ω (typ)
- Maximum switching frequency: 500kHz (typ)
- Output current detection accuracy: $\pm 2\%$
- Small package: SOT23-5

APPLICATIONS

- Cellular phone
- Pager
- Digital still camera
- Handy terminal
- PDAs
- Portable games
- White LED drive
- LCD bias supply
- Flash memory supply

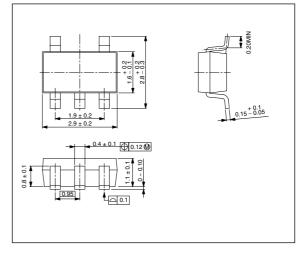
ORDERING INFORMATION

Device	Package		
SM8120AH	SOT23-5		

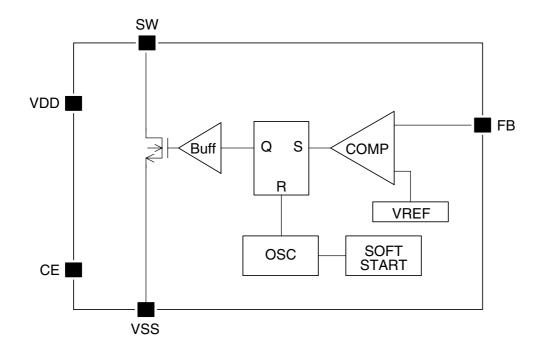


PACKAGE DIMENSIONS

(Unit: mm)



BLOCK DIAGRAM



PIN DESCRIPTION

Number	Name	I/O	Description
1	VDD	-	Power supply
2	VSS	-	GND
3	SW	0	Coil switching
4	FB	I	Feed back (Output current detection)
5	CE	lp ¹	Chip enable (High active)

1. Input with built-in pull-down resistor

SPECIFICATIONS

Absolute Maximum Ratings

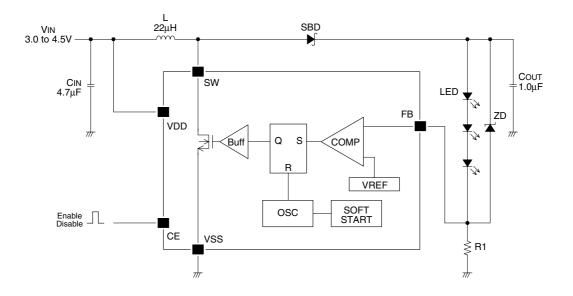
Parameter	Symbol	Rating	Unit
Supply voltage range	V _{DD}	-0.3 to 6.5	V
Input voltage range	V _{IN}	V_{SS} – 0.3 to V_{DD} + 0.3	V
SW output voltage range	V _{SW}	–0.3 to 27	V
SW input current	I _{SW}	250	mA
Power dissipation	PD	250 (Ta = 25°C)	mW
Operating temperature range	T _{opr}	-40 to 85	°C
Storage temperature range	T _{stg}	-55 to 125	°C

Electrical Characteristics

 $V_{DD} = 3.6V$, $V_{SS} = 0V$, $Ta = 25^{\circ}C$ unless otherwise noted

Parameter	Pin	Cumhal	Condition	Rating			Unit
Farameter	FIII	Symbol		min	typ	max	
Supply voltage	VDD	V _{DD}		2.4	3.6	5.5	V
Standby current	VDD	I _{STB}	V _{CE} = 0V	-	-	1.0	μΑ
Quiescent current	VDD	I _{DD}	V _{FB} = 1.0V	-	80	120	μA
SW-Tr ON resister	SW	R _{ON}	I _{SW} = 100mA, V _{DD} = 3.6V	-	2.0	3.0	Ω
SW-Tr leak current	SW	I _{LEAK}	V _{SW} = V _{DD}	-	-	1.0	μA
Maximum switching frequency	SW	f _{OSC}	V _{FB} = 0V	450	500	550	kHz
Duty	SW	Duty	V _{FB} = 0V	53	60	67	%
Input voltage CE	CE	V _{IH}		2.0	-	-	V
	UL	V _{IL}		-	-	0.6	V
Input ourront	CE	I _{CE}	V _{CE} = 3.6V	-	5.0	10	μΑ
Input current	FB	I _{FB}	V _{FB} = 0.5V	-1.0	-	1.0	μΑ
Soft-start time	SW	T _{SS}		-	500	-	μs
FB voltage	FB	V _{FB}		0.49	0.50	0.51	V

OPERATION OVERVIEW



The SM8120A basic structure is a step-up DC/DC converter. The booster control employs Pulse Frequency Modulation (PFM) which controls the frequency (500kHz max.) at constant SW-Tr ON time (1.2µs typ.). The LED current is set by a current-setting resistor R1 connected between pins FB (with stable voltage of 0.5V typ.) and VSS.

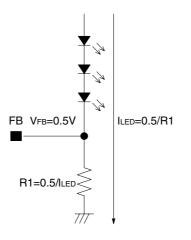
When the switching transistor SW-Tr is ON, energy is stored in the inductor L. When SW-Tr is rapidly switched OFF, the energy stored in the inductor generates a voltage across the terminals of the inductor. The induced voltage, after being added to the input voltage, turns ON the Schottky barrier diode SBD and the stored energy is transferred to the output capacitor. This sequence of events continues repeatedly, boosting the output voltage.

The SM8120A features a built-in soft-start function. The soft-start time is approximately 500 μ s from after the chip enable input CE rising edge. During this interval, the maximum SW-Tr ON time is restricted to 0.6 μ s.

Selecting the Current-setting Resistor (R1)

The SM8120A control stabilizes the voltage on pin FB (0.5V typ.). Hence, the current-setting resistor R1 connected between FB and VSS sets the LED current I_{LED} , where the resistance R1 is given by the following equation.

 $R1 = 0.5 / I_{LED}$



Selecting the Inductor (L)

The recommended inductance for use with the SM8120A is 22μ H. The inductor DC resistance affects the power efficiency, therefore a low DC resistance inductor is recommended. Note also that the peak inductor current I_{peak} should not exceed the inductor maximum current rating. In pulsed current mode control, the peak inductor current I_{peak} is given by the following equation.

 $I_{peak} = (V_{IN} \times T_{ON}) / L$

For example, if the input voltage V_{IN} is 3.6V, the inductance L is 22μ H, and the SW-Tr ON time T_{ON} is 1.2 μ s, then the peak inductor current I_{peak} is $(3.6 \times 1.2 \times 10^{-6}) / (2.2 \times 10^{-6}) = 0.2$ A = 200mA.

Selecting the Capacitors (C_{IN}, C_{OUT})

The recommended capacitances for use with the SM8120A are 4.7μ F ceramic input capacitor C_{IN} and 1.0μ F tantalum output capacitor C_{OUT}. The input capacitor ESR ratings affect the ripple voltage, therefore capacitors with low ESR rating are recommended. When the output capacitor ESR ratings are too low, it affect the response to the FB pin, therefore tantalum capacitors are recommended. The input capacitor should be mounted close to the SM8120A IC. Note that the capacitor voltage ratings should be selected to provide sufficient margin for the applied input and output voltages.

For example, if a lithium-ion battery (2.5 to 4.5V) is connected to the input and 3 white LEDs connected in series at the output draw 20mA, then the maximum input voltage is 4.5V and the maximum output voltage is $(4.0V \times 3 \text{ LEDs}) + 0.5V = 12.5V$. Therefore, the input capacitor should have a voltage rating of 6V, and the output capacitor should have a voltage rating of 16V.

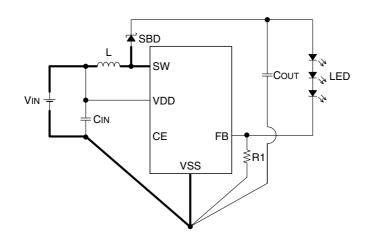
Selecting the Rectifier Schottky Barrier Diode (SBD)

The rectifier schottky barrier diode forward-direction voltage drop affects the power efficiency, therefore a Schottky barrier diode with low forward-direction voltage drop is recommended. Note that the diode should be selected to provide sufficient margin for the rated current and reverse-direction withstand voltage.

Board Layout Notes

The following precautions should be followed for stable device operation.

- The inductor L and Schottky barrier diode SBD should be connected close to the pin SW using thick, short circuit wiring.
- The input capacitor C_{IN} should be mounted close to the IC.
- The IC supply voltage V_{DD} wiring and inductor supply wiring should be isolated, reducing any common impedances.
- The ground wiring should be connected at a single point, reducing any common impedances.



LED OPEN-CIRCUIT PROTECTION

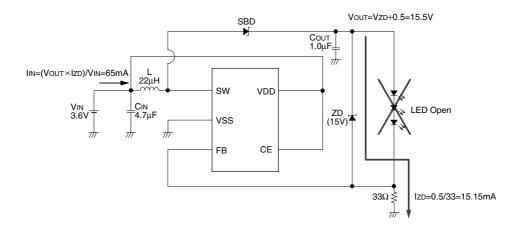
When there is no load (LED open-circuit), the FB pin is pulled-down and then switching occurs at maximum frequency. Consequently, the output voltage continues to be boosted and the SW pin voltage may exceed the maximum rating of 27V. A zener diode can be added so that it acts as the output load when the LED is open-circuit, preventing the SW voltage from rising. The zener diode must be selected so that the zener does not breakdown during normal operation. The zener voltage V_{ZD} range is given by the following relationship, where N is the number of LEDs connected in series, V_{FMAX} is the maximum LED forward-bias voltage drop, $V_{OUT MAX}$ is the SW pin maximum output voltage, V_{FB} is the FB pin voltage, and V_{SBD} is the Schottky-barrier diode forward-bias voltage drop.

 $(V_{FMAX} \times N) \leq V_{ZD} \leq (V_{OUTMAX} - V_{FB} - V_{SBD})$

When the load is applied using a connector (SM8120A and LEDs on separate boards), the zener diode should be mounted on the same board as the SM8120A device so that the SW boost prevention function can operate when the load is disconnected.

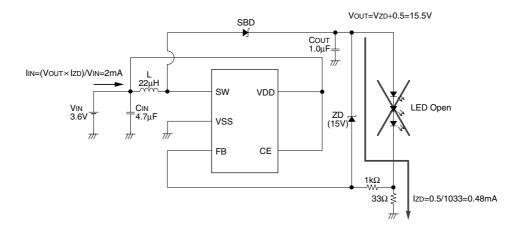
Zener Diode (ZD) Only Connection

When the load is removed (LEDs open circuit), the output voltage is determined by the zener voltage, and the output current is determined by the output current-setting resistance. Consequently, the output current when the load is removed is not limited, and thus the input current cannot be controlled.



Zener Diode (ZD) and Current-Limiting Resistance Connection

When the load is removed (LEDs open circuit), the output voltage is determined by the zener voltage, and the output current is determined by the sum of the output current-setting resistance and the current-limiting resistance. Consequently, the output current is limited when the load is removed, and the input current can be controlled.

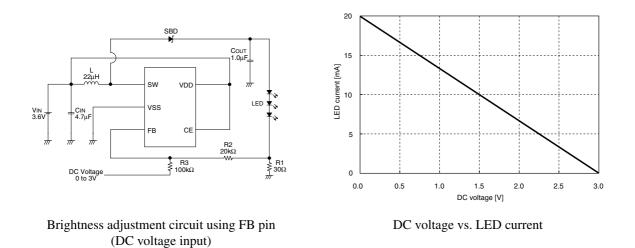


BRIGHTNESS ADJUSTMENT

Brightness Adjustment using FB Pin

The LED brightness can be adjusted using an input DC control voltage connected through resistor R3 to the FB pin. Alternatively, the brightness can be controlled by a PWM signal by adding a low-pass filter comprising resistor R4 and capacitor C1. The PWM signal frequency range is determined by the low-pass filter coefficients. For example, the recommended values for resistor R4 ($50k\Omega$) and capacitor C1 (0.1μ F) provide a PWM signal frequency range of 1kHz to 1MHz.

Brightness adjustment using FB pin (DC voltage input)



When the brightness is controlled by DC voltage (V_{DC}) connected to resistor R3, the LED current (I_{LED}) is given by equation 1.

ILED =
$$\frac{VFB - \frac{R2 \times (VDC - VFB)}{R3}}{R1} \qquad \cdots (1)$$

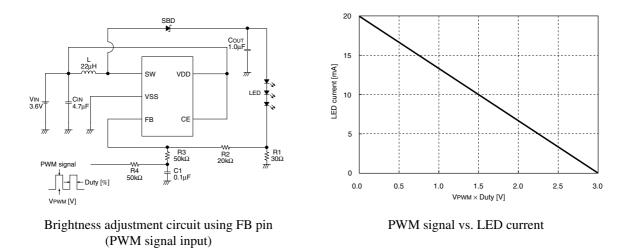
If the values R1 = 30Ω , R2 = $20k\Omega$, R3 = $100k\Omega$, V_{FB} = 0.5V, and V_{DC} = 0V are inserted in equation 1, the LED current I_{LED} = 20mA, as shown in equation 2.

ILED =
$$\frac{0.5 - \frac{20,000 \times (0 - 0.5)}{100,000}}{30} = \frac{0.6}{30} = 20$$
mA ... (2)

If the values R1 = 30Ω , R2 = $20k\Omega$, R3 = $100k\Omega$, V_{FB} = 0.5V, and V_{DC} = 3V are inserted in equation 1, the LED current I_{LED} = 0mA, as shown in equation 3.

ILED =
$$\frac{0.5 - \frac{20,000 \times (3 - 0.5)}{100,000}}{30} = \frac{0}{30} = 0$$
mA ... (3)

Taking the above diagram as an example, inserting the values $R1 = 30\Omega$, $R2 = 20k\Omega$, $R3 = 100k\Omega$, $V_{FB} = 0.5V$, and $V_{DC} = 0$ to 3V into equation 1 gives the maximum LED current I_{LED} of 20mA when $V_{DC} = 0V$ (equation 2) and the minimum LED current I_{LED} of 0mA when $V_{DC} = 3V$ (equation 3).



Brightness adjustment using FB pin (PWM signal input)

When the brightness is controlled by PWM signal ($V_{PWM} \times Duty$), the LED current (I_{LED}) is given by equation 4.

$$ILED = \frac{VFB - \frac{R2 \times (VPWM \times Duty - VFB)}{R3 + R4}}{R1} \qquad \dots (4)$$

If the values R1 = 30Ω , R2 = $20k\Omega$, R3 = $50k\Omega$, R4 = $50k\Omega$, V_{FB} = 0.5V, V_{PWM} = 3V, and Duty = 0% are inserted in equation 4, the LED current I_{LED} = 20mA, as shown in equation 5.

ILED =
$$\frac{0.5 - \frac{20,000 \times (3 \times 0 - 0.5)}{50,000 + 50,000}}{30} = \frac{0.6}{30} = 20 \text{mA} \qquad \dots (5)$$

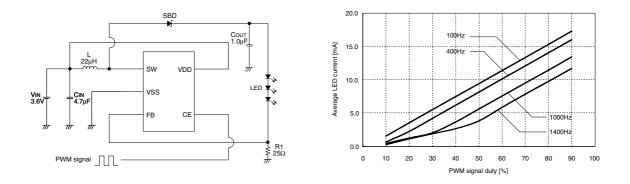
If the values $R1 = 30\Omega$, $R2 = 20k\Omega$, $R3 = 50k\Omega$, $R4 = 50k\Omega$, $V_{FB} = 0.5V$, $V_{PWM} = 3V$, and Duty = 100% are inserted in equation 4, the LED current $I_{LED} = 0$ mA, as shown in equation 6.

ILED =
$$\frac{0.5 - \frac{20,000 \times (3 \times 1 - 0.5)}{50,000 + 50,000}}{30} = \frac{0}{30} = 0 \text{mA} \qquad \dots (6)$$

Taking the above diagram as an example, inserting the values $R1 = 30\Omega$, $R2 = 20k\Omega$, $R3 = 50k\Omega$, $R4 = 50k\Omega$, $V_{FB} = 0.5V$, $V_{PWM} = 3V$, and Duty = 0 to 100% into equation 4 gives the maximum LED current I_{LED} of 20mA when Duty = 0% (equation 5) and the minimum LED current I_{LED} of 0mA when Duty = 100% (equation 6).

Brightness Adjustment using CE Pin

The LED average current can be adjusted by controlling the duty of a PWM signal input on the CE pin. When CE goes from LOW to HIGH, the soft start function operates (with 500µs constant soft start time) and, therefore, the LED average current ratio for a given PWM signal duty falls with increasing PWM signal frequency. Taking this into consideration, the recommended PWM control signal has a frequency range of 100 to 400Hz with duty cycle range of 10 to 90%.

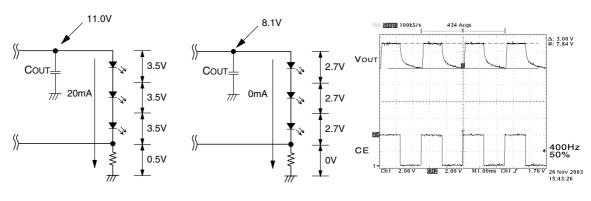




PWM signal duty vs. LED average current

When adjusting the brightness using the CE pin, a ripple voltage synchronized to the PWM signal is generated across the output capacitor C_{OUT} . The amplitude of the ripple voltage is determined by the number of LEDs and their forward-bias voltage drop characteristics. If a ceramic capacitor is used for the output capacitor C_{OUT} , an audible noise may be generated due to the ceramic capacitor's piezoelectric effect. The audible noise level depends on the ceramic capacitor (capacitance, bias dependency, withstand voltage etc.), LEDs (number, forward-bias voltage drop etc.), and mounting board (thickness, mounting conditions etc.), and thus should be verified under actual conditions.

Alternatively, a tantalum capacitor or film capacitor with low piezoelectric effect can be used as the output capacitor C_{OUT} to minimize the noise level, or the brightness can be adjusted using the FB pin as described earlier. The audible noise generated when using the CE pin is not an inherent phenomena of the SM8120A device, but of the brightness adjustment method employed.



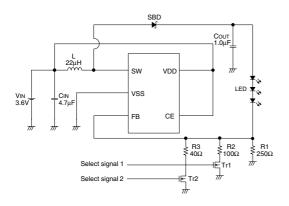
Output voltage with LEDs ON

Output voltage with LEDs OFF

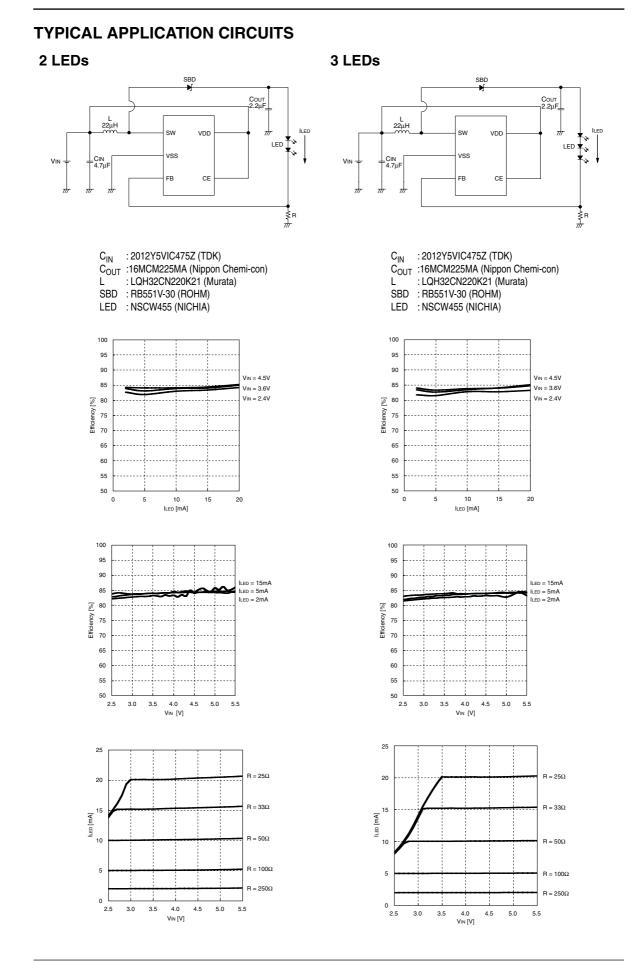
CE input signal and output ripple voltage

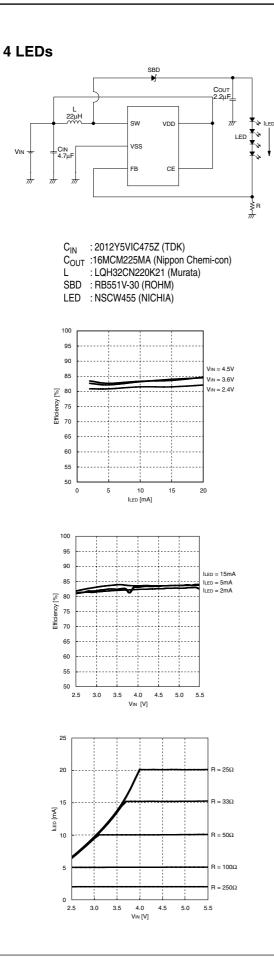
Current Switching using External Transistors

If only a few brightness steps are required, the LED current can be adjusted by switching the LED current setting resistance using external transistors (Tr).



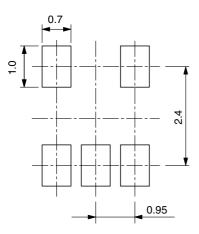
Select signal 2	Select signal 1	I _{LED}
Low	Low	2mA
Low	High	2 + 5 = 7mA
High	Low	2 + 12.5 = 14.5mA
High	High	2 + 5 + 12.5 = 19.5mA





FOOTPRINT PATTERN

SOT23-5



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