TECHNICAL DATA DATASHEET 4113, REV A

Three-Phase IGBT BRIDGE, With Gate Driver and Optical Isolation

DESCRIPTION: A 600 VOLT, 150 AMP, THREE PHASE IGBT BRIDGE

ELECTRICAL CHARACTERISTICS PER IGBT DEVICE		(Tj=2	(Tj=25°C UNLESS OTHERWISE SPECIFIED)					
PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT		
	IGBT SPE	CIFICATIONS	5					
Collector to Emitter Breakdown Voltage		BV _{CES}	600	-	-	V		
$I_{C} = 250 \ \mu A, \ V_{GE} = 0V$								
Continuous Collector Current	$T_c = 25$ °C	I _C	-	-	150	А		
	$T_c = 90$ $^{\circ}C$				130			
Pulsed Collector Current, 1mS		I _{CM}	-	-	250	А		
Gate to Emitter Voltage		V _{GE}	-	-	+/-20	V		
Gate-Emitter Leakage Current , V _{GE} = +/-2	0V	I _{GES}	-	-	+/- 100	nA		
Zero Gate Voltage Collector Current		I _{CES}	-	-				
$V_{CE} = 600 \text{ V}, V_{GE} = 0 \text{V} \text{ T}_{i} = 25^{\circ} \text{C}$		020			3	mA		
$V_{CE} = 480 \text{ V}, V_{GE} = 0 \text{ V} \text{ T}_i = 125^{\circ}\text{C}$					20	mA		
Collector to Emitter Saturation Voltage,	$T_c = 25 \ ^{O}C$	V _{CE(SAT)}	-	1.7	2.0	V		
$I_{C} = 100A, V_{GE} = 15V,$								
Maximum Thermal Resistance		$R_{ ext{ heta}JC}$	-	-	0.25	°C/W		
	Brake IGBT S		DNS					
Continuous Collector Current	$T_c = 25 \ ^{\circ}C$	I _c	-	-	80	A		
	$T_{c} = 90 {}^{\circ}C$				60			
Pulsed Collector Current, 0.5mS		I _{CM}	-	-	120	А		
Maximum Thermal Resistance		R _{θJC}	-	-	0.45	°C/W		
	Over-Temper	ature Shutdo	wn					
Over-Temperature Shutdown		Tsd	100	110	120	°C		
Over-Temperature Shutdown Hysteresis				20		°C		
Over-Temperature Output		Тсо		10		10mV/°C		

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PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
ULTRAFAST DIODES RA	TING AND CHA	RACTER	STICS		
Diode Peak Inverse Voltage	PIV	600	-	-	V
Continuous Forward Current, $T_c = 90$ ^o C	I _F	-	_	130	А
Forward Surge Current, t _p = 10 msec	I _{FSM}	-	_	500	А
Diode Forward Voltage, $I_F = 100A$	V _F	_	1.4	1.7	V
Diode Reverse Recovery Time (I _F =100A, V _{RR} =300V , di/dt=200 A/µs)	t _{rr}	-	90	160	nsec
Maximum Thermal Resistance	R _{θJC}	-	-	0.4	°C/W
Gate Driver					
Supply Voltage	VCC	10	15	20	V
Input On Current	HIN, LIN	2		5.0	mA
Opto-Isolator Logic High Input Threshold	l _{th}	-	1.6	-	mA
Input Reverse Breakdown Voltage	BV _{in}	5.0	_	-	V
Input Forward Voltage @ $I_{in} = 5mA$	V _F	-	1.5	1.7	V
Under Voltage Lockout	VCCUV	7.0	_	9.7	V
ITRIP Refernce Voltage (1)	Itrip-ref	1.45	1.5	1.55	V
Desaturation Over-Current Protection Blanking time ⁽²⁾	tbl	3	5	TBD	μsec
Input-to-Output Turn On Delay	t _{ond}	-		800	nsec
Output Turn On Rise Time	t _r	-		180	
Input-to-Output Turn Off Delay	t _{offd}	-		1000	
Output Turn Off Fall Time	t _f			160	
At VCC=300V, IC=50A, T _c = 25					
Input-Output Isolation Voltage	-	1000	-	-	V

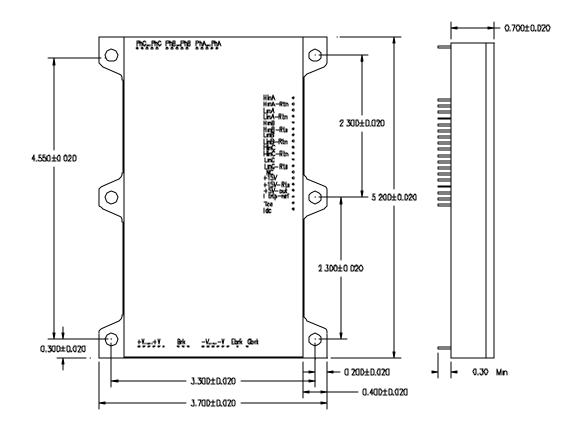
Maximum operating Junction Temperature	T _{jmax}	-40	-	150	°C
Maximum Storage Junction Temperature	T _{jmax}	-55	-	150	C°

(1) ITRIP Cycle-by cycle current limit is internally set to 70A peak. The set point can be lowered by connecting a resistor between ltrip-ref and Gnd. The set point can be increased by connecting a resistor between ltrip-ref and +5V ref

(2) Desaturation blanking maximum time is TBD and is only provided at the low-side IGBTs.

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Package Drawing:



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