

TECHNICAL DATA DATASHEET 4109, REV ENG-

Three-Phase IGBT BRIDGE, With Gate Driver and Optical Isolation

DESCRIPTION: A 1200 VOLT, 250 AMP, THREE PHASE IGBT BRIDGE

ELECTRICAL CHARACTERISTICS PER IGBT DEVICE

(Tj=25°C UNLESS OTHERWISE SPECIFIED)

SYMBOL	MIN	TYP	MAX	UNIT
BV _{CES}	1200	-	-	V
I _C	-	-	250	А
;			240	
I _{CM}	-	-	600	А
V_{GE}	-	-	+/-20	V
I _{GES}	-	-	+/- 300	nA
V _{GE(TH)}	3.0	-	6.0	V
I _{CES}	-	-		
			5	mA
			40	mA
V _{CE(SAT)}	-	2.5	2.8	V
$R_{ heta JC}$	-	-	0.10	°C/W
			0.20	°C/W
Ic	-	-	150	А
;			120	
I _{CM}	-	-	300	А
		•	•	
Tsd	100	110	120	°C
		20		°C
Tso		10		10mV/°C
	BV _{CES} I _C I _{CM} V _{GE} I _{GES} V _{GE(TH)} I _{CES} V _{CE(SAT)} R _{θJC} I _C I _{CM} Tsd	BV _{CES} 1200	BV _{CES} 1200 -	BV _{CES} 1200

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Diode Peak Inverse Voltage	PIV	1200	-	-	V
Continuous Forward Current, T _C = 90 °C	I _F	-	-	240	Α
Forward Surge Current, t _p = 10 msec	I _{FSM}	-	-	700	Α
Diode Forward Voltage, $I_F = 200A$	V _F	-	2.0	2.3	V
Diode Reverse Recovery Time (I _F =200A, V _{RR} =600V , di/dt=200 A/μs)	t _{rr}	-	180	250	nsec
Maximum Thermal Resistance	$R_{ heta JC}$	-	-	0.15	°C/W
Gate Driver					
Supply Voltage	VCC	10	15	20	V
Input On Current	HIN, LIN	2		5.0	mA
Opto-Isolator Logic High Input Threshold	I _{th}	-	1.6	-	mA
Input Reverse Breakdown Voltage	BV _{in}	5.0	-	-	V
Input Forward Voltage @ I _{in} = 5mA	V _F	-	1.5	1.7	V
Under Voltage Lockout	VCCUV	7.0	-	9.7	V
ITRIP Refernce Voltage (1)	Itrip-ref	1.45	1.5	1.55	V
Desaturation Over-Current Protection Blanking time (2)		3	5	TBD	μsec
Input-to-Output Turn On Delay	t _{ond}	-		800	nsec
Output Turn On Rise Time	t _r	-		180	
Input-to-Output Turn Off Delay	t _{offd}	-		1000	
Output Turn Off Fall Time	t _f			160	
At VCC=300V, IC=50A, $T_C = 25$					
Input-Output Isolation Voltage	-	1000	-	-	V
Hall Current Sensors Gain, at DC bus, Phase A, and Phase		TBD	TBD	TBD	V/A
Maximum operating Junction Temperature	T _{jmax}	-40	_	150	°C
Maximum operating outloadin reliiperature	' jmax	+0		100	O

⁽¹⁾ ITRIP Cycle-by cycle current limit is internally set to 200A peak. The set point can be lowered by connecting a resistor between Itrip-ref and Gnd. The set point can be increased by connecting a resistor between Itrip-ref and +5V ref

⁽²⁾ Desaturation blanking maximum time is TBD and is only provided at the low-side IGBTs.

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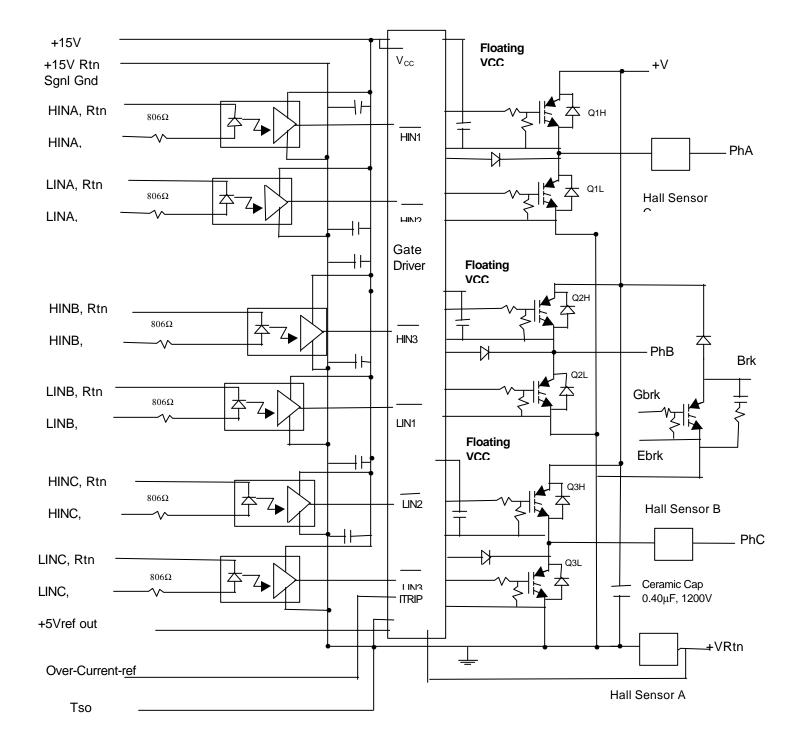
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Schematic Diagram:



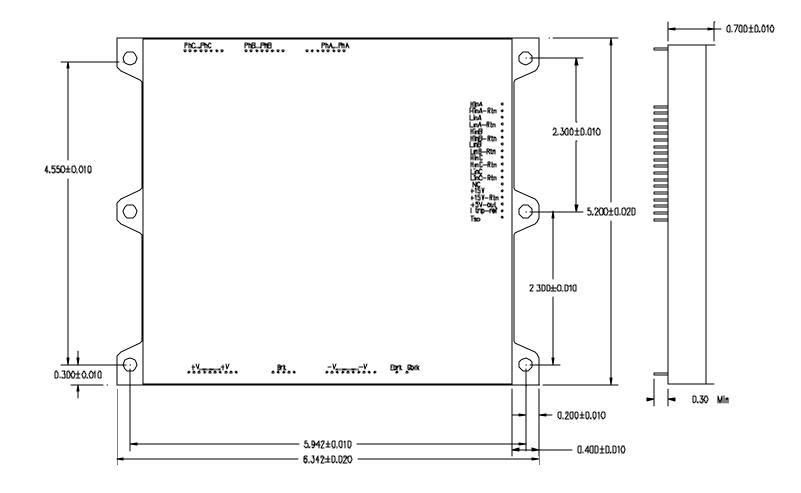
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Package Drawing:



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