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***DISCRETE POWER DIODES and THYRISTORS***  
***DATA BOOK***

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**PHASE CONTROL THYRISTORS**
**Hockey Puk Version**
**Features**

- Center amplifying gate
- Metal case with ceramic insulator
- International standard case TO-200AB (A-PUK)
- Extended temperature range

500A

**Typical Applications**

- DC motor controls
- Controlled DC power supplies
- AC controllers



case style TO-200AB (A-PUK)

**Major Ratings and Characteristics**

Parameters	ST280CH..C	Units
$I_{T(AV)}$	500	A
	@ $T_{hs}$	80 °C
$I_{T(RMS)}$	1130	A
	@ $T_{hs}$	25 °C
$I_{TSM}$	@ 50Hz	7200 A
	@ 60Hz	7500 A
$I^2t$	@ 50Hz	260 $KA^2s$
	@ 60Hz	230 $KA^2s$
$V_{DRM}/V_{RRM}$	400 to 600	V
$t_q$ typical	100	$\mu s$
$T_J$	- 40 to 150	°C

## ST280CH..C Series

### ELECTRICAL SPECIFICATIONS

#### Voltage Ratings

Type number	Voltage Code	$V_{DRM}/V_{RRM}$ , max. repetitive peak and off-state voltage V	$V_{RSM}$ , maximum non-repetitive peak voltage V	$I_{DRM}/I_{RRM}$ max. @ $T_J = T_J$ max mA
ST280CH..C	04	400	500	75
	06	600	700	

#### On-state Conduction

Parameter	ST280CH..C	Units	Conditions	
$I_{T(AV)}$ Max. average on-state current @ Heatsink temperature	500 (185)	A	180° conduction, half sine wave double side (single side) cooled	
	80 (110)	°C		
$I_{T(RMS)}$ Max. RMS on-state current	1130	A	DC @ 25°C heatsink temperature double side cooled	
$I_{TSM}$ Max. peak, one-cycle non-repetitive surge current	7200		t = 10ms	No voltage reappplied
	7500		t = 8.3ms	reappplied
	6000		t = 10ms	100% $V_{RRM}$
	6300		t = 8.3ms	reappplied
$I^2t$ Maximum $I^2t$ for fusing	260	KA <sup>2</sup> s	t = 10ms	No voltage reappplied
	235		t = 8.3ms	reappplied
	180		t = 10ms	100% $V_{RRM}$
	165		t = 8.3ms	reappplied
$I^2\sqrt{t}$ Maximum $I^2\sqrt{t}$ for fusing	2600	KA <sup>2</sup> √s	t = 0.1 to 10ms, no voltage reappplied	
$V_{T(TO)1}$ Low level value of threshold voltage	0.84	V	$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)})$ , $T_J = T_J$ max.	
$V_{T(TO)2}$ High level value of threshold voltage	0.88		$(I > \pi \times I_{T(AV)})$ , $T_J = T_J$ max.	
$r_{t1}$ Low level value of on-state slope resistance	0.50	mΩ	$(16.7\% \times \pi \times I_{T(AV)} < I < \pi \times I_{T(AV)})$ , $T_J = T_J$ max.	
$r_{t2}$ High level value of on-state slope resistance	0.47		$(I > \pi \times I_{T(AV)})$ , $T_J = T_J$ max.	
$V_{TM}$ Max. on-state voltage	1.35	V	$I_{pk} = 1000A$ , $T_J = T_J$ max, $t_p = 10ms$ sine pulse	
$I_H$ Maximum holding current	600	mA	$T_J = 25^\circ C$ , anode supply 12V resistive load	
$I_L$ Max (typical) latching current	1000 (300)			

#### Switching

Parameter	ST280CH..C	Units	Conditions
$di/dt$ Max. non-repetitive rate of rise of turned-on current	1000	A/μs	Gate drive 20V, 20Ω, $t_r \leq 1\mu s$ $T_J = T_J$ max, anode voltage $\leq 80\% V_{DRM}$
$t_d$ Typical delay time	1.0	μs	Gate current 1A, $di_g/dt = 1A/\mu s$ $V_d = 0.67\% V_{DRM}$ , $T_J = 25^\circ C$
$t_q$ Typical turn-off time	100		$I_{TM} = 300A$ , $T_J = T_J$ max, $di/dt = 20A/\mu s$ , $V_R = 50V$ $dv/dt = 20V/\mu s$ , Gate 0V 100Ω, $t_p = 500\mu s$

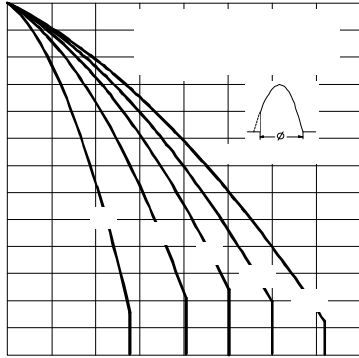


Fig. 3 - Current Ratings Characteristics

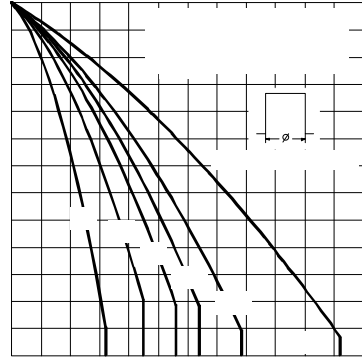


Fig. 4 - Current Ratings Characteristics

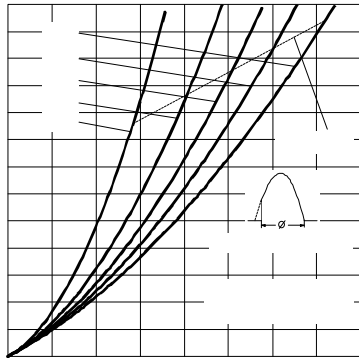


Fig. 5 - On-state Power Loss Characteristics

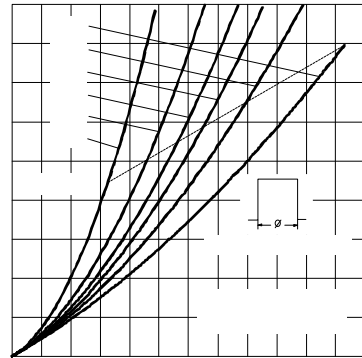


Fig. 6 - On-state Power Loss Characteristics

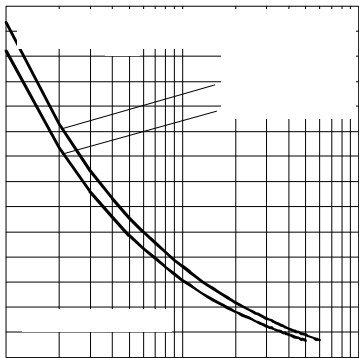


Fig. 7 - Maximum Non-Repetitive Surge Current  
Single and Double Side Cooled

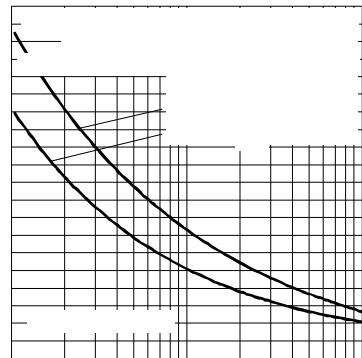


Fig. 8 - Maximum Non-Repetitive Surge Current  
Single and Double Side Cooled

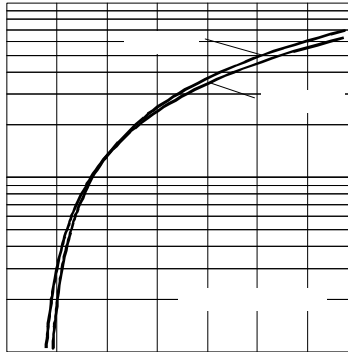


Fig. 9 - On-state Voltage Drop Characteristics

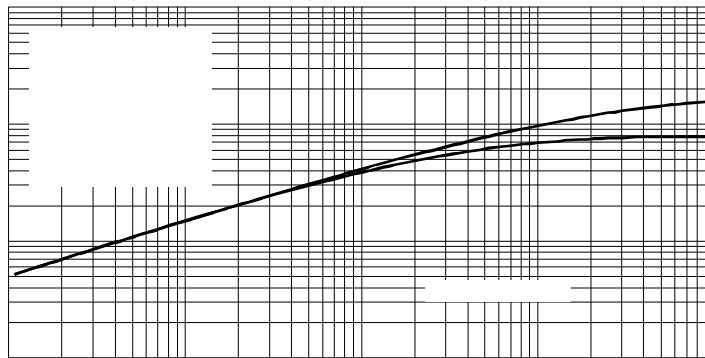


Fig. 10 - Thermal Impedance  $Z_{thj-hs}$  Characteristics

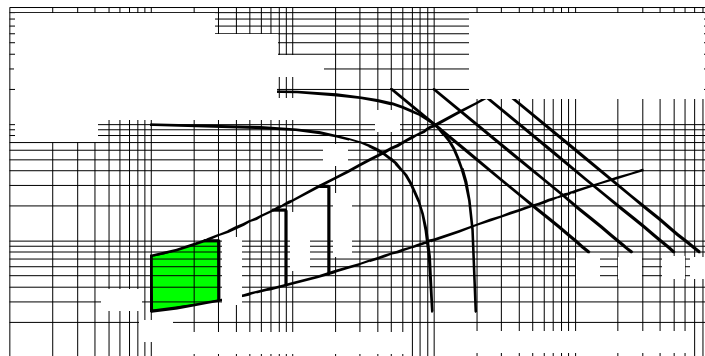


Fig. 11 - Gate Characteristics

Blocking

Parameter	ST280CH..C	Units	Conditions
dv/dt Maximum critical rate of rise of off-state voltage	500	V/μs	T <sub>J</sub> = T <sub>J</sub> max. linear to 80% rated V <sub>DRM</sub>
I <sub>DRM</sub> I <sub>RRM</sub> Max. peak reverse and off-state leakage current	75	mA	T <sub>J</sub> = T <sub>J</sub> max, rated V <sub>DRM</sub> /V <sub>RRM</sub> applied

Triggering

Parameter	ST280CH..C	Units	Conditions
P <sub>GM</sub> Maximum peak gate power	10.0	W	T <sub>J</sub> = T <sub>J</sub> max, t <sub>p</sub> ≤ 5ms
P <sub>G(AV)</sub> Maximum average gate power	2.0		T <sub>J</sub> = T <sub>J</sub> max, f = 50Hz, d% = 50
I <sub>GM</sub> Max. peak positive gate current	3.0	A	T <sub>J</sub> = T <sub>J</sub> max, t <sub>p</sub> ≤ 5ms
+V <sub>GM</sub> Maximum peak positive gate voltage	20	V	T <sub>J</sub> = T <sub>J</sub> max, t <sub>p</sub> ≤ 5ms
-V <sub>GM</sub> Maximum peak negative gate voltage	5.0		
I <sub>GT</sub> DC gate current required to trigger	TYP.	MAX.	T <sub>J</sub> = - 40°C T <sub>J</sub> = 25°C T <sub>J</sub> = 150°C Max. required gate trigger/ current/ voltage are the lowest value which will trigger all units 12V anode-to-cathode applied
	180	-	
	90	150	
V <sub>GT</sub> DC gate voltage required to trigger	2.9	-	T <sub>J</sub> = - 40°C T <sub>J</sub> = 25°C T <sub>J</sub> = 150°C
	1.8	3.0	
	1.0	-	
I <sub>GD</sub> DC gate current not to trigger	10	mA	T <sub>J</sub> = T <sub>J</sub> max Max. gate current/voltage not to trigger is the max. value which will not trigger any unit with rated V <sub>DRM</sub> anode-to-cathode applied
V <sub>GD</sub> DC gate voltage not to trigger	0.30	V	

Thermal and Mechanical Specification

Parameter	ST280CH..C	Units	Conditions
T <sub>J</sub> Max. operating temperature range	-40 to 150	°C	
T <sub>stg</sub> Max. storage temperature range	-40 to 150		
R <sub>thJ-hs</sub> Max. thermal resistance, junction to heatsink	0.17 0.08	K/W	DC operation single side cooled DC operation double side cooled
R <sub>thC-hs</sub> Max. thermal resistance, case to heatsink	0.033 0.017	K/W	DC operation single side cooled DC operation double side cooled
F Mounting force, ± 10%	4900 (500)	N (Kg)	
wt Approximate weight	50	g	
Case style	TO - 200AB (A-PUK)		See Outline Table

## ST280CH..C Series

### $\Delta R_{thJ-hs}$ Conduction

(The following table shows the increment of thermal resistance  $R_{thJ-hs}$  when devices operate at different conduction angles than DC)

Conduction angle	Sinusoidal conduction		Rectangular conduction		Units	Conditions
	Single Side	Double Side	Single Side	Double Side		
180°	0.016	0.017	0.011	0.011	K/W	$T_J = T_{J \text{ max.}}$
120°	0.019	0.019	0.019	0.019		
90°	0.024	0.024	0.026	0.026		
60°	0.035	0.035	0.036	0.037		
30°	0.060	0.060	0.060	0.061		

### Ordering Information Table

Device Code							
ST	28	0	CH	06	C	1	
①	②	③	④	⑤	⑥	⑦	⑧
<b>1</b>	- Thyristor	<b>2</b>	- Essential part number	<b>3</b>	- 0 = Converter grade	<b>4</b>	- CH = Ceramic Puk, High temperature
<b>5</b>	- Voltage code: Code x 100 = $V_{RRM}$ (See Voltage Rating Table)	<b>6</b>	- C = Puk Case TO-200AB (A-PUK)	<b>7</b>	- 0 = Eyelet terminals (Gate and Auxiliary Cathode Unsoldered Leads)	1 = Fast-on terminals (Gate and Auxiliary Cathode Unsoldered Leads)	2 = Eyelet terminals (Gate and Auxiliary Cathode Soldered Leads)
<b>7</b>	- 1 = Fast-on terminals (Gate and Auxiliary Cathode Unsoldered Leads)	<b>8</b>	- Critical dv/dt: None = 500V/ $\mu$ sec (Standard selection)	L	= 1000V/ $\mu$ sec (Special selection)	3 = Fast-on terminals (Gate and Auxiliary Cathode Soldered Leads)	

Outline Table

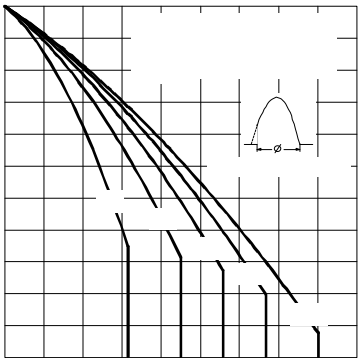
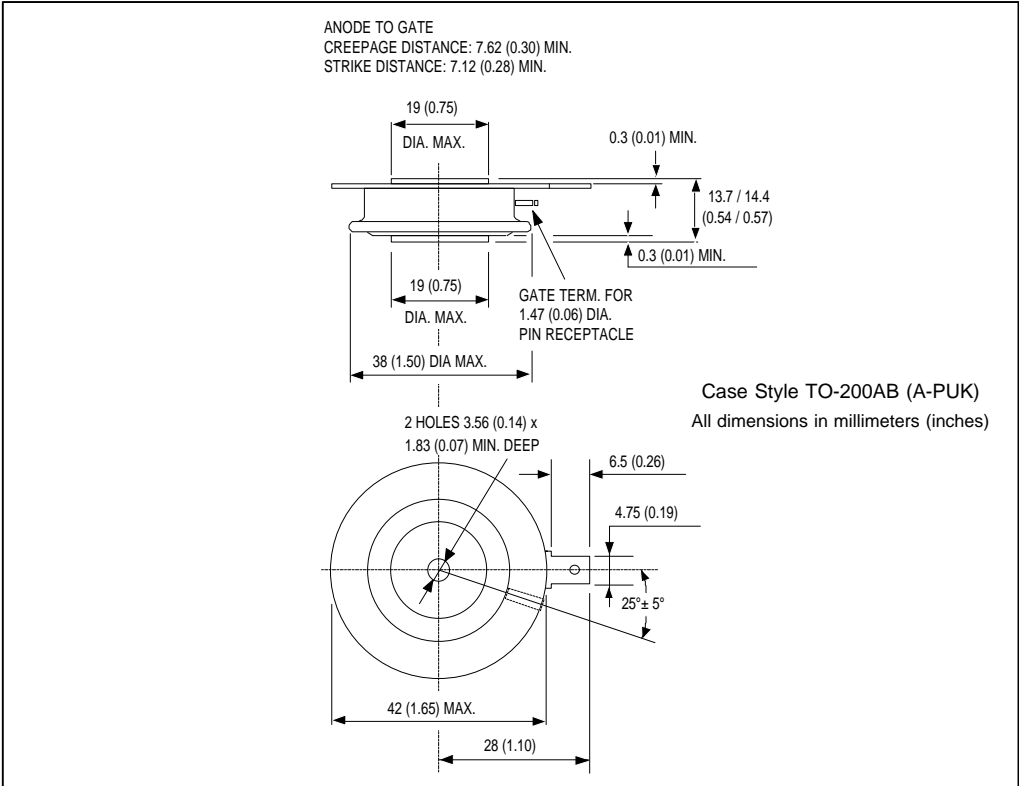


Fig. 1 - Current Ratings Characteristics

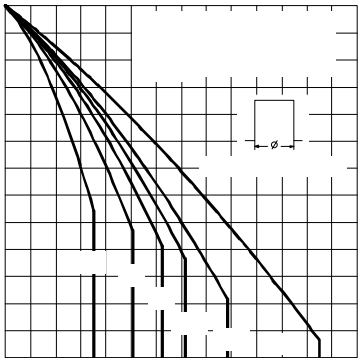


Fig. 2 - Current Ratings Characteristics