



## Dual Enhancement Mode Field Effect Transistor ( N and P Channel)

PRODUCT SUMMARY (N-Channel)		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>D(S)ON</sub> ( mΩ ) Max
30V	7.2A	25 @ V <sub>GS</sub> = 10V
		36 @ V <sub>GS</sub> = 4.5V

PRODUCT SUMMARY (P-Channel)		
V <sub>DSS</sub>	I <sub>D</sub>	R <sub>D(S)ON</sub> ( mΩ ) Max
-30V	-5.2A	48 @ V <sub>GS</sub> = -10V
		72 @ V <sub>GS</sub> = -4.5V

ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage	V <sub>DS</sub>	30	-30	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	±20	V	
Drain Current-Continuous @ T <sub>a</sub>	25°C	I <sub>D</sub>	7.2	-5.2	A
	70°C		6.1	-4.4	A
-Pulsed <sup>a</sup>	I <sub>DM</sub>		29	-20	A
Drain-Source Diode Forward Current	I <sub>S</sub>	1.7	-1.7	A	
Maximum Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	2	W	
	T <sub>a</sub> =70°C		1.44		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150		°C	

## THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient <sup>a</sup>	R <sub>θJA</sub>	62.5	°C/W
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N-Channel ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V		1		µA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
<b>ON CHARACTERISTICS <sup>b</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA	1	1.8	3.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 7A		17	25	m ohm
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A		22	36	m ohm
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> = 5V, V <sub>GS</sub> = 4.5V	20			A
Forward Transconductance	g <sub>F</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 7A		18		S
<b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz		810	950	pF
Output Capacitance	C <sub>OSS</sub>			170		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			110		pF
Gate resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1.0MHz		2.2		ohm
<b>SWITCHING CHARACTERISTICS <sup>c</sup></b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> = 15V I <sub>D</sub> = 7 A V <sub>GS</sub> = 10V R <sub>GEN</sub> = 3 ohm		12		ns
Rise Time	t <sub>r</sub>			18		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			22		ns
Fall Time	t <sub>f</sub>			12		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 15V, I <sub>D</sub> = 7A, V <sub>GS</sub> = 10V		18.5		nC
		V <sub>DS</sub> = 15V, I <sub>D</sub> = 7A, V <sub>GS</sub> = 4.5V		9.2		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = 15V, I <sub>D</sub> = 7 A V <sub>GS</sub> = 10V		2.2		nC
Gate-Drain Charge	Q <sub>gd</sub>			4.6		nC

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P-Channel ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA	-30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V		-1		µA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V		±100		nA
<b>ON CHARACTERISTICS <sup>b</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA	-1	-1.7	-3.0	V
Drain-Source On-State Resistance	R <sub>DSON</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A		40	48	m ohm
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4A		60	72	m ohm
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>DS</sub> = -5V, V <sub>GS</sub> = -10V	20			A
Forward Transconductance	g <sub>F</sub>	V <sub>DS</sub> = -5V, I <sub>D</sub> = -5A		9		S
<b>DYNAMIC CHARACTERISTICS <sup>c</sup></b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V f = 1.0MHz		700	833	pF
Output Capacitance	C <sub>OSS</sub>			175		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			115		pF
Gate resistance	R <sub>G</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1.0MHz		3		ohm
<b>SWITCHING CHARACTERISTICS <sup>c</sup></b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> = -15V R <sub>L</sub> = 3 ohm V <sub>GS</sub> = -10V R <sub>GEN</sub> = 3 ohm		10		ns
Rise Time	t <sub>r</sub>			18		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>			48		ns
Fall Time	t <sub>f</sub>			32		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DS</sub> = -15V, I <sub>D</sub> = -5A, V <sub>GS</sub> = -10V		13.8		nC
		V <sub>DS</sub> = -15V, I <sub>D</sub> = -5A, V <sub>GS</sub> = -4.5V		7.2		nC
Gate-Source Charge	Q <sub>GS</sub>	V <sub>DS</sub> = -15V, I <sub>D</sub> = -5 A V <sub>GS</sub> = -10V		1.3		nC
Gate-Drain Charge	Q <sub>GD</sub>			4.5		nC

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ELECTRICAL CHARACTERISTICS ( $T_A=25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0\text{V}, I_S = 1.7\text{A}$ $V_{GS} = 0\text{V}, I_S = -1.7\text{A}$	N-Ch		0.8 -0.79	1.2 -1.2
			P-Ch			V

## Notes

- a. Surface Mounted on FR4 Board,  $t \leq 10\text{sec}$ .
- b. Pulse Test: Pulse Width  $\leq 300\text{us}$ , Duty Cycle  $\leq 2\%$ .
- c. Guaranteed by design, not subject to production testing.

## N-Channel

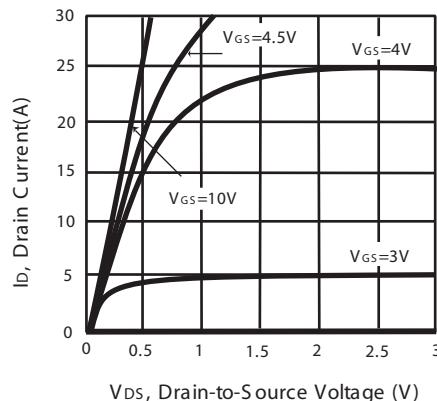


Figure 1. Output Characteristics

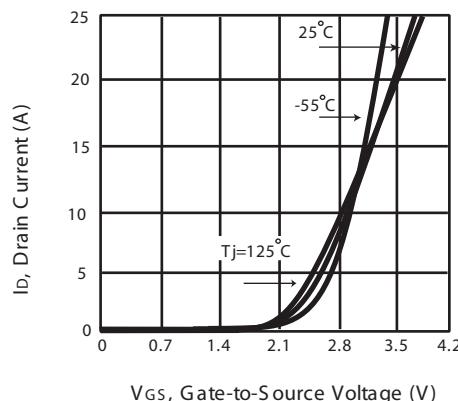


Figure 2. Transfer Characteristics

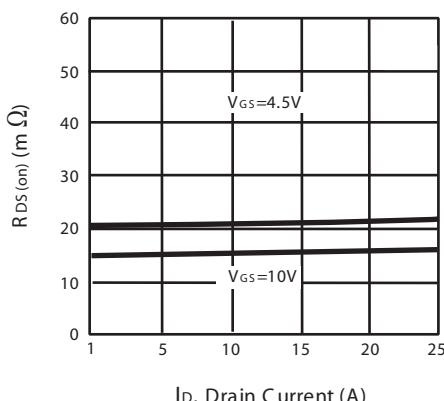


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

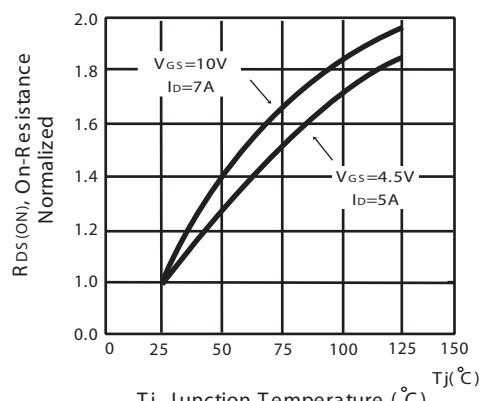
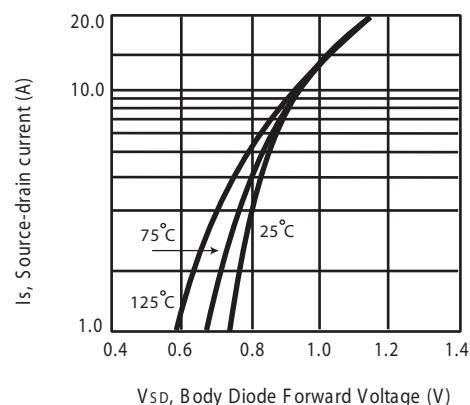
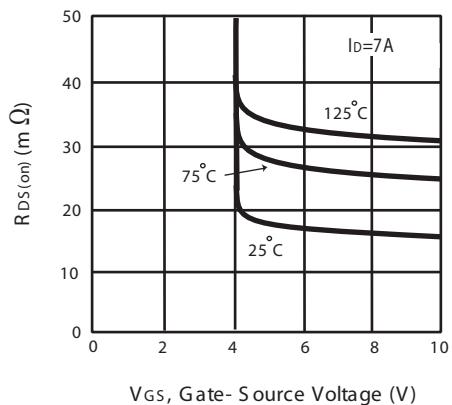
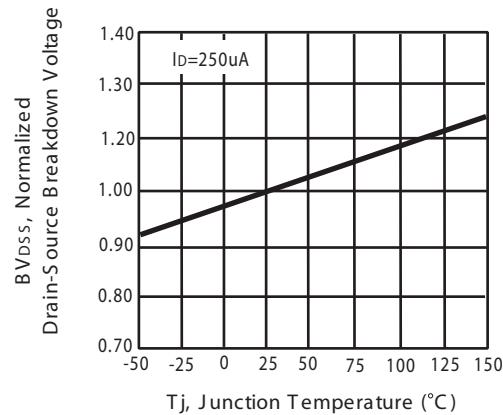
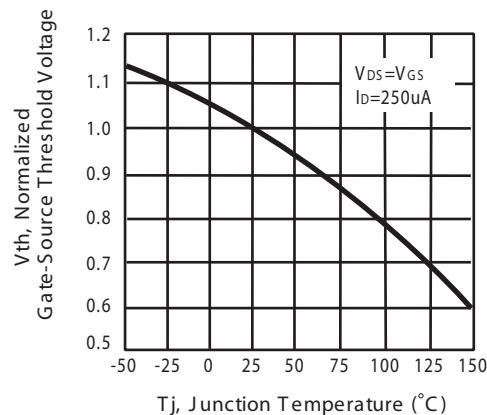


Figure 4. On-Resistance Variation with Drain Current and Temperature

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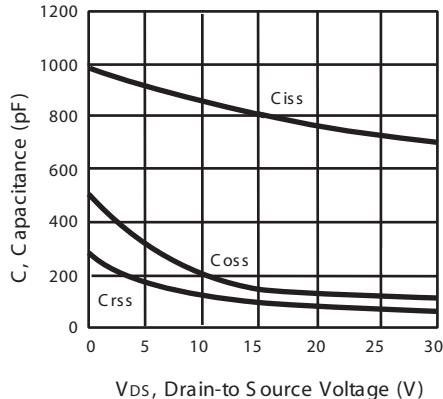


Figure 9. Capacitance

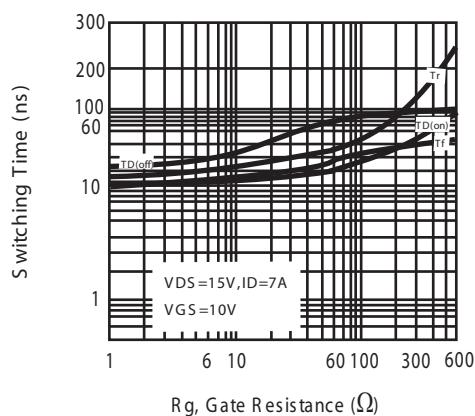
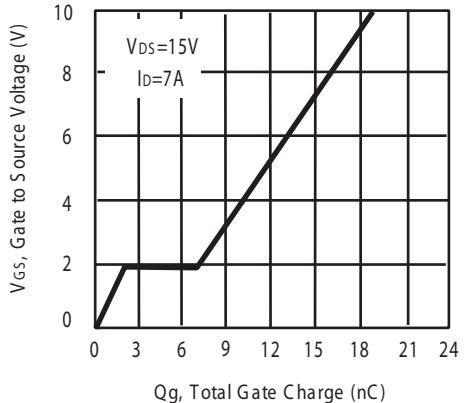


Figure 11. switching characteristics

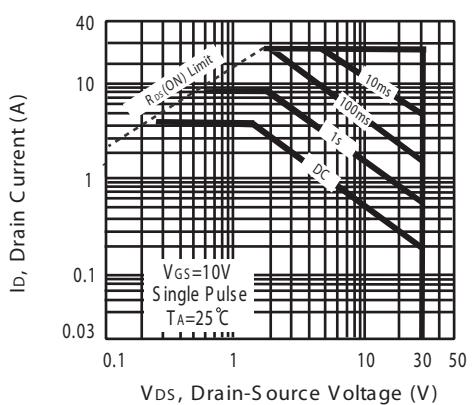


Figure 12. Maximum Safe Operating Area

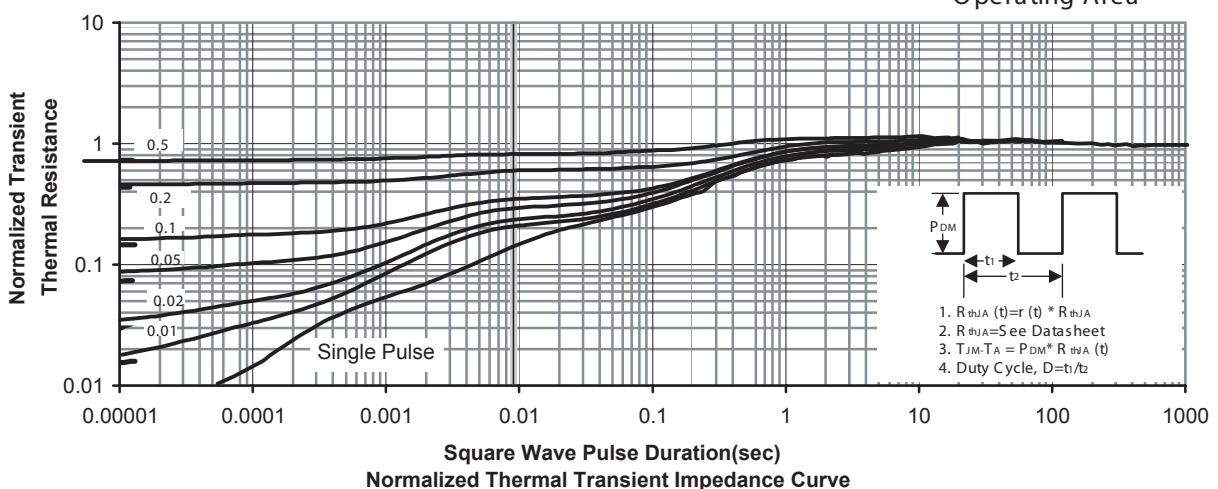


Figure 13. Normalized Thermal Transient Impedance Curve

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## P-Channel

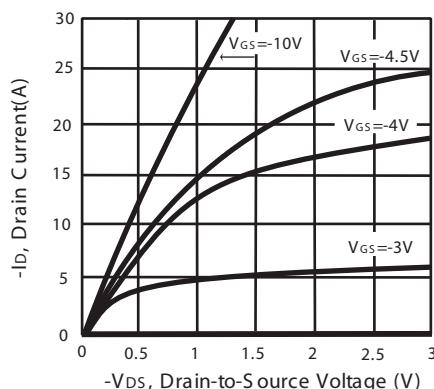


Figure 1. Output Characteristics

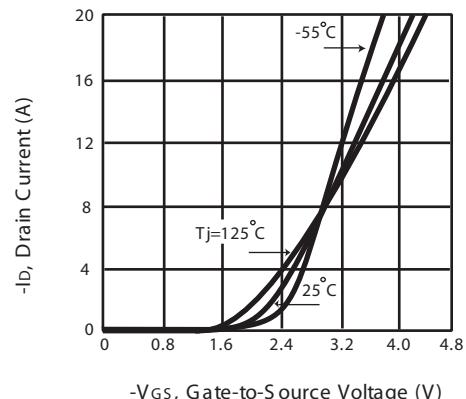


Figure 2. Transfer Characteristics

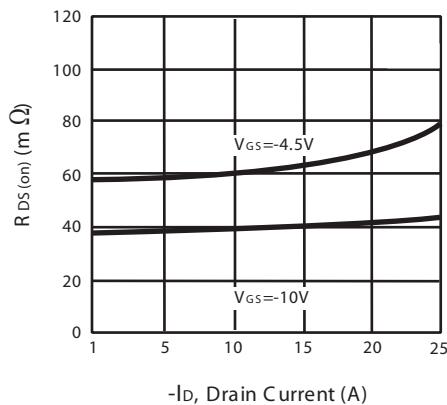


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

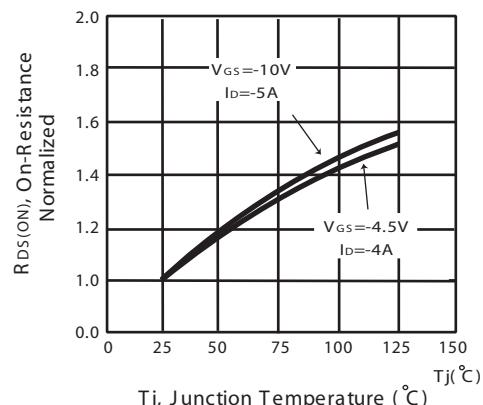


Figure 4. On-Resistance Variation with Drain Current and Temperature

# STM8358S

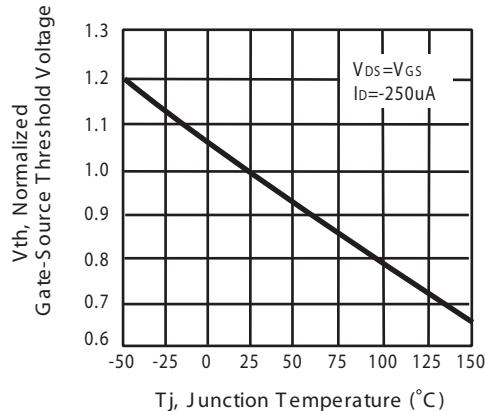


Figure 5. Gate Threshold Variation with Temperature

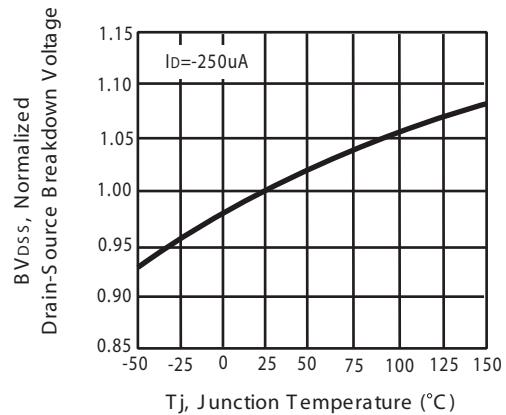


Figure 6. Breakdown Voltage Variation with Temperature

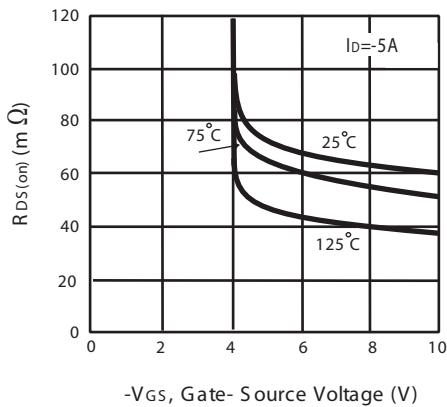


Figure 7. On-Resistance vs. Gate-Source Voltage

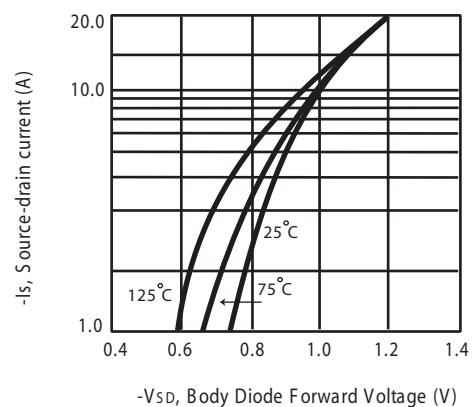


Figure 8. Body Diode Forward Voltage Variation with Source Current

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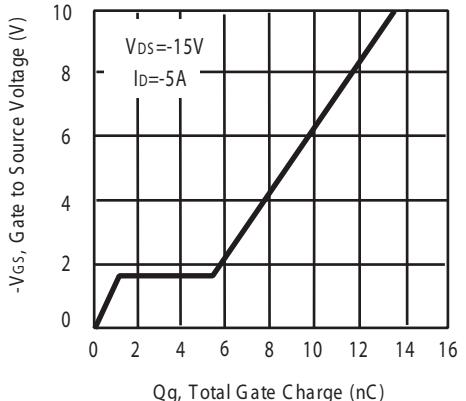
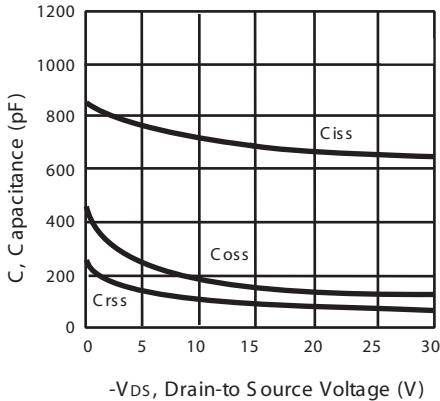


Figure 9. Capacitance

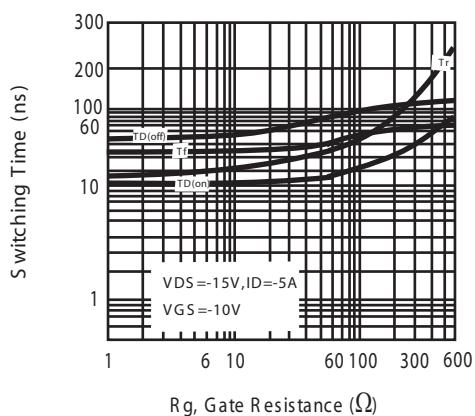


Figure 11. switching characteristics

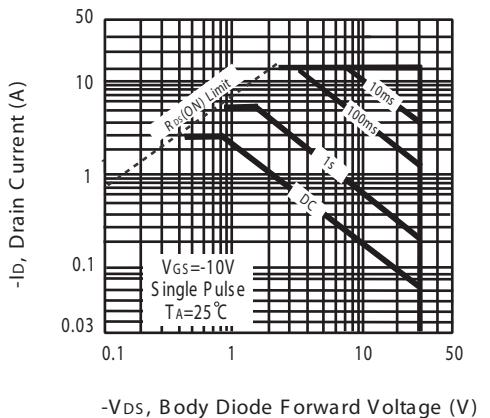


Figure 12. Maximum Safe Operating Area

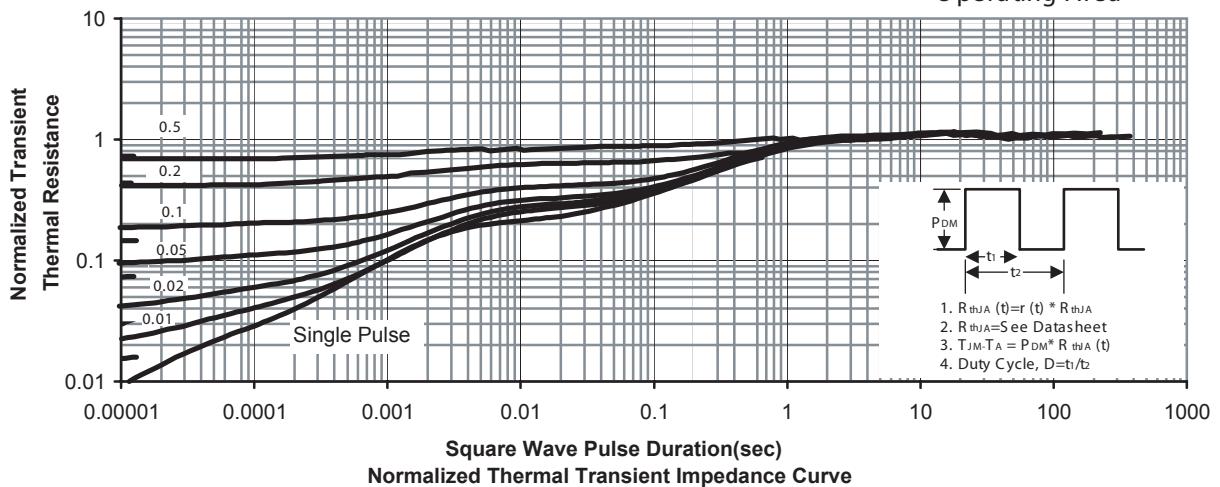
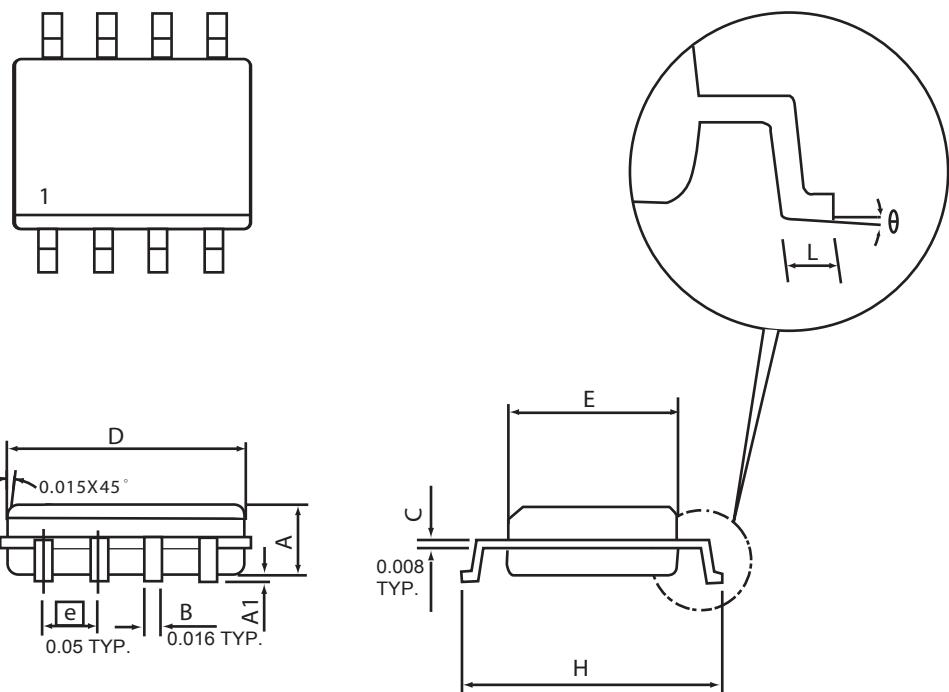


Figure 13. Normalized Thermal Transient Impedance Curve

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## PACKAGE OUTLINE DIMENSIONS

SO-8

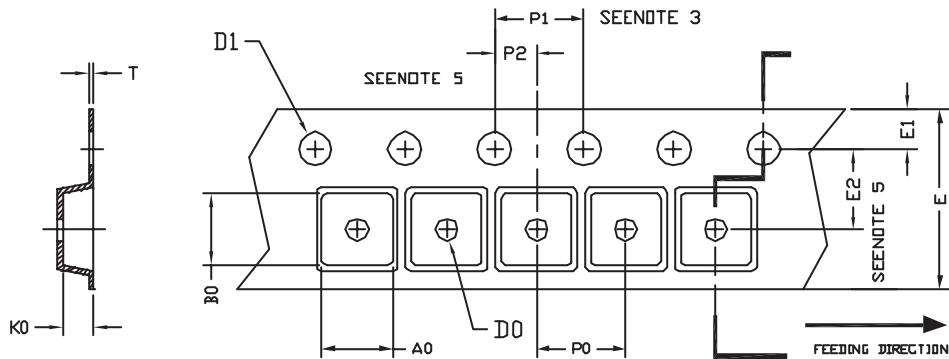


SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°

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## SO-8 Tape and Reel Data

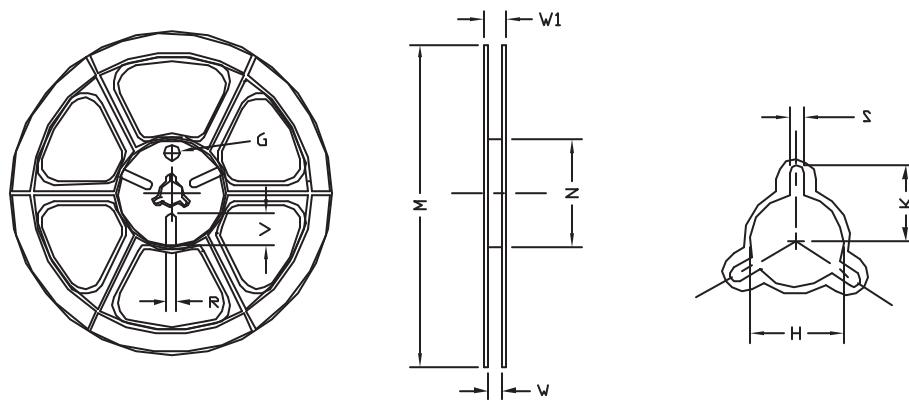
### SO-8 Carrier Tape



unit:mm

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150mil	6.40	5.20	2.10	$\phi 1.5$ (MIN)	$\phi 1.5$ $+ 0.1$ $- 0.0$	12.0 $\pm 0.3$	1.75	5.5 $\pm 0.05$	8.0	4.0	$2.0$ $\pm 0.05$	0.3 $\pm 0.05$

### SO-8 Reel



UNIT:mm

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	330 $\pm 1$	62 $\pm 1.5$	12.4 $+ 0.2$	16.8 $- 0.4$	$\phi 12.75$ $+ 0.15$	---	2.0 $\pm 0.15$	---	---	---