



N-Channel 55-V (D-S), 175°C MOSFET

PRODUCT SUMMARY			
$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)	Q_g (Typ)
55	0.006 @ $V_{GS} = 10$ V	110	65
	0.0085 @ $V_{GS} = 4.5$ V	92	

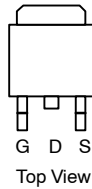
FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- New Low Thermal Resistance Package

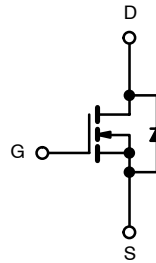
APPLICATIONS

- Automotive and Industrial

TO-263



Ordering Information: SUM110N05-06L
SUM110N05-06L—E3 (Lead (Pb)-Free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	55	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175^\circ\text{C}$)	I_D	$T_C = 25^\circ\text{C}$	A
		$T_C = 125^\circ\text{C}$	
Pulsed Drain Current	I_{DM}	240	A
Avalanche Current	I_{AR}	60	
Repetitive Avalanche Energy ^a	E_{AR}	180	
Maximum Power Dissipation	P_D	$T_C = 25^\circ\text{C}$	W
		$T_A = 25^\circ\text{C}$ ^c	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient—PCB Mount ^c	R_{thJA}	40	$^\circ\text{C/W}$
Junction-to-Case	R_{thJC}	0.95	

Notes

- Duty cycle $\leq 1\%$.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 μA	55			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		3	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 55 V, V _{GS} = 0 V			1	μA
		V _{DS} = 55 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 55 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	120			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 30 A		0.0047	0.006	Ω
		V _{GS} = 4.5 V, I _D = 20 A		0.0066	0.0085	
		V _{GS} = 10 V, I _D = 30 A, T _J = 125 °C			0.0102	
		V _{GS} = 10 V, I _D = 30 A, T _J = 175 °C			0.0132	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 30 A	30			S
Dynamic^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		3300		pF
Output Capacitance	C _{oss}			625		
Reverse Transfer Capacitance	C _{rss}			310		
Total Gate Charge ^c	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 110 A		65	100	nC
Gate-Source Charge ^c	Q _{gs}			15		
Gate-Drain Charge ^c	Q _{gd}			16		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 0.27 Ω I _D ≅ 110 A, V _{GEN} = 10 V, R _g = 2.5 Ω		15	25	ns
Rise Time ^c	t _r			15	25	
Turn-Off Delay Time ^c	t _{d(off)}			35	55	
Fall Time ^c	t _f			15	25	
Source-drain Diode Ratings and Characteristics (T_c = 25 °C)^b						
Continuous Current	I _S				110	A
Pulsed Current	I _{SM}				240	
Forward Voltage ^a	V _{SD}	I _F = 110 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 110 A, di/dt = 100 A/μs		70	125	ns
Peak Reverse Recovery Current	I _{RM(REC)}			2.5	5	A
Reverse Recovery Charge	Q _{rr}			0.09	0.31	μC

Notes

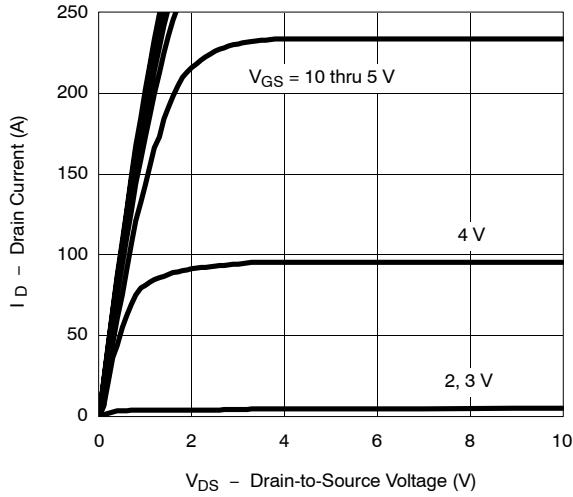
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

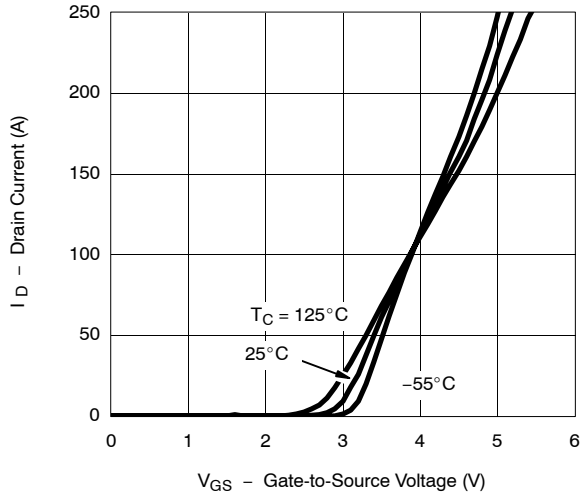


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

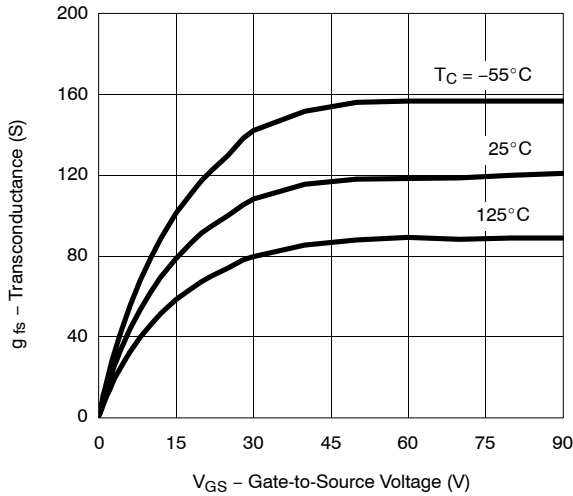
Output Characteristics



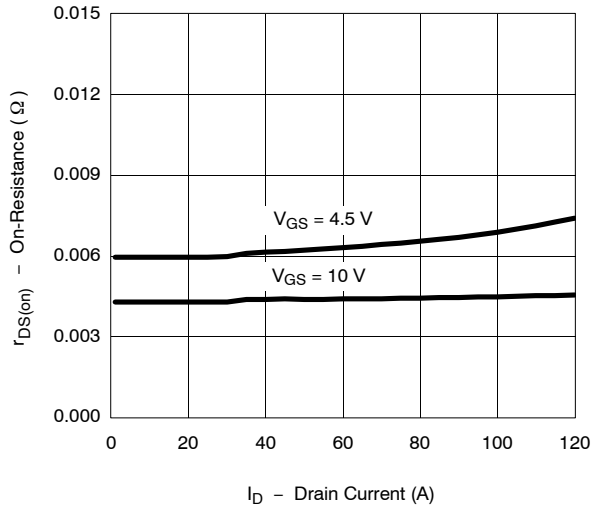
Transfer Characteristics



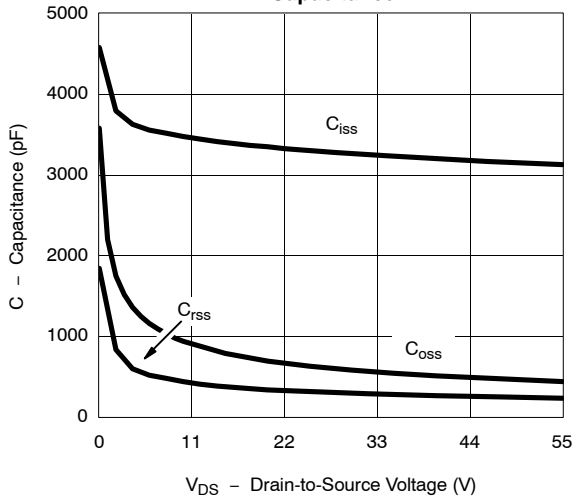
Transconductance



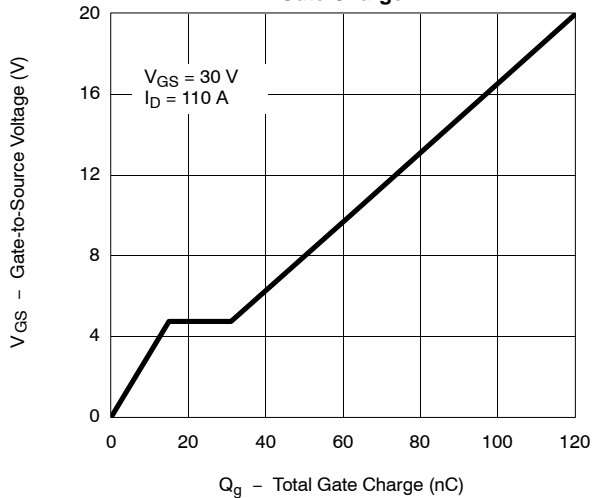
On-Resistance vs. Drain Current



Capacitance

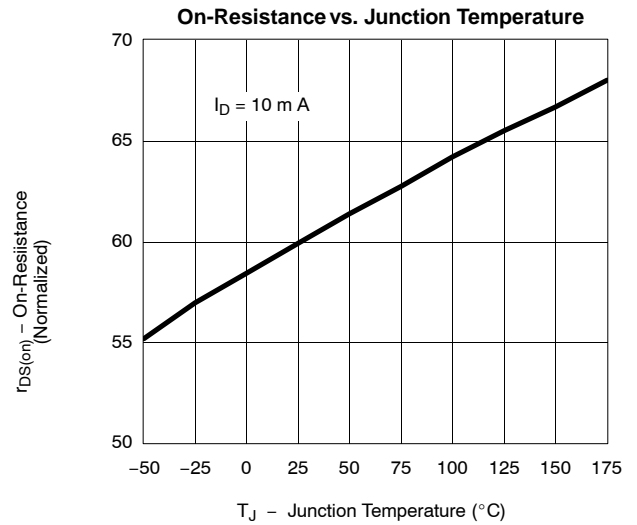
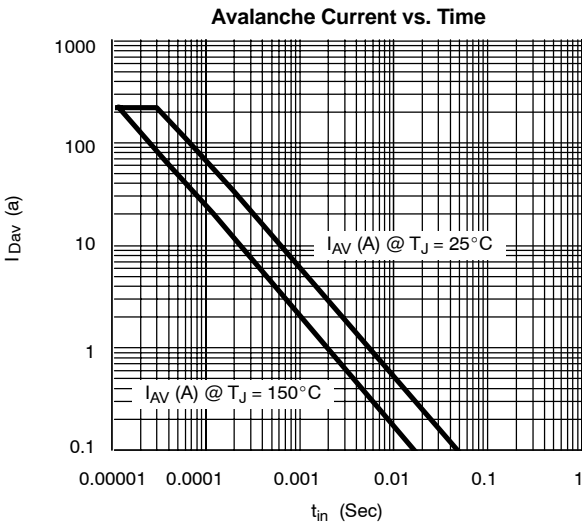
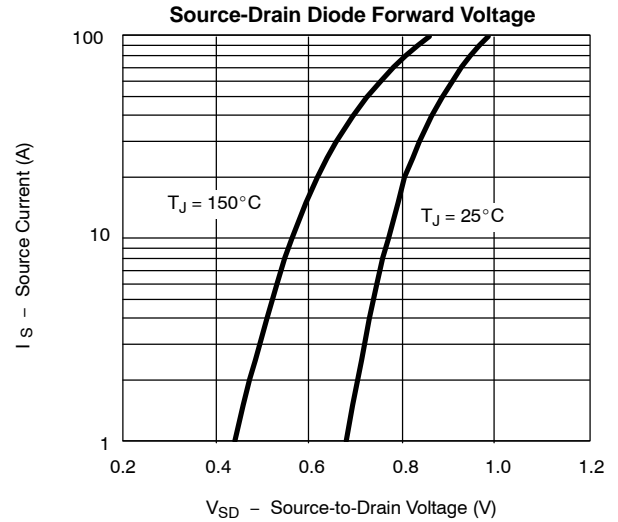
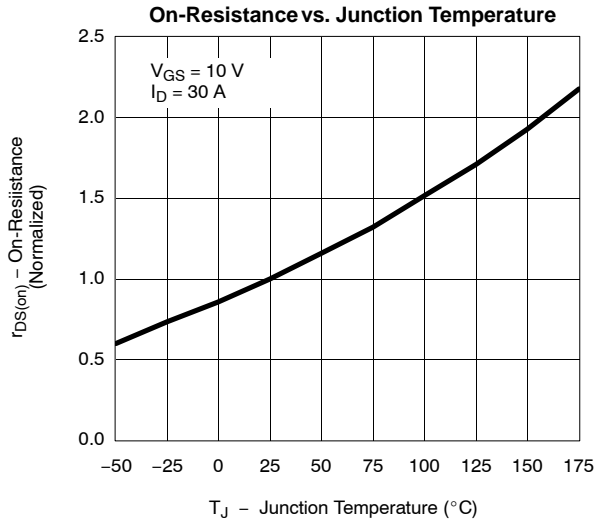


Gate Charge





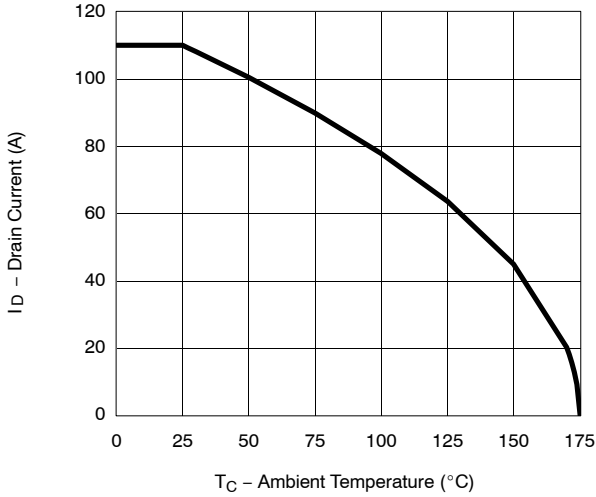
TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



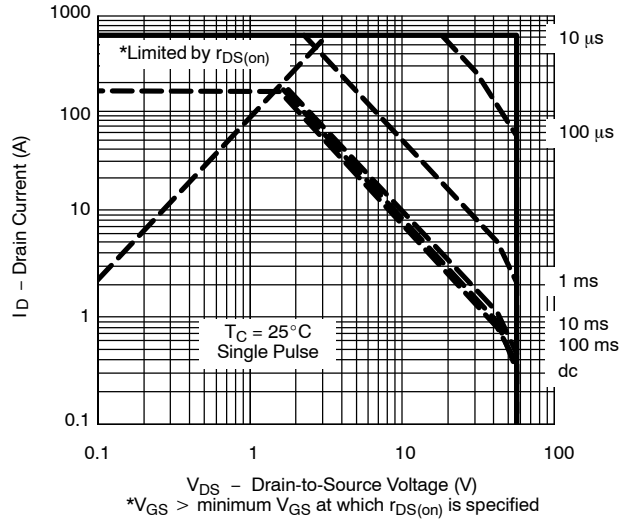


THERMAL RATINGS

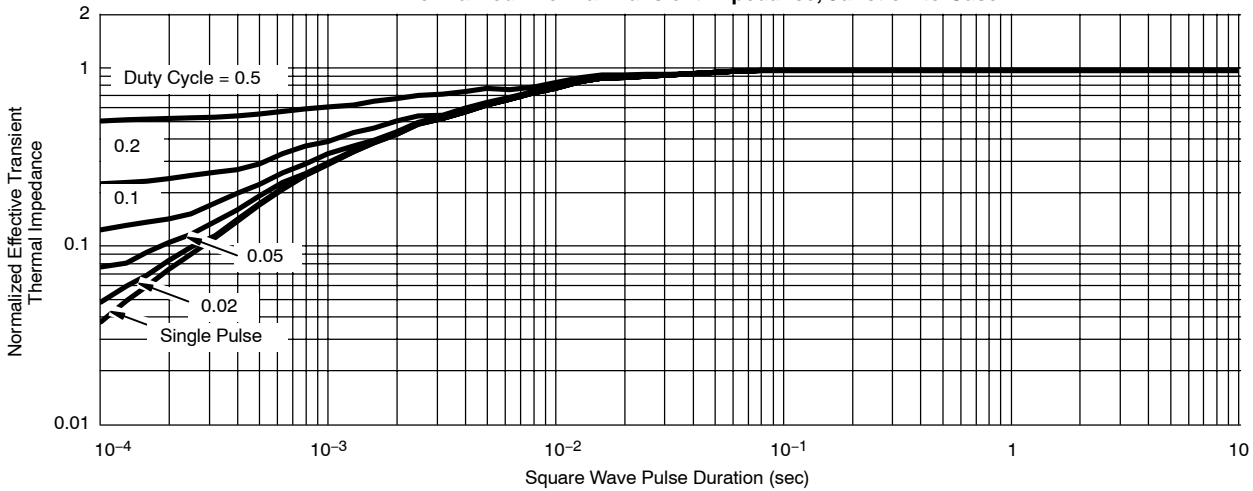
Maximum Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?72005>.