### TOSHIBA PHOTOCOUPLER PHOTO RELAY

## **TLP3230**

# MEASUREMENT INSTRUMENTS LOGIC IC TESTERS / MEMORY TESTERS BOARD TESTERS / SCANNERS

The TOSHIBA TLP3230 is a super small-outline photorelay, suitable for surface-mount assembly. The TLP3230 consists of a GaAs infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Its characteristics also include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measuring instruments.

### **FEATURES**

• 4 pin SSOP (SSOP4) : 1.8 mm high, 1.27 mm pitch

• 1-Form-A

Peak Off-State Voltage : 20 V (MIN.)
 Trigger LED Current : 4 mA (MAX.)
 On-State Current : 160 mA (MAX.)
 On-State Resistance : 8 (MAX.), 5 (TYP.)

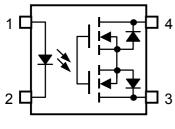
• Output Capacitance : 2.5 pF (MAX.), 1.0 pF (TYP.)

• Isolation Voltage : 1500 Vrms (MIN.)

# JEDEC EIAJ TOSHIBA 11-2A1

Weight: 0.03 g

### **PIN CONFIGURATION (TOP VIEW)**



- 1: ANODE
- 2: CATHODE
- 3: DRAIN
- 4 : DRAIN

2004-1-8

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
	Forward Current	lF	50	mA	
Ω	Forward Current Derating (Ta ≥ 25°C)	ΔI <sub>F</sub> /°C	-0.5	mA/°C	
当	Reverse Voltage	$V_{R}$	5	V	
	Junction Temperature	Tj	125	°C	
<u>~</u>	Off-State Output Terminal Voltage	V <sub>OFF</sub>	20	V	
DETECTOR	On-State Current	I <sub>ON</sub>	160	mA	
	On-State Current Derating (Ta ≥ 25°C)	Δl <sub>ON</sub> /°C	-1.6	mA/°C	
	Junction Temperature		125	°C	
Storage Temperature Range		T <sub>stg</sub>	<b>−40~125</b>	°C	
Operating Temperature Range		T <sub>opr</sub>	-20~85	°C	
Lead Soldering Temperature (10 s)		T <sub>sol</sub>	260	°C	
Isolat	tion Voltage (AC, 1 minute, R.H. $\leq$ 60%) (NOTE1)	BV <sub>S</sub>	1500	Vrms	

(NOTE1): Device considered a two-terminal device: Pins 1 and, 2 shorted together, and pins 3 and 4 shorted together.

### Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

This device is applying super small package which is free for Moisture-Proof packing. However, the application of this device is premised on use under controlled environmental condition like as measuring instrument. It is necessary to take precautions of storage condition and operating environmental condition.

### RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	$V_{DD}$	_	_	20	V
Forward Current	I <sub>F</sub>	10	_	30	mA
On-State Current	I <sub>ON</sub>	_	_	160	mA
Operating Temperature	T <sub>opr</sub>	25	_	60	°C

### INDIVIDUAL ELECTRICAL CHARACTERISTICS (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μА
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	15	_	pF
DETECTOR	Off-State Current	l <sub>OFF</sub>	V <sub>OFF</sub> = 20 V, Ta = 50°C	_	_	1000	pА
DETE	Capacitance	C <sub>OFF</sub>	V = 0, f = 100 MHz, t < 1 s	_	1.0	2.5	pF

### **COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Trigger LED Current	I <sub>FT</sub>	I <sub>ON</sub> = 100 mA	_	_	4	mA
Return LED Current	I <sub>FC</sub>	I <sub>OFF</sub> = 10 μA	0.2	0.75	_	mA
On-State Resistance	R <sub>ON</sub>	$I_{ON} = 160 \text{ mA}, I_F = 5 \text{ mA}, t < 1 \text{ s}$	_	5	8	Ω

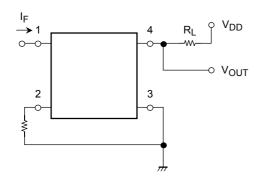
### **ISOLATION CHARACTERISTICS (Ta = 25°C)**

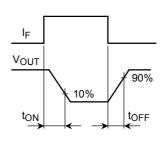
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Capacitance Input to Output	CS	V <sub>S</sub> = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation Resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≦ 60%	5 × 10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation Voltage	$BV_S$	AC, 1 second (in oil)	_	3000	_	VIIIIS
		DC, 1 minute (in oil)	_	3000	_	Vdc

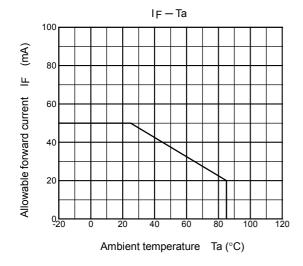
### **SWITCHING CHARACTERISTICS (Ta = 25°C)**

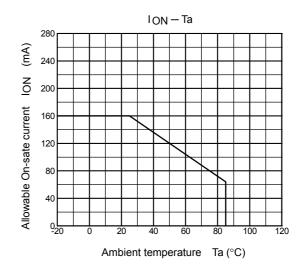
CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Turn-on Time	t <sub>ON</sub>	$R_L = 200 \Omega$	(NOTE 4)		60	500	116
Turn-off Time	tOFF	$V_{DD} = 10 \text{ V}, I_F = 5 \text{ mA}$		_	120	500	μs

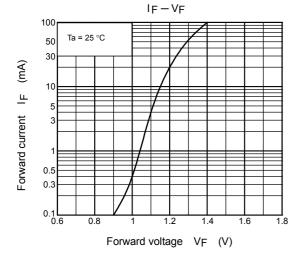
(NOTE 4): SWITCHING TIME TEST CIRCUIT

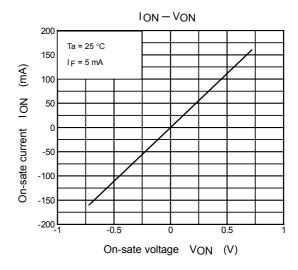


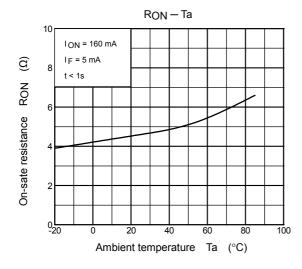


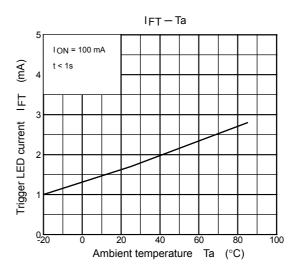


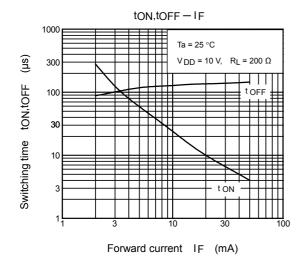


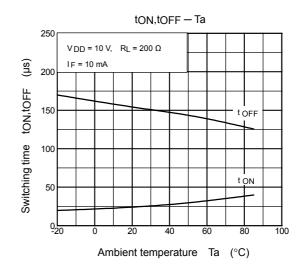


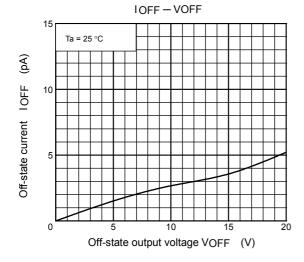


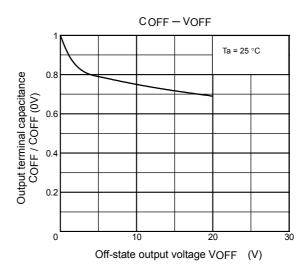




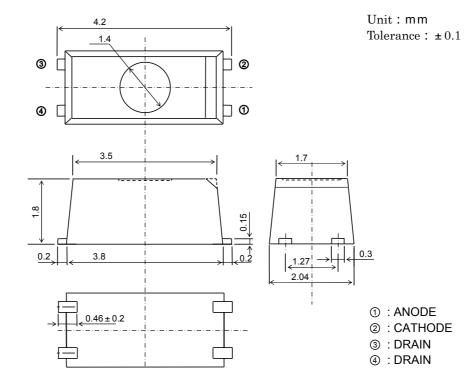








### **OUTLINE DRAWING**



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