+ TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra High speed U-MOSIII)

TPCM8001-H

High Speed and High Efficiency DC-DC Converters Notebook PC Applications Portable Equipment Applications

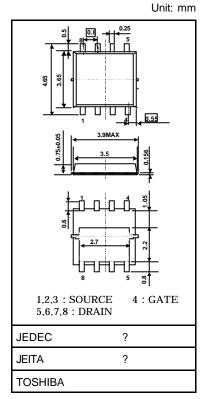
- · Small footprint due to small and thin package
- High speed switching
- Small gate charge: Qg = 19 nC (typ.)
- Low drain-source ON resistance: RDS (ON) = 7 mO (typ.)
- High forward transfer admittance: | Yfs | =36 S (typ.)
- Low leakage current: $IDSS = 10 \mu A \text{ (max) (VDS} = 30 \text{ V)}$
- Enhancement mode: $V_{th} = 1.1$ to 2.3 V ($V_{DS} = 10$ V, $I_{D} = 1$ mA)

Maximum Ratings (Ta = 25°C)

Characte	ristics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	30	V	
Drain-gate voltage (R	$GS = 20 \text{ k}\Omega$)	V_{DGR}	30	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	20	Α	
Diam current	Pulsed (Note 1)	I_{DP}	60	A	
Drain power dissipati	on (Tc=25)	P_{D}	TBD	W	
Drain power dissipati	on (t = 10 s) (Note 2a)	P_{D}	TBD	W	
Drain power dissipati	on $(t = 10 s)$ (Note 2b)	P_{D}	TBD	W	
Single pulse avalanc	he energy (Note 3)	E _{AS}	TBD	mJ	
Avalanche current		I _{AR}	TBD	Α	
Repetitive avalanche	energy c=25) (Note 4)	E _{AR}	TBD	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature	range	T _{stg}	–55 to 150	°C	

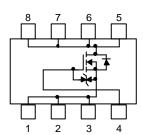
Note: For (Note 1), (Note 2), (Note 3), (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: g (typ.)

Circuit Configuration

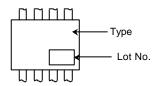




Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25)	R _{th (ch-c)}	TBD	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	TBD	°C/W
Thermal resistance, channel to ambient $(t=10\;\text{s}) \tag{Note 2b}$	R _{th (ch-a)}	TBD	°C/W

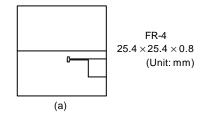
Marking (Note 5)

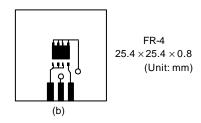


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)

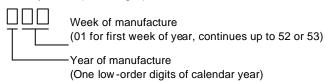




Note 3: V $_{DD}$ = 24 V , T $_{ch}$ = 25 $^{\circ}C$ (initial) , L = $\,$ mH , R $_{G}$ = 25 Ω , I $_{AR}$ = $\,$ A

Note 4: Repetitive rating: pulse width limited by max channel temperature

Note 5: * Weekly code: (Three digits)



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Electrical Characteristics (Ta = 25°C)

Cha	Characteristics		Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Drain cut-OFF cu	ırrent	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	10	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V
Diam-source bre	ardown voltage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15 — —		·	
Gate threshold v	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.1	_	2.3	V
Drain-source ON	resistance	R _{DS} (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$	_	10	14	mΩ
Drain-source ON resistance		TYDS (ON)	$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$	_	7	9.5	11122
Forward transfer	Forward transfer admittance		$V_{DS} = 10 \text{ V}, I_{D} = 10 \text{ A}$	18	36	_	S
Input capacitance	Э	C _{iss}		_	1130	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	120	_	pF
Output capacitance		C _{oss}]	_	480	_	
Switching time	Rise time	t _r	VGS 0 V	_	2.5	_	
	Turn-ON time	t _{on}		_	9	_	
	Fall time	tr		_	3	_	ns
	Turn-OFF time	t _{off}	$V_{DD} \simeq 15 \text{ V}$ Duty $\leq 1\%$, $t_W = 10 \mu\text{s}$	_	19	_	
Total gate charge	Total gate charge (gate-source plus gate-drain)		$V_{DD} \simeq 24 \ V, \ V_{GS} = 10 \ V, \ I_D = 20 \ A$	_	19	_	
			$V_{DD} \simeq 24 \ V, \ V_{GS} = 5 \ V, \ I_D = 20 \ A$	_	11	_	
Gate-source charge 1		Q _{gs1}		_	3.9	_	nC
Gate-drain ("miller") charge		Q _{gd}	$V_{DD} \simeq 24 \ V, \ V_{GS} = 10 \ V, \ I_D = 20 \ A$	_	4.0	_	
Gate switch char	Gate switch charge]	_	6.0	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I _{DRP}	_	_	_	60	Α
Forward voltage (diode)			V_{DSF}	$I_{DR} = 20 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.2	V

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