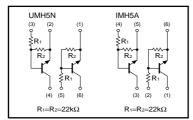
General purpose (dual digital transistors) **UMH5N / IMH5A**

Features

1) Two DTC124E chips in a EMT or UMT or SMT package.

Circuit schematic



● Absolute maximum ratings (Ta = 25°C)

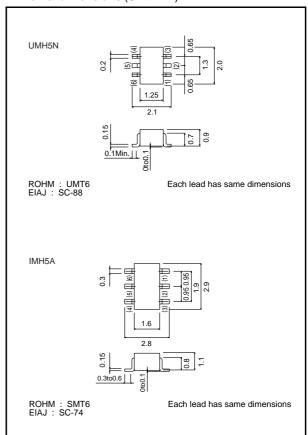
| Parameter | | Symbol | Limits | Unit | |
|----------------------|-------|---------|-------------|-------|--|
| Supply voltage | | Vcc | 50 | V | |
| Input voltage | | Vin | 40 | V | |
| | | VIN | -10 | 1 ° | |
| Output current | | lo | 30 | mA | |
| Collector current | | Ic(MAX) | 100 | mA | |
| Power dissipation | UMH5N | Pd | 150(TOTAL) | mW *1 | |
| | IMH5A | l Fu | 300(TOTAL) | *2 | |
| Junction temperature | | Tj | 150 | °C | |
| Storage temperature | | Tstg | -55 to +150 | °C | |

 ^{1 120}mW per element must not be exceeded.
 2 200mW per element must not be exceeded.

● Package, marking, and packaging specifications

| Туре | UMH5N | IMH5A |
|------------------------------|-------|-------|
| Package | UMT6 | SMT6 |
| Marking | H5 | H5 |
| Code | TR | T110 |
| Basic ordering unit (pieces) | 3000 | 3000 |
| | | |

●External dimensions (Unit: mm)



• Electrical characteristics (Ta = 25° C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions | |
|----------------------|----------------|------|------|------|------|------------------------------|---|
| Input voltage | VI (off) | - | - | 0.5 | ٧ | Vcc=5V, Io=100μA | |
| | VI (on) | 3 | - | - | | Vo=0.2V, Io=5mA | |
| Output voltage | Vo (on) | - | 0.1 | 0.3 | V | lo=10mA, li=0.5mA | |
| Input current | lı . | - | - | 0.36 | mA | V⊫5V | |
| Output current | IO (off) | - | - | 0.5 | μΑ | Vcc=50V, Vi=0V | |
| DC current gain | Gı | 56 | - | - | - | Vo=5V, Io=5mA | |
| Transition frequency | f⊤ | - | 250 | - | MHz | Vce=10V, Ie= -5mA , f=100MHz | * |
| Input resistance | R ₁ | 15.4 | 22 | 28.6 | kΩ | _ | |
| Resistance ratio | R2/R1 | 0.8 | 1 | 1.2 | - | = | |

^{*} Characteristics of built-in transistor

•Electrical characteristics curves

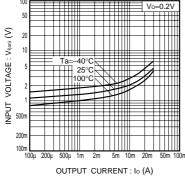


Fig.1 Input voltage vs. output current (ON characteristics)

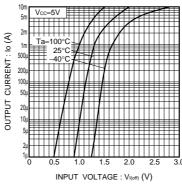


Fig.2 Output current vs. input voltage (OFF characteristics)

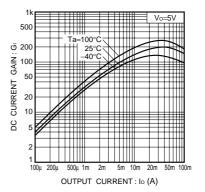


Fig.3 DC current gain vs. output current

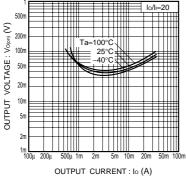


Fig.4 Output voltage vs. output current

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