

SWITCHING REGULATOR APPLICATIONS

**Features**

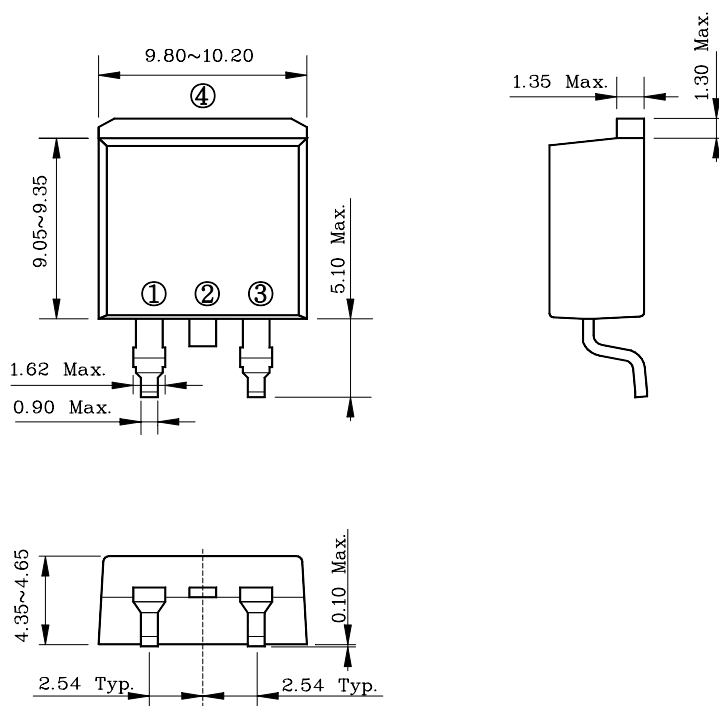
- **High Voltage:**  $BV_{DSS}=300V(\text{Min.})$
- **Low  $C_{rSS}$  :**  $C_{rSS}=21pF(\text{Typ.})$
- **Low gate charge :**  $Q_g=31nc(\text{Typ.})$
- **Low  $R_{DS(on)}$  :**  $R_{DS(on)}=0.32\Omega(\text{Typ.})$

**Ordering Information**

Type NO.	Marking	Package Code
STK1430DI	STK1430	D2-PAK

**Outline Dimensions**

unit : mm



**PIN Connections**

1. Gate
2. Drain
3. Source
4. Drain

## Absolute maximum ratings

(Tc=25°C)

Characteristic	Symbol	Rating	Unit	
Drain-source voltage	$V_{DSS}$	300	V	
Gate-source voltage	$V_{GSS}$	±30	V	
Drain current (DC)	$I_D$	(Tc=25°C)	14	A
		(Tc=100°C)	14	A
Drain current (Pulsed) *	$I_{DM}$	56	A	
Drain power dissipation	$P_D$	140	W	
Avalanche current (Single) ②	$I_{AS}$	14	A	
Single pulsed avalanche energy ②	$E_{AS}$	715	mJ	
Avalanche current (Repetitive) ①	$I_{AR}$	14	A	
Repetitive avalanche energy ①	$E_{AR}$	18	mJ	
Junction temperature	$T_J$	150	°C	
Storage temperature range	$T_{stg}$	-55~150		

\* Limited by maximum junction temperature

Characteristic		Symbol	Typ.	Max	Unit
Thermal resistance	Junction-case	$R_{th(J-C)}$	-	0.89	°C/W
	Junction-ambient	$R_{th(J-a)}$	-	62.5	

## Electrical Characteristics

(Tc=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Drain-source breakdown voltage	$BV_{DSS}$	$I_D=250\mu A, V_{GS}=0$	300	-	-	V	
Gate threshold voltage	$V_{GS(th)}$	$I_D=250\mu A, V_{DS}=V_{GS}$	3.0	-	5.0	V	
Drain-source cut-off current	$I_{DSS}$	$V_{DS}=300V, V_{GS}=0V$	-	-	1	$\mu A$	
Gate leakage current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 30V$	-	-	$\pm 100$	nA	
Drain-source on-resistance ④	$R_{DS(on)}$	$V_{GS}=10V, I_D=7A$	-	0.32	0.48	$\Omega$	
Forward transfer conductance ④	$g_{fs}$	$V_{DS}=5V, I_D=7A$	-	7.8	-	S	
Input capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=25V$ $f=1MHz$	-	937	1405	pF	
Output capacitance	$C_{oss}$		-	133	200		
Reverse transfer capacitance	$C_{rss}$		-	21	32		
Turn-on delay time	$t_{d(on)}$	$V_{DD}=150V, I_D=14A$ $R_G=25\Omega$	-	22	33	ns	
Rise time	$t_r$		-	145	217		
Turn-off delay time	$t_{d(off)}$		③④	-	45		68
Fall time	$t_f$		-	70	105		
Total gate charge	$Q_g$	$V_{DS}=150V, V_{GS}=10V$ $I_D=14A$	-	31	47	nC	
Gate-source charge	$Q_{gs}$		③④	-	7.5		11.3
Gate-drain charge	$Q_{gd}$		-	12.5	19		

## Source-Drain Diode Ratings and Characteristics

(Tc=25°C)

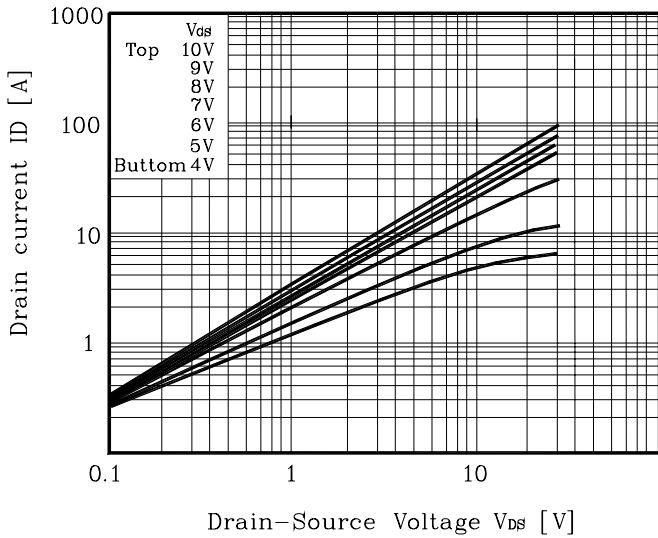
Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Source current (DC)	$I_S$	Integral reverse diode in the MOSFET	-	-	14	A
Source current (Pulsed) ①	$I_{SM}$		-	-	56	
Forward voltage ④	$V_{SD}$	$V_{GS}=0V, I_S=14A$	-	-	1.4	V
Reverse recovery time	$t_{rr}$	$I_S=14A, V_{GS}=0,$ $di_S/dt=100A/\mu s$	-	270	-	ns
Reverse recovery charge	$Q_{rr}$		-	1.8	-	$\mu C$

Note ;

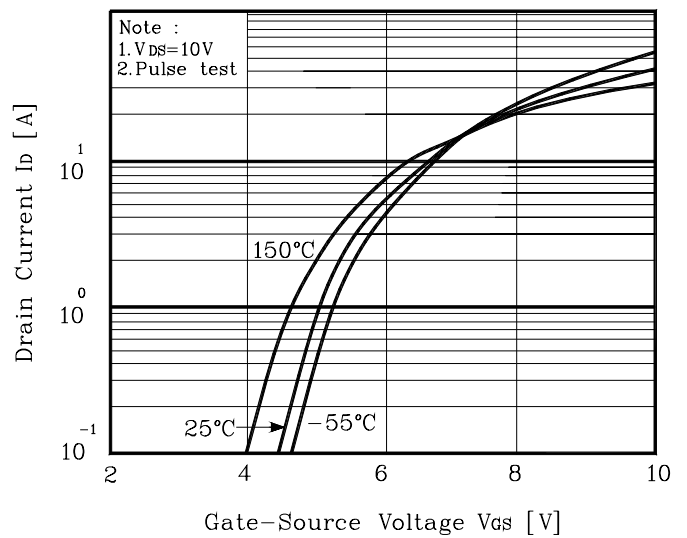
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ②  $L=5mH, I_{AS}=14A, V_{DD}=50V, R_G=25\Omega$
- ③ Pulse Test : Pulse Width < 300us, Duty cycle  $\leq 2\%$
- ④ Essentially independent of operating temperature

## Electrical Characteristic Curves

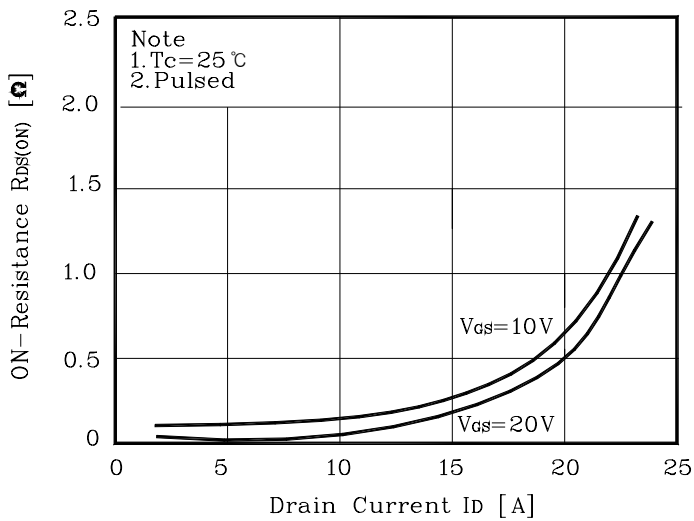
**Fig. 1  $I_D - V_{DS}$**



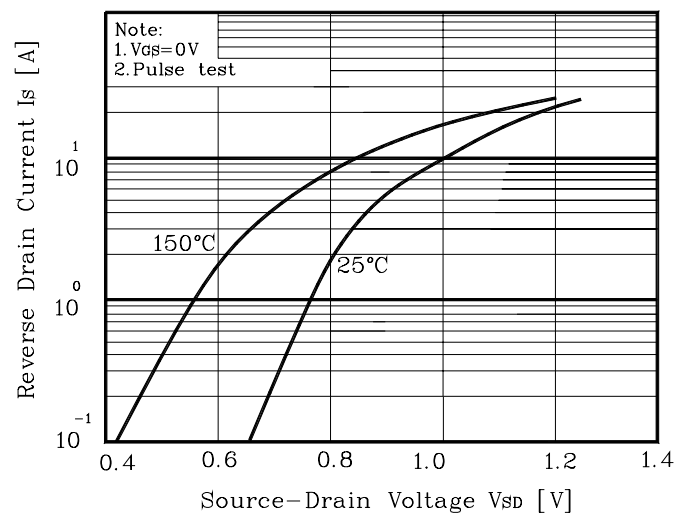
**Fig. 2  $I_D - V_{GS}$**



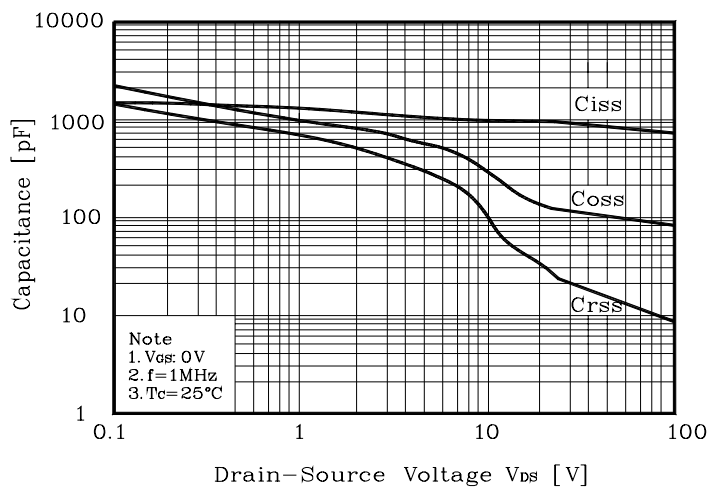
**Fig. 3  $R_{DS(on)} - I_D$**



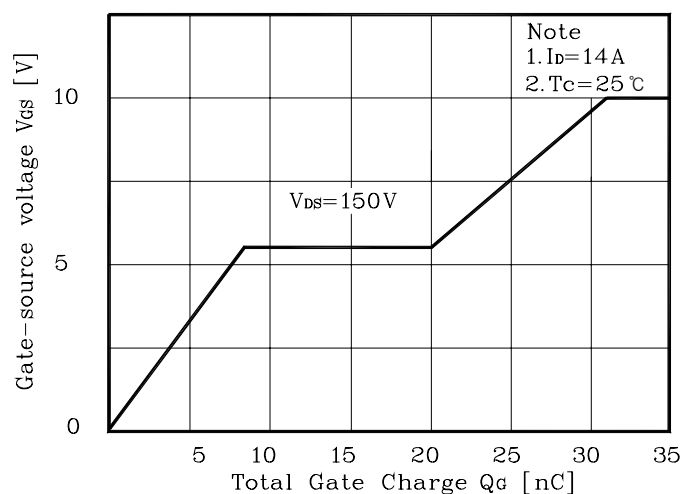
**Fig. 4  $I_S - V_{SD}$**



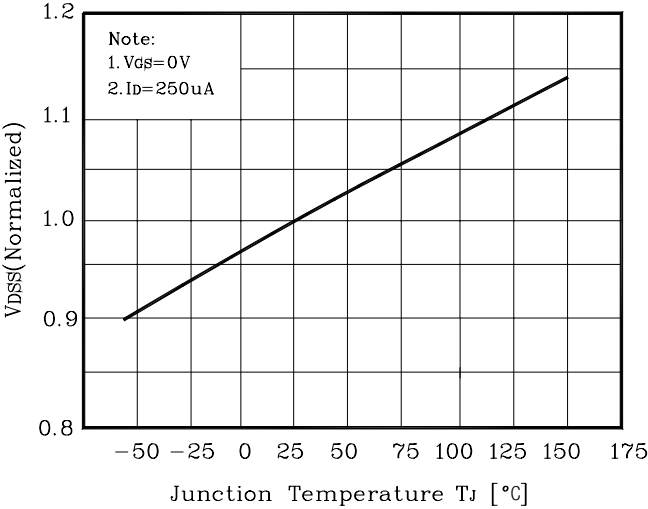
**Fig. 5 Capacitance -  $V_{DS}$**



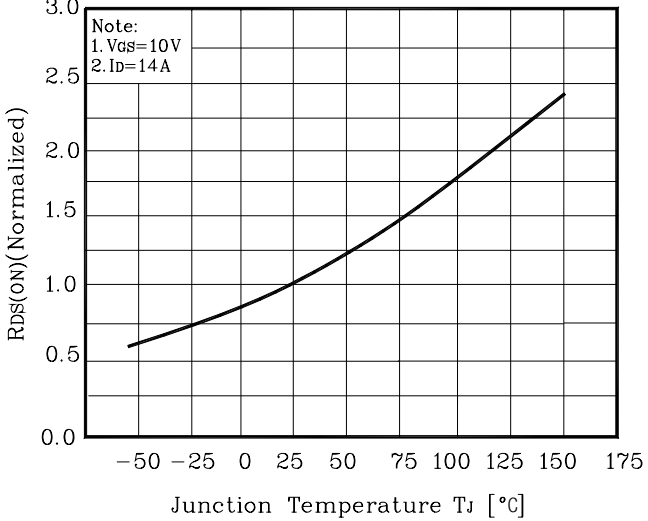
**Fig. 6  $V_{GS} - Q_G$**



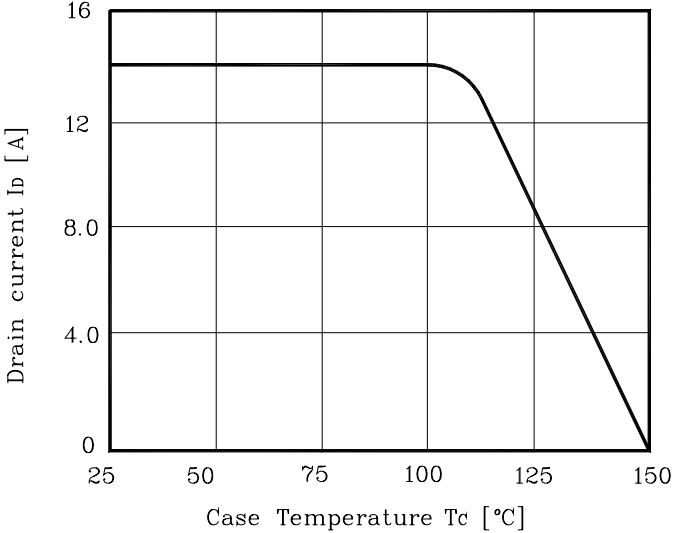
**Fig. 7  $V_{DSS} - T_J$**



**Fig. 8  $R_{DS(on)} - T_J$**



**Fig. 9  $I_D - T_C$**



**Fig. 10 Safe Operating Area**

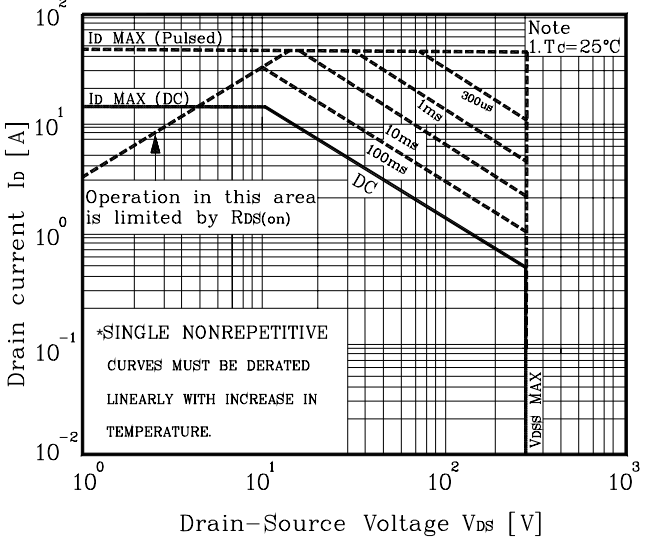


Fig. 11 Gate Charge Test Circuit & Waveform

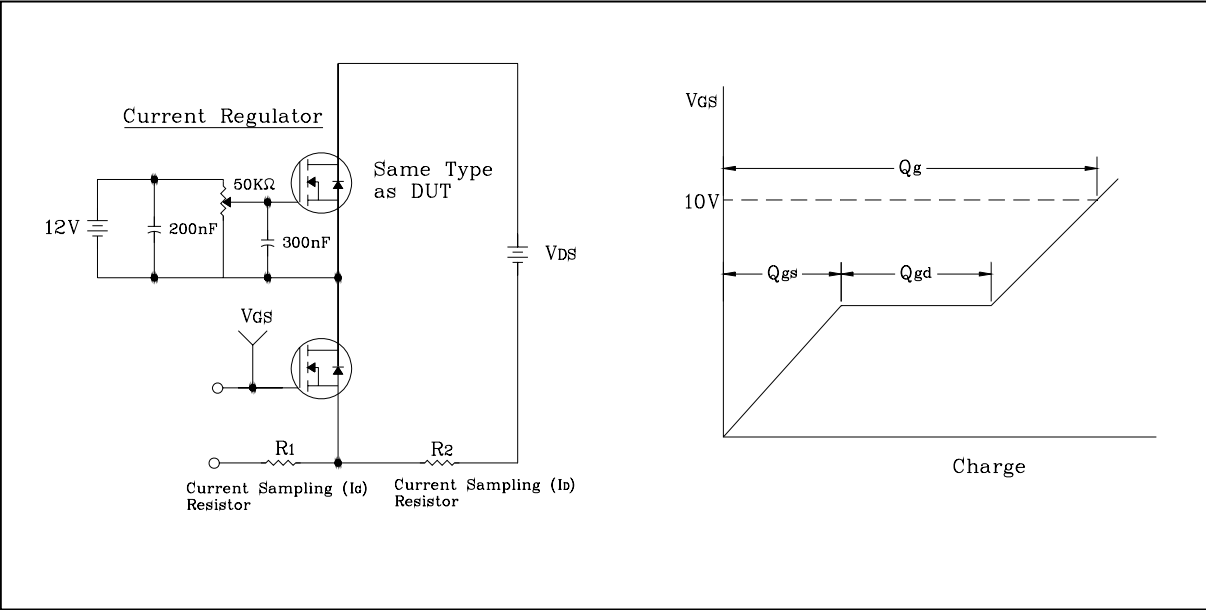


Fig. 12 Resistive Switching Test Circuit & Waveform

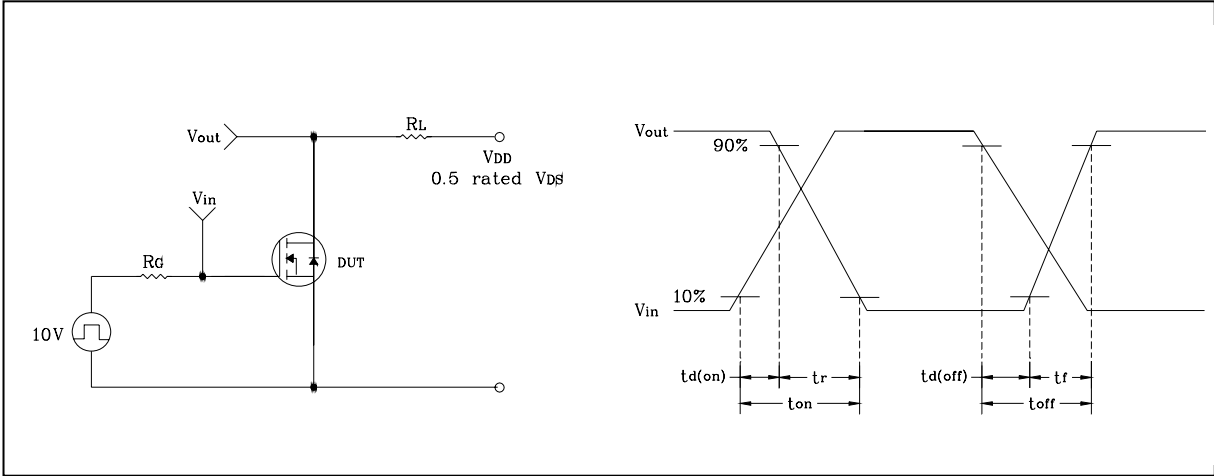


Fig. 13 E<sub>AS</sub> Test Circuit & Waveform

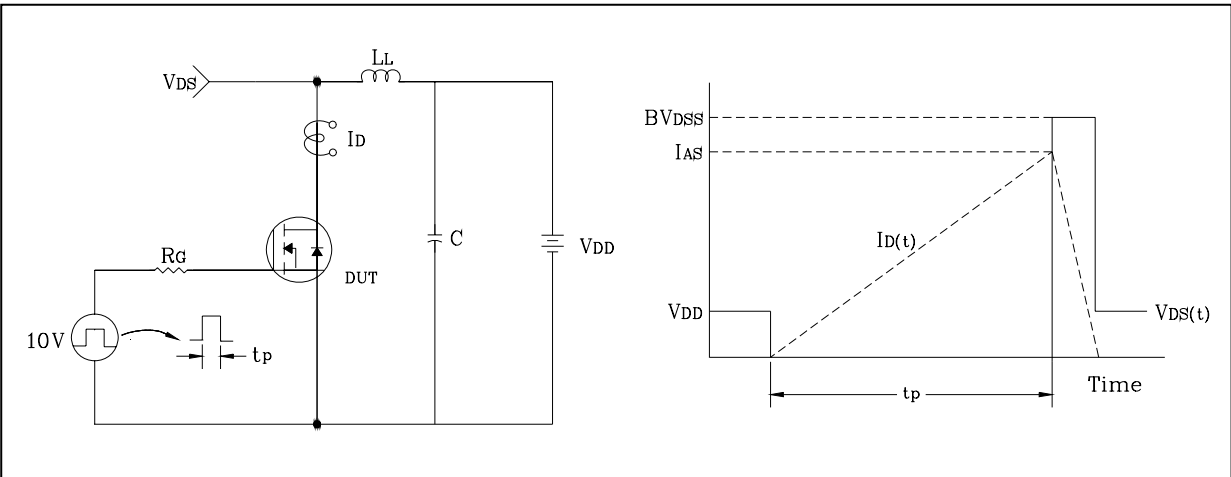
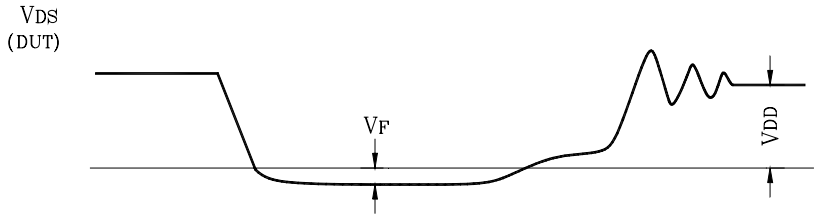
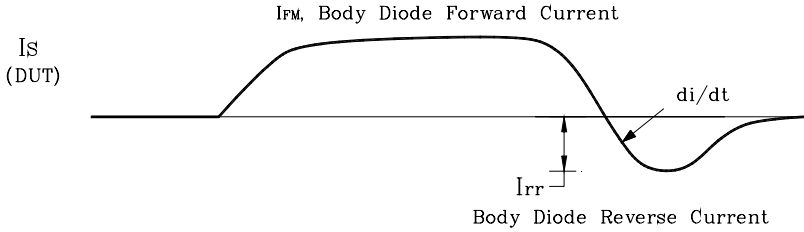
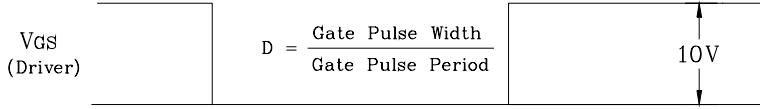
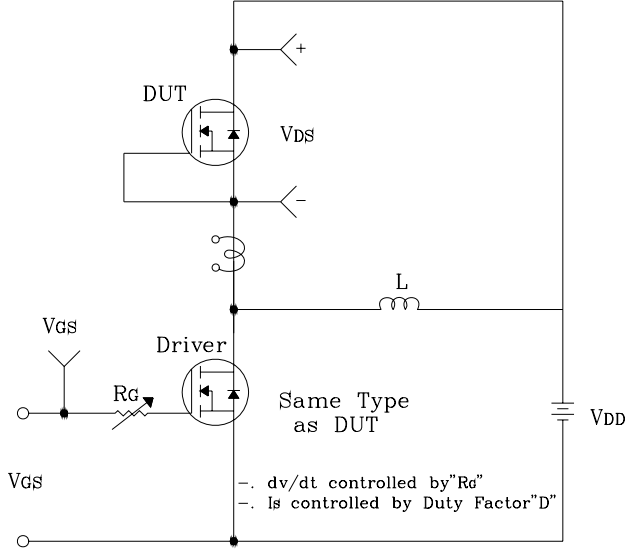


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



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