TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type (π-MOS V)

2SJ680

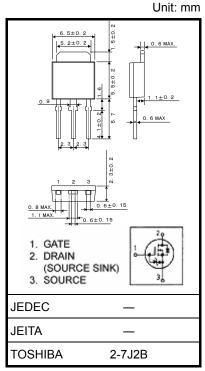
Switching Applications

Chopper Regulator, DC/DC Converter and Motor Drive Applications

- Low drain-source ON-resistance: $R_{DS(ON)} = 1.6 \Omega$ (typ.)
- High forward transfer admittance: |Y_{fs}| = 2.0 S (typ.)
- Low leakage current: $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -200 \text{ V)}$
- Enhancement model: $V_{th} = -1.5 \sim -3.5 \text{ V} (V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-200	V		
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	-200	V	
Gate-source voltage			V_{GSS}	±20	V	
Drain current	DC (Note	1)	I _D	-2.5	Α	
Dialii cuiteit	Pulse (Note	1)	I _{DP}	-10	A	
Drain power dissipat	Drain power dissipation (Tc = 25°C)			20	W	
Single pulse avalanche energy (Note 2)			E _{AS}	97.5	mJ	
Avalanche current			I _{AR}	-2.5	Α	
Repetitive avalanche energy (Note 3)			E _{AR}	2.0	mJ	
Channel temperature			T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C		



Weight: 0.36 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	6.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: $V_{DD} = -50$ V, Tch = 25°C (initial), L = -25.2 mH, $I_{AR} = -2.5$ A, $R_G = 25$ Ω
- Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

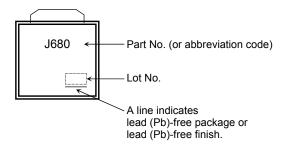
Electrical Characteristics (Ta = 25°C)

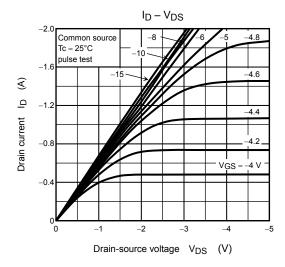
Char	Characteristic Symbol		Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	±16 V, V _{DS} = 0 V		±10	μΑ
Drain cutoff curre	ent	I _{DSS}	V _{DS} = -200 V, V _{GS} = 0 V — — — — — — — — — — — — — — — — — —		-100	μА	
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$			_	V
Gate threshold ve	oltage	V _{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-1.5	_	-3.5	V
Drain-source ON	-resistance	R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -1.5 \text{ A}$		1.6	2.0	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -1.5 \text{ A}$		2.0	_	S
Input capacitance	9	C _{iss}		_	410	_	
Reverse transfer	capacitance	C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	40	_	pF
Output capacitance		C _{oss}	1	_	145	_	
Rise time	Rise time	t _r	0 V ¬	_	20	_	
Switching time	Turn-on time	t _{on}	V_{GS} V	_	45	_	20
Switching time	Fall time	t _f		_	15	_	ns
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \mu s$ $V_{DD}^{\sim} \simeq -100 \text{ V}$	_	85	_	
Total gate charge (gate-source plus	e charge irce plus gate-drain)		$V_{DD} \simeq -160 \text{ V}, V_{GS} = -10 \text{ V},$ $I_D = -2.5 \text{ A}$	_	10	_	
Gate-source charge Gate-drain ("Miller") charge		Q _{gs}		_	6	_	nC
		Q _{gd}		_	4	_	

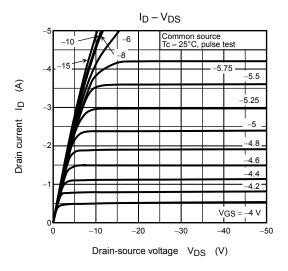
Source-Drain Ratings and Characteristics (Ta = 25°C)

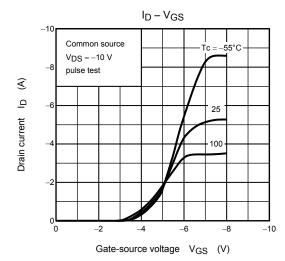
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	(Note 1)	I _{DR}	_	_	_	-2.5	Α
Pulse drain reverse current	(Note 1)	I _{DRP}	_	_	_	-10	Α
Forward voltage (diode)		V _{DSF}	I _{DR} = -2.5 A, V _{GS} = 0 V	_	_	2.0	V
Reverse recovery time		t _{rr}	$I_{DR} = -2.5 \text{ A}, V_{GS} = 0 \text{ V},$	_	135	_	ns
Reverse recovery charge		Q _{rr}	dl _{DR} /dt = 100 A/μs	_	0.81	_	μС

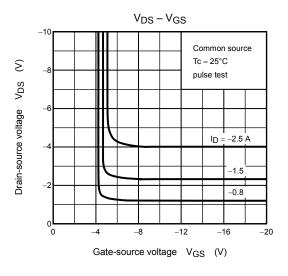
Marking

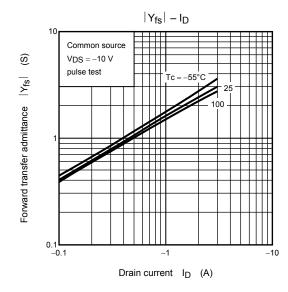


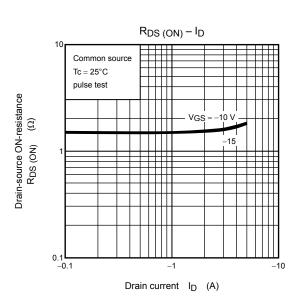




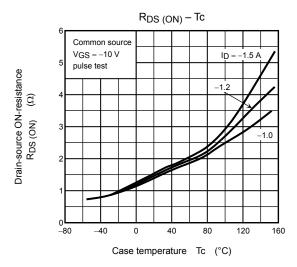


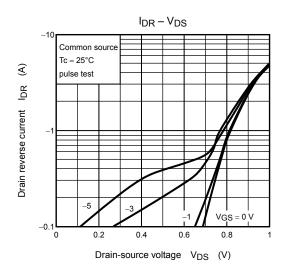


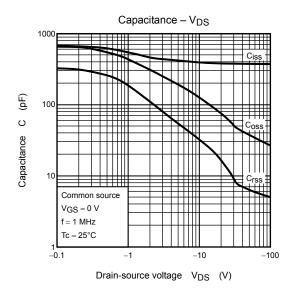


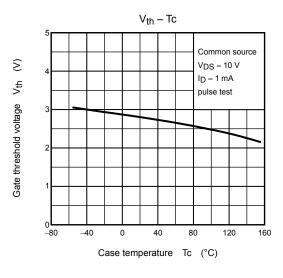


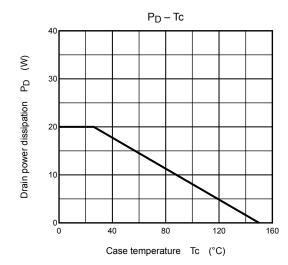
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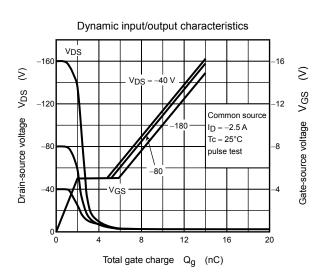


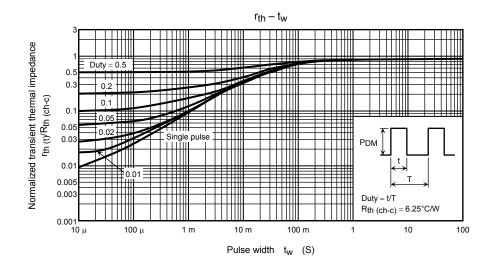


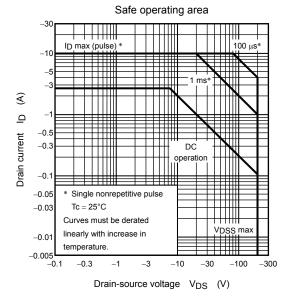


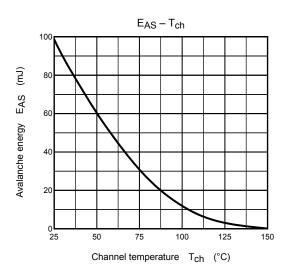


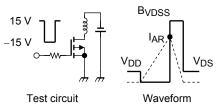












$$R_G = 25 \Omega$$

$$V_{DD} = -50 \text{ V, } L = 25.2 \text{ mH}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS} - V_{DD} \right)$$

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20070701-EN

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