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2SK3142

Silicon N Channel MOS FET High Speed Power Switching

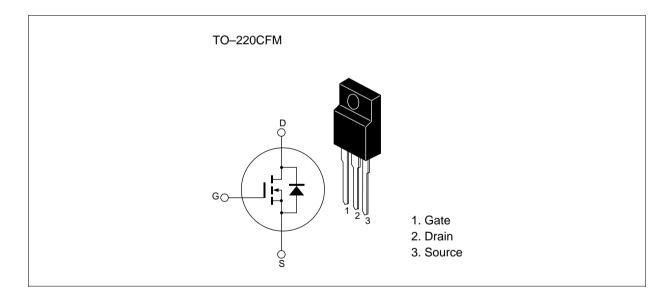


ADE-208-681A (Z) 2nd. Edition Feb. 1999

Features

- Low on-resistance $R_{DS(on)} = 4m\Omega$ typ.
- Low drive current
- 4V gate drive device can be driven from 5V source

Outline



2SK3142

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	60	A
Drain peak current	Note 1	240	A
Body-drain diode reverse drain current	I _{DR}	60	A
Avalanche current	I Note 3	35	A
Avalanche energy	E _{AR} Note 3	122	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %

2. Value at Tc = 25°C

3. Value at Tch = 25° C, Rg 50Ω

Electrical Characteristics ($Ta = 25^{\circ}C$)

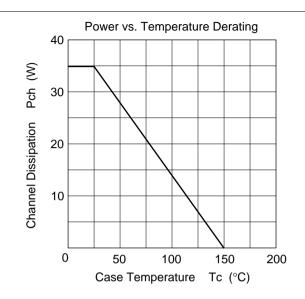
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_{D} = 10 \text{mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	10	μΑ	$V_{DS} = 30V$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.5	V	$I_D = 1 \text{mA}, V_{DS} = 10 \text{V}^{\text{Note 1}}$
Static drain to source on state	R _{DS(on)}	_	4.0	5.0	mΩ	$I_{\rm D} = 30 {\rm A}, \ V_{\rm GS} = 10 {\rm V}^{\rm Note 1}$
resistance		_	5.5	8.5	mΩ	$I_D = 30A, V_{GS} = 4V^{Note 1}$
Forward transfer admittance	y _{fs}	45	75	_	S	$I_D = 30A, V_{DS} = 10V^{Note 1}$
Input capacitance	Ciss	_	6800	_	pF	V _{DS} = 10V
Output capacitance	Coss	_	1550	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	500	_	pF	f = 1MHz
Total gate charge	Qg	_	130	_	nc	V _{DD} = 10V
Gate to source charge	Qgs	_	16	_	nc	V _{GS} = 10V
Gate to drain charge	Qgd	_	30	_	nc	I _D = 60A
Turn-on delay time	t _{d(on)}	_	50	_	ns	$V_{GS} = 10V, I_{D} = 30A$
Rise time	t _r	_	340	_	ns	$R_L = 0.33\Omega$
Turn-off delay time	t _{d(off)}	_	560	_	ns	
Fall time	t _f	_	350	_	ns	
Body-drain diode forward voltage	V_{DF}	_	1.0	_	V	$I_F = 60A, V_{GS} = 0$
Body-drain diode reverse recovery time	t _{rr}	_	70	_	ns	$I_F = 60A, V_{GS} = 0$ diF/ dt =50A/µs

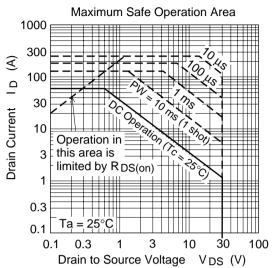
Note: 1. Pulse test

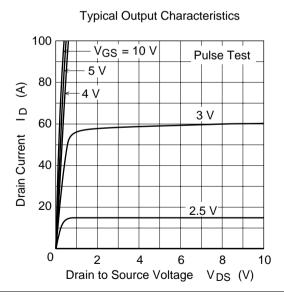
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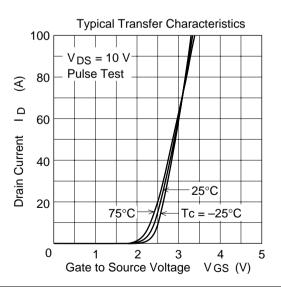
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Main Characteristics

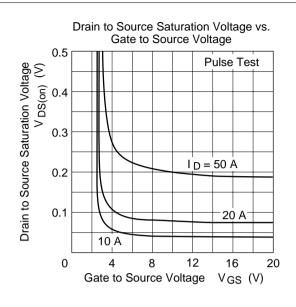


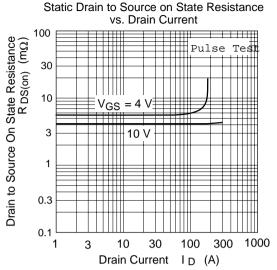


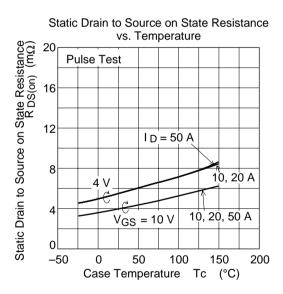


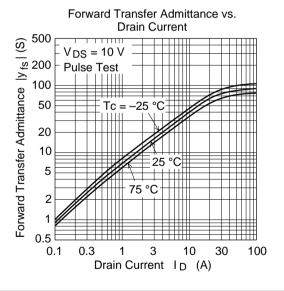


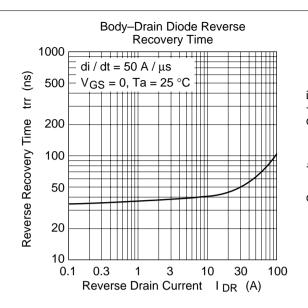
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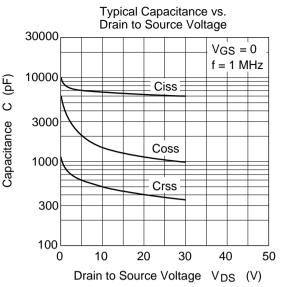




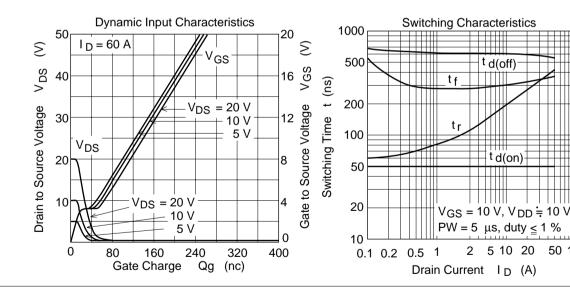


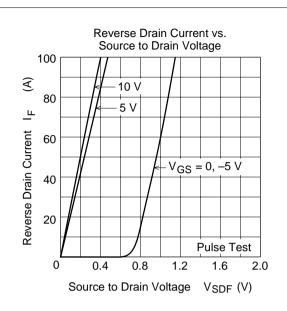


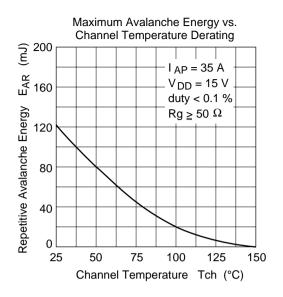


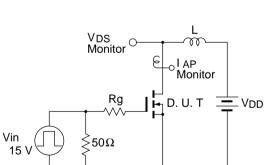


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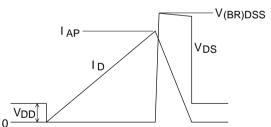


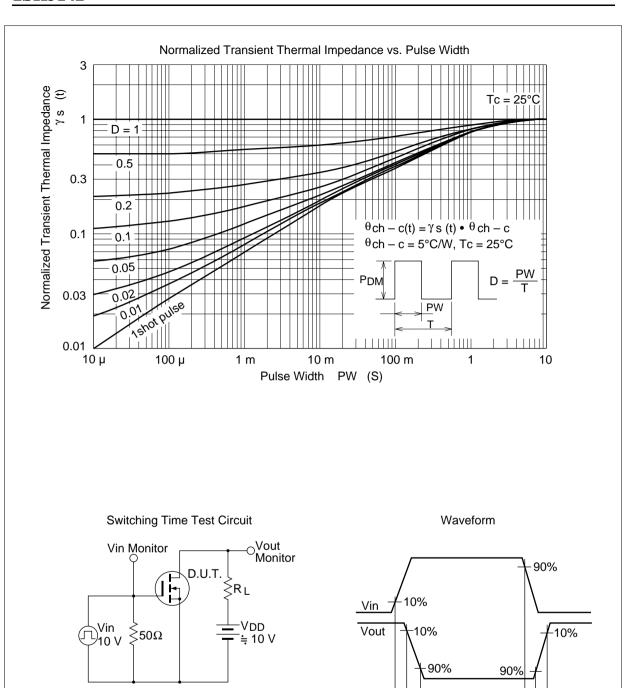




Avalanche Test Circuit

Avalanche Waveform $E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^{2} \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$





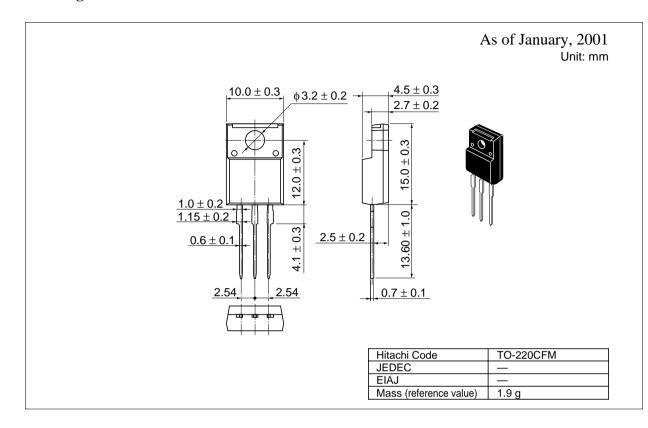
td(on)

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Package Dimensions



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