Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

2SK2749

Chopper Regulator, DC-DC Converter and Motor Drive Applications

 $\begin{array}{ll} \bullet & Low \ drain-source \ ON \ resistance & : R_{DS} \ (on) = 1.6 \ \Omega \ (typ.) \\ \bullet & High \ forward \ transfer \ admittance & : |Y_{fs}| = 5.0 \ S \ (typ.) \\ \bullet & Low \ leakage \ current & : I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 720 \ V) \\ \end{array}$

• Enhancement mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	900	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	900	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	7	Α	
	Pulse (Note 1)	I _{DP}	21	^	
Drain power dissipatio	n (Tc = 25°C)	P _D	150	W	
Single pulse avalanche energy (Note 2)		E _{AS}	682	mJ	
Avalanche current		I _{AR}	7	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 25.5 mH, I_{AR} = 7 A, R_G = 25 Ω

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



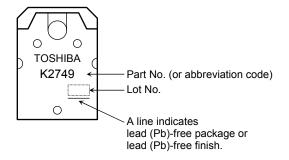
Electrical Characteristics (Ta = 25°C)

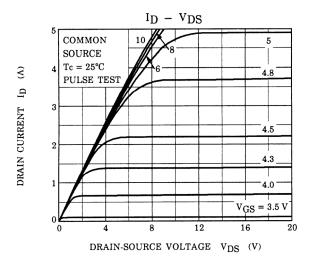
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	_	_	V
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 3.5 A	_	1.6	2.0	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3.5 A	1.25	5.0	_	S
Input capacitano	e	C _{iss}		_	1500	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		30	_	pF
Output capacitance		Coss			140	_	
Switching time	Rise time	t _r	$V_{\rm GS}$ $V_{\rm GS}$ $V_{\rm OV}$ $V_{\rm DD}$ $V_{\rm DD}$ $V_{\rm DD}$	_	35	_	- ns
	Turn-on time	t _{on}		_	80	_	
	Fall time	t _f		_	50	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu \text{s}$	_	220	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	55	_	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 7 \text{ A}$		30	_	nC
Gate-drain ("miller") Charge		Q_{gd}			25	_	

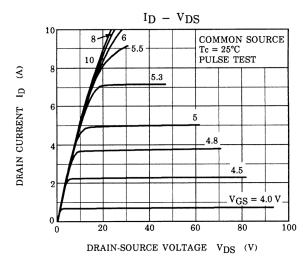
Source-Drain Ratings and Characteristics (Ta = 25°C)

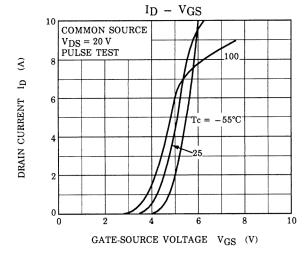
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	7	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	21	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 7 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 7 A, V _{GS} = 0 V	ı	1400	1	ns
Reverse recovery charge	Qrr	dl _{DR} / dt = 100 A / μs	_	14	_	μC

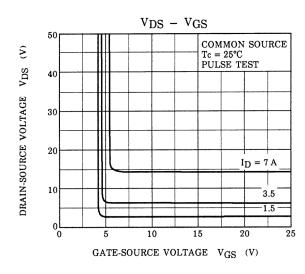
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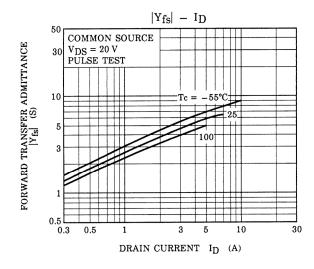


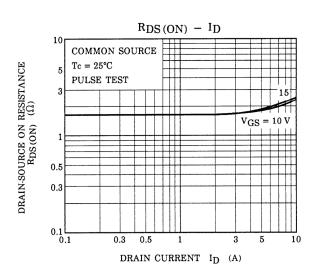


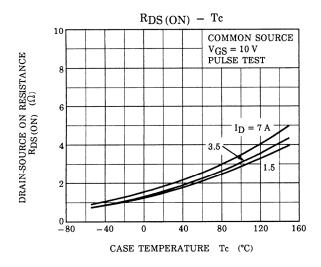


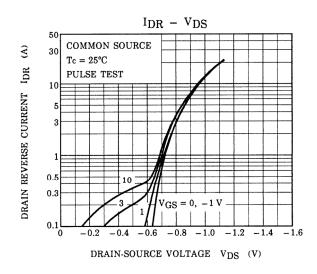


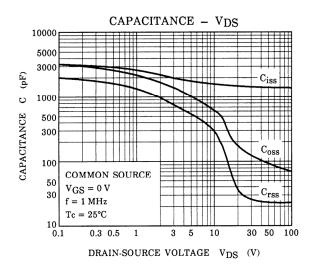


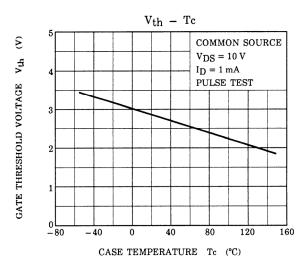


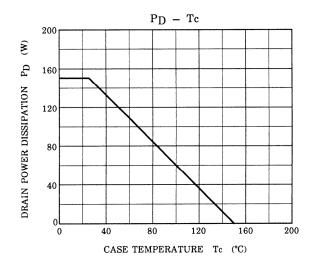


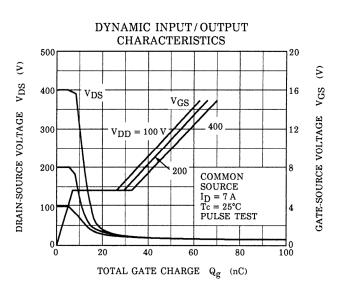


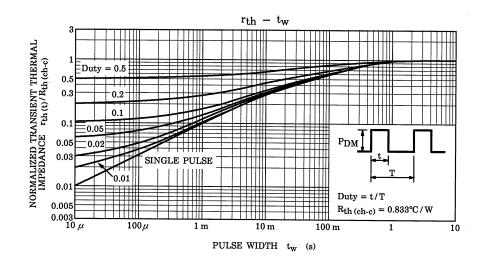


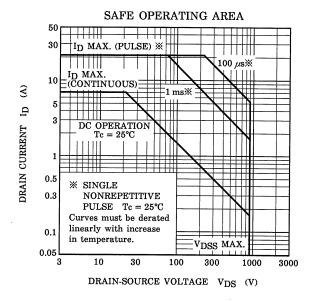


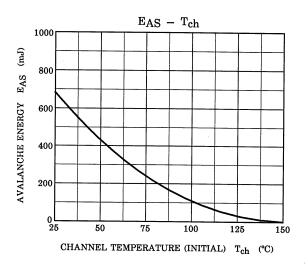


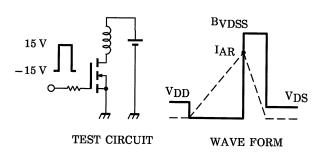












$$\begin{aligned} &R_G = 25~\Omega \\ &V_{DD} = 90~V,~L = 25.5~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BV_{DSS}}{BV_{DSS} - V_{DD}}\right) \end{aligned}$$

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20070701-EN

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