TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSV)

# 2SK3437

## DC-DC Converter, Relay Drive and Motor Drive Applications

- Low drain-source ON resistance: RDS (ON) =  $0.74 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 4.5 \text{ S} (typ.)$
- Low leakage current:  $I_{DSS} = 100 \ \mu A (max) (V_{DS} = 600 \ V)$
- Enhancement mode:  $V_{th} = 3.0 \sim 5.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	600	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	600	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	10		
	Pulse (Note 1)	I <sub>DP</sub>	30	A	
Drain power dissipation (Tc = 25°C)		PD	80	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	252	mJ	
Avalanche current		I <sub>AR</sub>	10	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	8	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

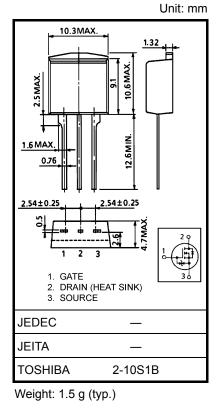
#### **Thermal Characteristics**

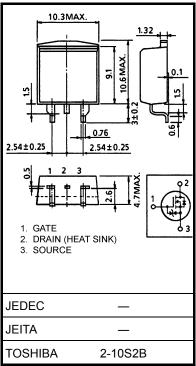
Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.56	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

- Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 4.41 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 10 A
- Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.





Weight: 1.5 g (typ.)

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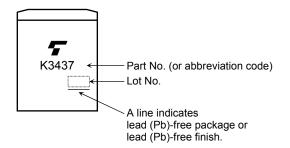
**Electrical Characteristics (Ta = 25°C)** 

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS}=\pm 25~V,~V_{DS}=0~V$			±10	μA
Drain-source brea	akdown voltage	V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30			V
Drain cut-OFF cu	rrent	I <sub>DSS</sub>	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_	_	100	μA
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_		V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0		5.0	V
Drain-source ON	resistance	R <sub>DS (ON)</sub>	$V_{GS}=10~V,~I_D=5~A$	_	0.74	1.0	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 15 V, I_D = 5 A$	2.0	4.5		S
Input capacitance Reverse transfer capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	1200		pF
		C <sub>rss</sub>		_	10		
Output capacitance		C <sub>oss</sub>		_	130		
Switching time	Rise time	tr	$V_{GS}^{10 V}$ $V_{GS}^{10 V}$ $V_{GS}^{10 V}$ $V_{GS}^{10 V}$ $V_{DD}^{10 V}$ $R_{L} = 60 \Omega$ $V_{DD}^{10 V}$ $V_{DD}^{10 V}$ $V_{DD}^{10 V}$ $V_{DD}^{10 V}$ $V_{DD}^{10 V}$		13	_	- ns
	Turn-ON time	t <sub>on</sub>			40		
	Fall time	t <sub>f</sub>			8		
	Turn-OFF time	t <sub>off</sub>		_	50		
Total gate charge (gate-source plus gate-drain)		Qg			28		nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \simeq 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	_	16		
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	12		

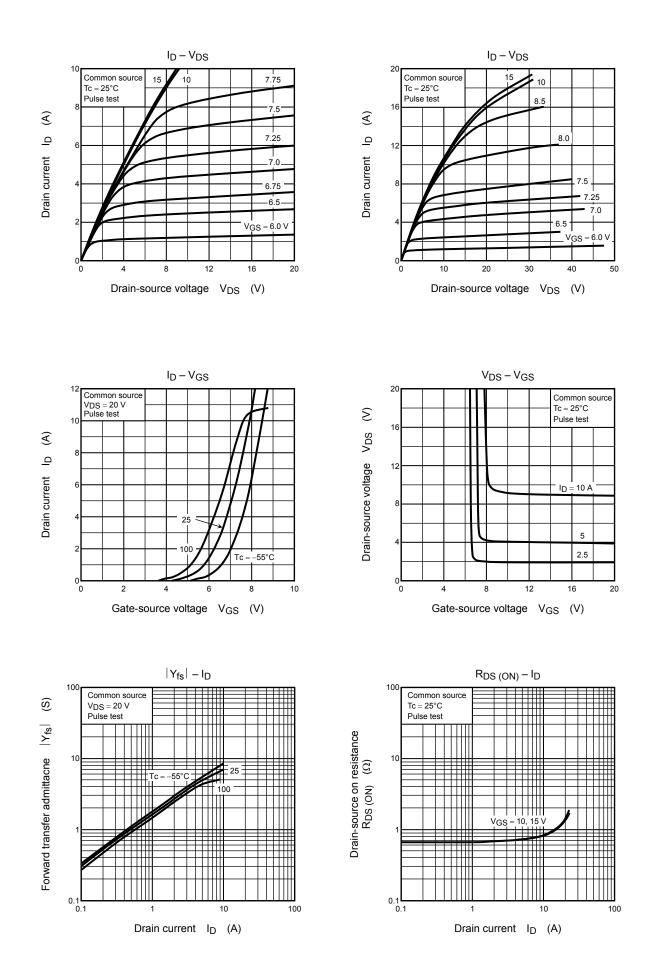
#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	—	_	10	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	30	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V,	_	1600	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/μs	_	17	_	μC

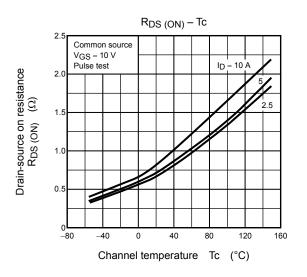
#### Marking

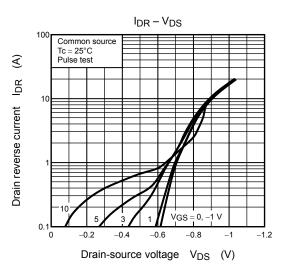


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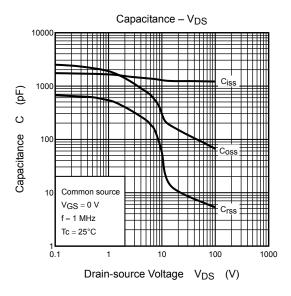


 $V_{th} - Tc$ 

Common source  $V_{DS} = 10 V$ ID = 1 mA

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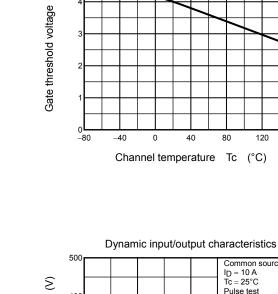
Pulse test



P<sub>D</sub> – Tc

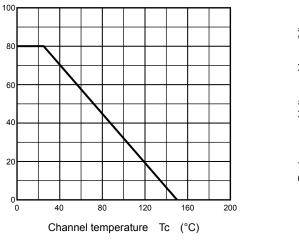
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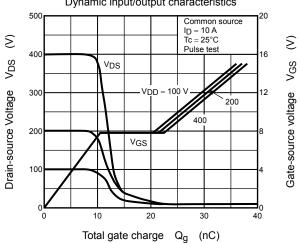
Drain Power dissipation PD

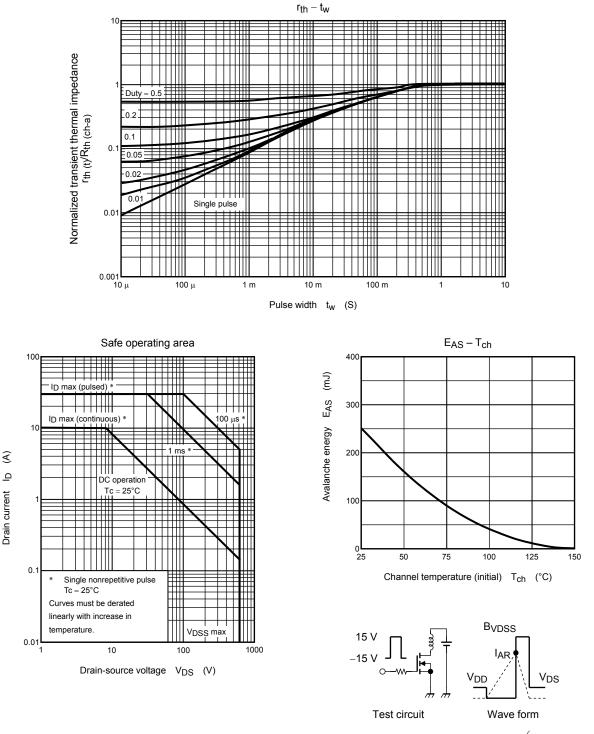


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