

TOSHIBA Photo-IC Silicon Epitaxial Planar

# TPS855(F)

## Lead(Pb)-Free

Luminosity Adjustment for TV Screens, CRT Monitors and Liquid-crystal Display Monitors

Other Equipment Requiring Luminosity Adjustment

The TPS855(F) is a linear-output photo-IC which incorporates a photodiode and a current amp circuit in a single chip. This photo-IC is current output type, so can set up output voltage freely by arbitrary load resistance.

- High sensitivity  $I_L = 280 \mu\text{A}$  (typ.)  
@EV = 100 lx Using the fluorescent light
- Little fluctuation in light current  
: 1.67 times width ( $\pm 25\%$  typ.)
- Excellent illumination output linearity
- Open-emitter output
- Side-view package
- Environmentally friendly silicon used as chip material instead of CdS  
Suitable as a substitute for CdS-based products

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	-0.5 to 7	V
Output voltage	$V_{OUT}$	$\leq V_{CC}$	V
Light current	$I_L$	10	mA
Permissible power dissipation	P	150	mW
Operating temperature range	$T_{opr}$	-25 to 85	°C
Storage temperature range	$T_{stg}$	-40 to 100	°C
Soldering temperature range (5s) ( Note 1 )	$T_{sol}$	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Solder under the lead stopper.

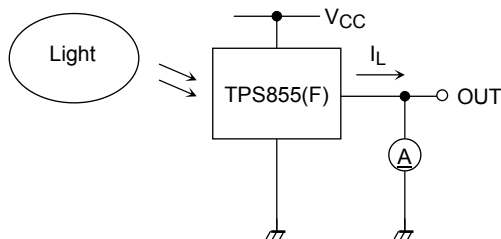
## Electrical and Optical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Supply voltage	V <sub>CC</sub>	—	2.7	—	5.5	V
Supply current	I <sub>CC</sub>	V <sub>CC</sub> = 5 V, E <sub>V</sub> = 1000 lx R <sub>L</sub> = 250 Ω (Note 2)	—	4.5	—	mA
Light current (1)	I <sub>L</sub> (1)	V <sub>CC</sub> = 5 V, E <sub>V</sub> = 100 lx (Note 2), (Note 4)	—	365	—	μA
Light current (2)	I <sub>L</sub> (2)	V <sub>CC</sub> = 5 V, E <sub>V</sub> = 10 lx (Note 3), (Note 4)	21	28	35	μA
Light current (3)	I <sub>L</sub> (3)	V <sub>CC</sub> = 5 V, E <sub>V</sub> = 100 lx (Note 3), (Note 4)	210	280	350	μA
Light current ratio	$\frac{I_L (1)}{I_L (3)}$	—	—	1.3	1.7	
Dark current	I <sub>LEAK</sub>	V <sub>CC</sub> = 5.5 V, E <sub>V</sub> = 0	—	—	0.5	μA
Saturation output voltage	V <sub>O</sub>	V <sub>CC</sub> = 5 V, R <sub>L</sub> = 75 kΩ, E <sub>V</sub> = 100 lx (Note 3)	4.2	4.35	—	V
Peak sensitivity wavelength	λ <sub>p</sub>	—	—	640	—	nm
Switching time	Rise time	V <sub>CC</sub> = 5 V, R <sub>L</sub> = 5 kΩ (Note 5)	—	0.2	—	ms
	Fall time		—	0.6	—	

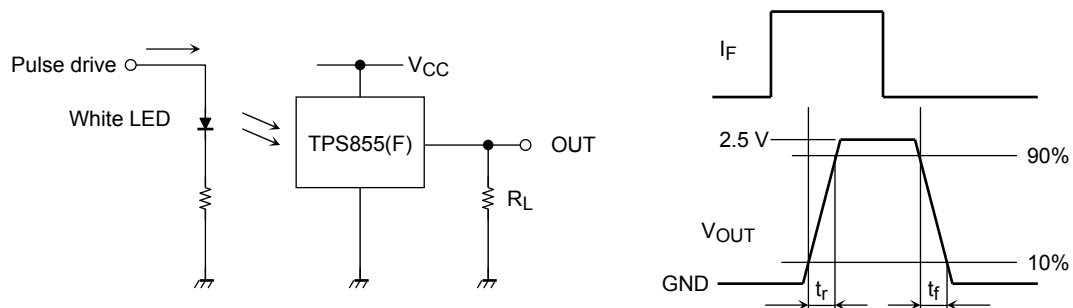
Note 2: CIE standard A light source is used (color temperature = 2856K, approximated incandescence light)

Note 3: Fluorescence light is used as light source. However, white LED is substituted in a mass-production process.

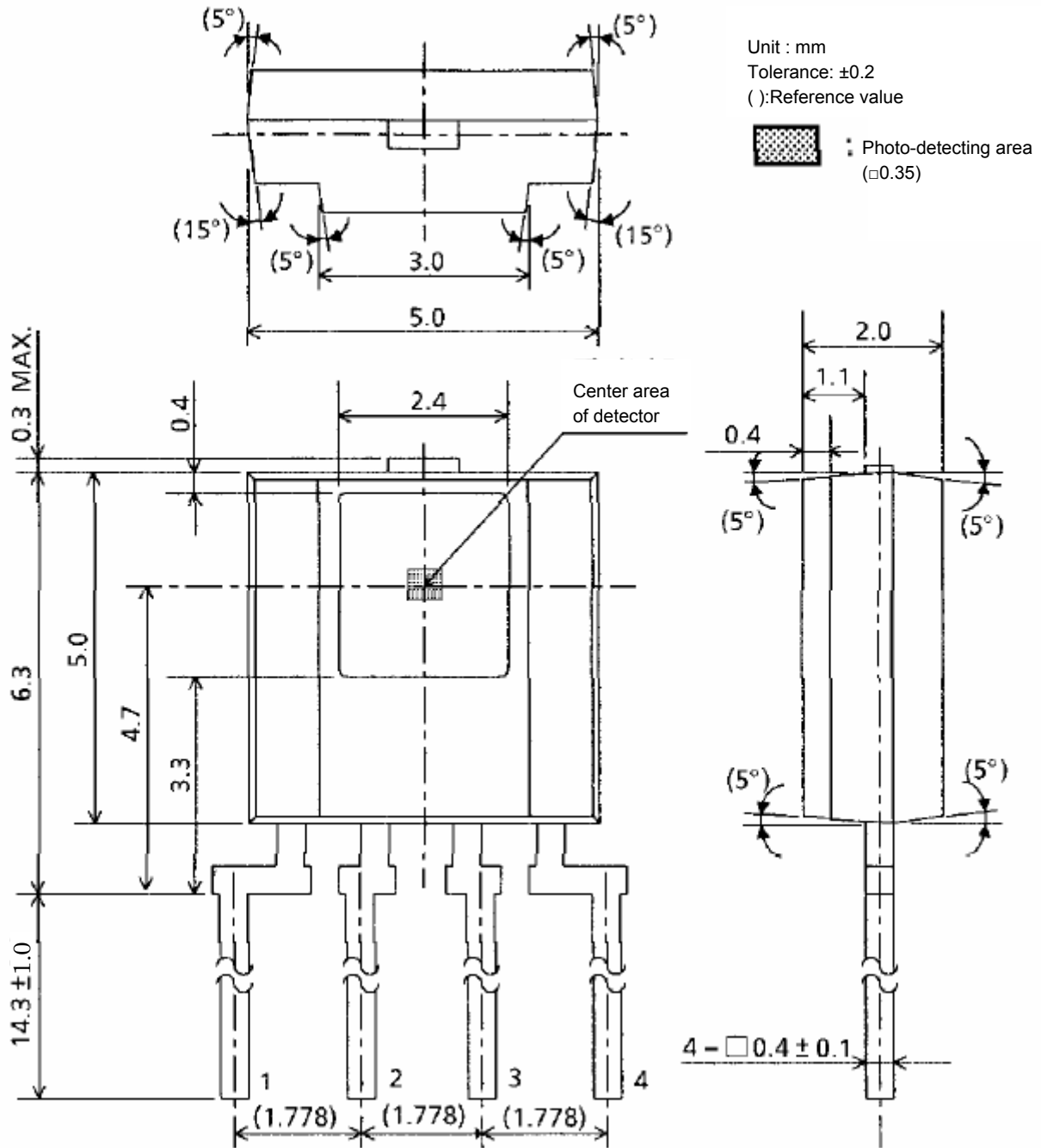
Note 4: Light current measurement circuit



Note 5: Rise time/fall time measurement method

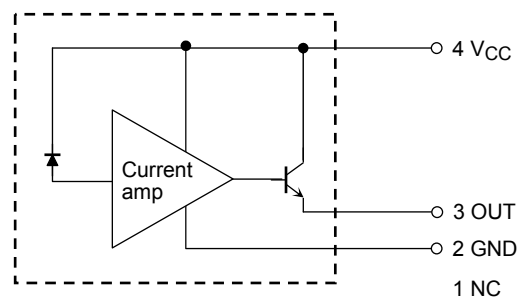


## Package Dimensions: TOSHIBA 0-5K1



Weight: 0.20 g (typ.)

## Block Diagram

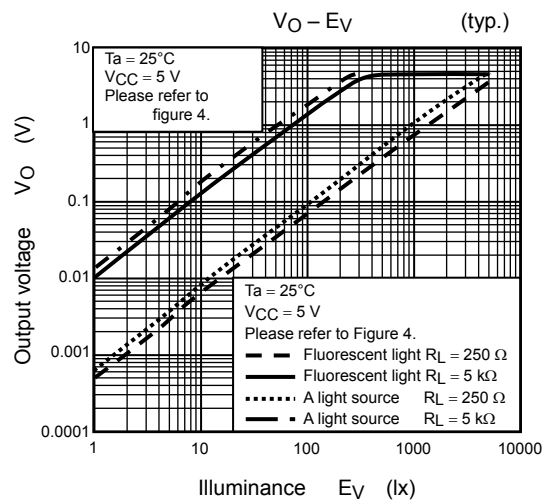
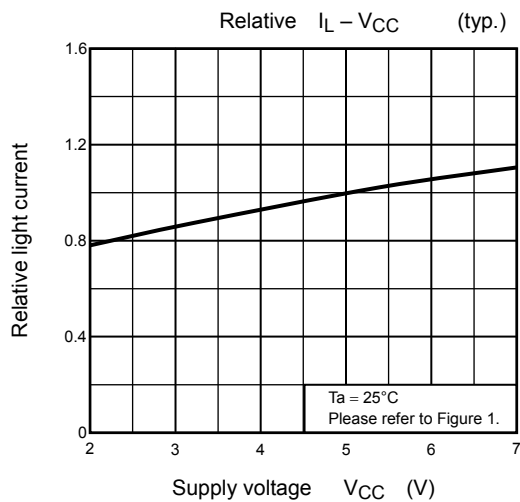
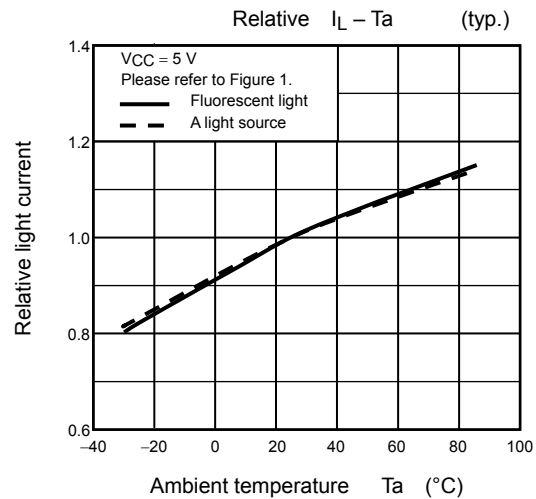
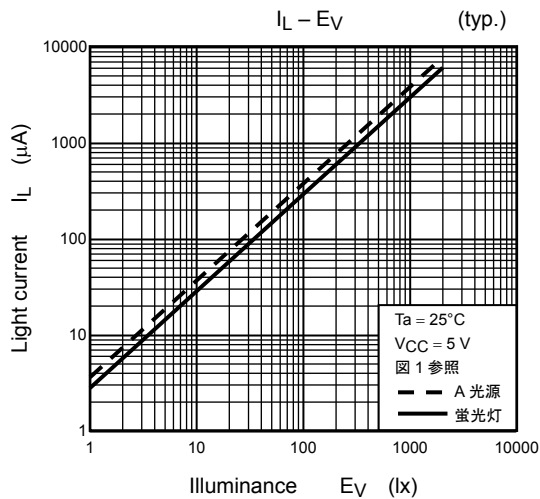
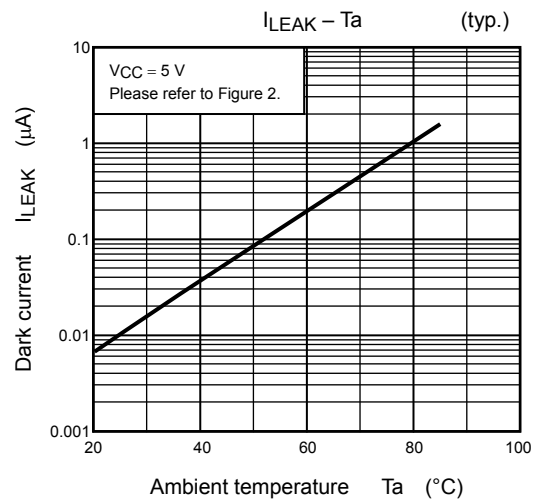
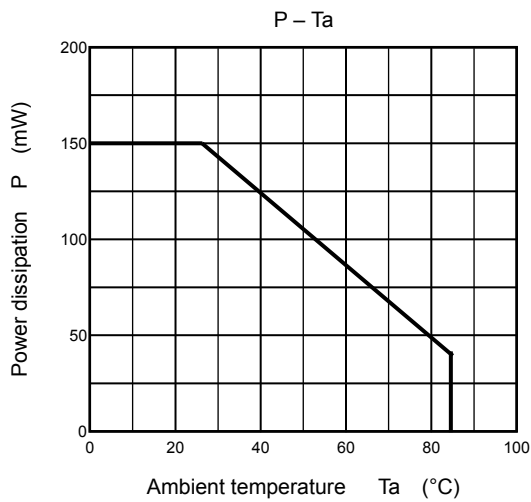


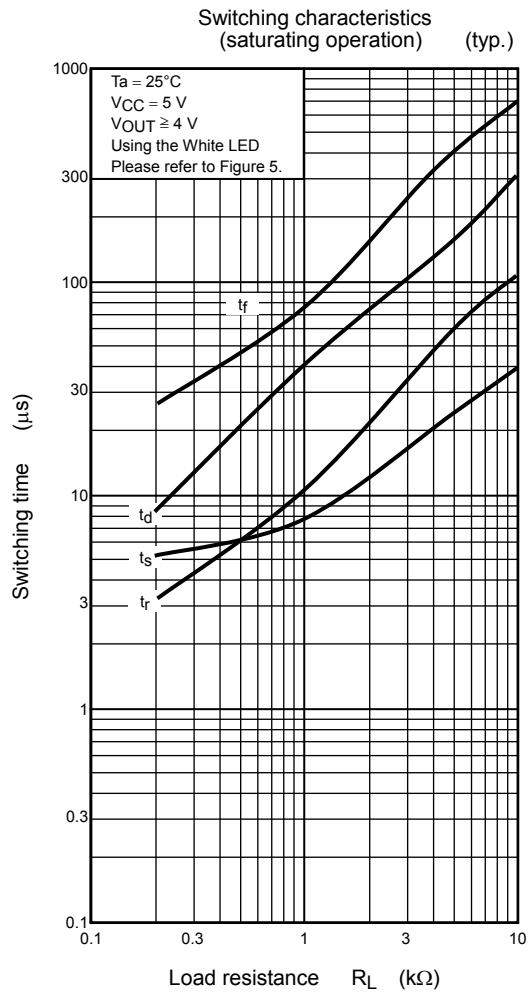
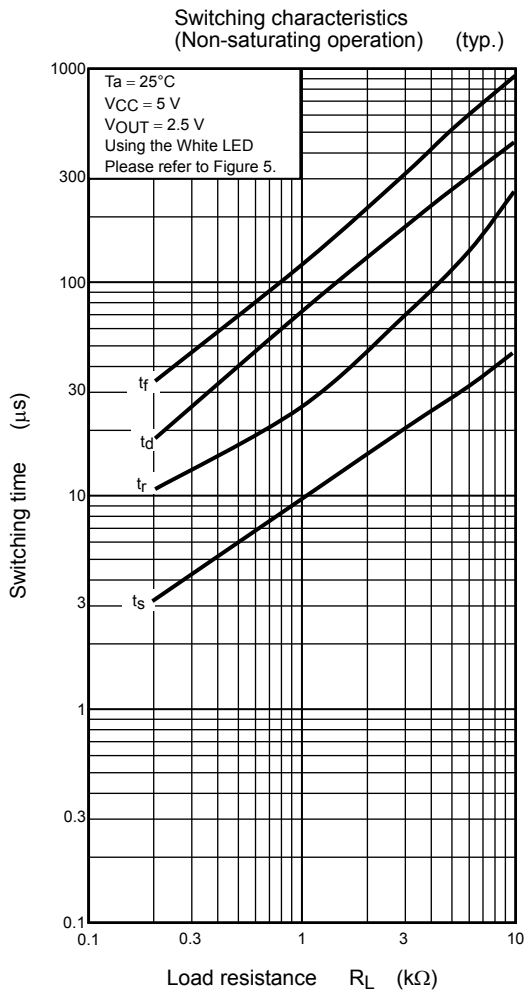
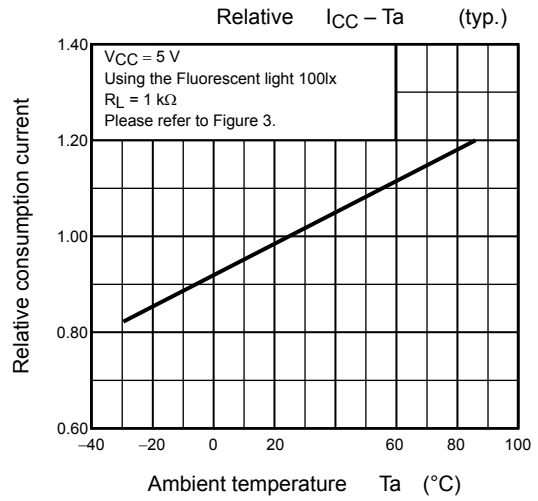
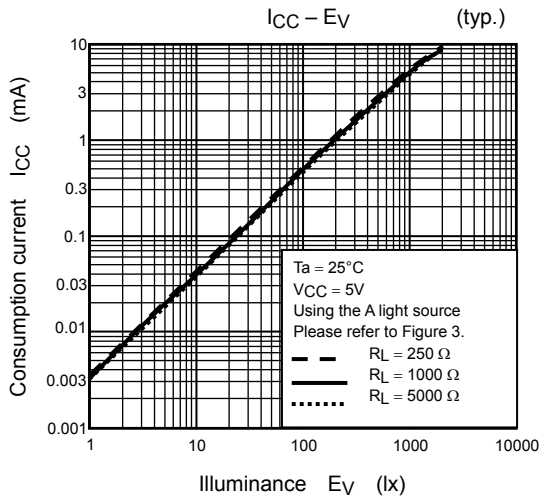
**Handling Precautions**

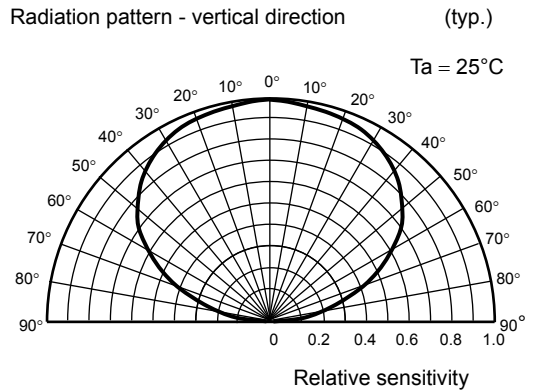
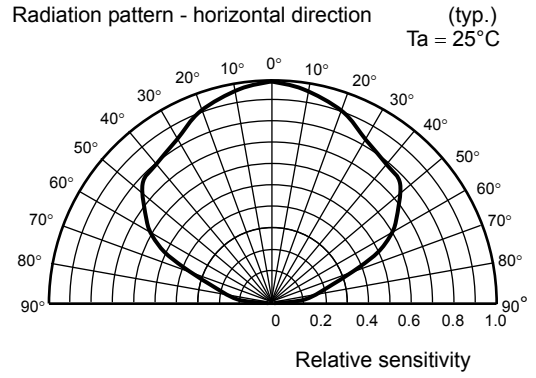
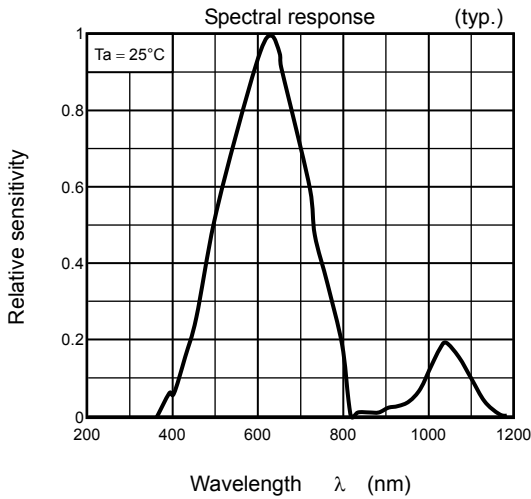
At power-on in darkness, the internal circuit takes about 50 ms to stabilize. During this period the output signal is unstable and may change. Please take this into account.

**Mounting Precautions**

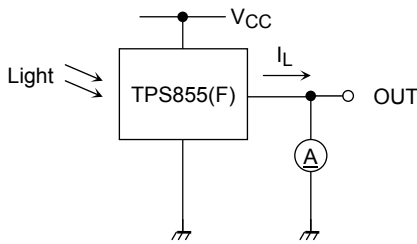
- (1) When forming the leads, bend each lead under the lead stopper. Soldering must be performed after the leads have been formed.
- (2) Soldering must be performed under the stopper.
- (3) To stabilize the power line, insert a bypass capacitor of up to 0.01  $\mu$  F between V<sub>CC</sub> and GND, close to the device.



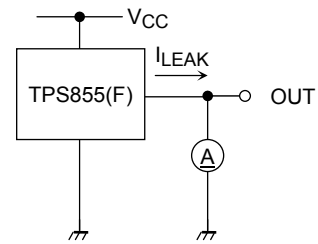




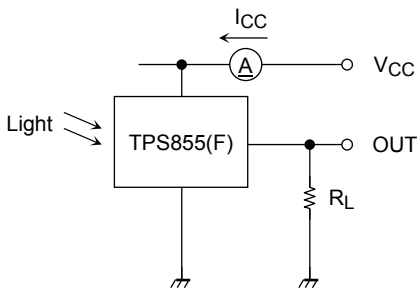
**Measurement Circuits**



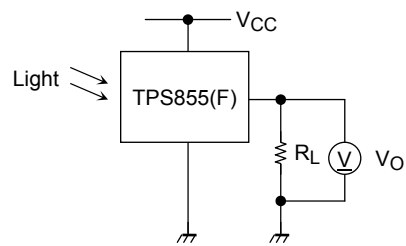
**Figure 1 Light current measurement circuit**



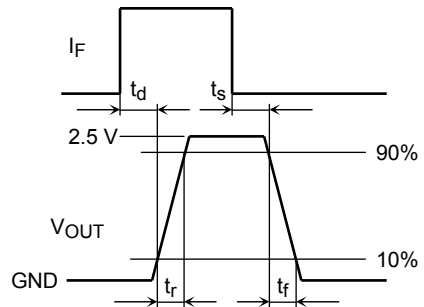
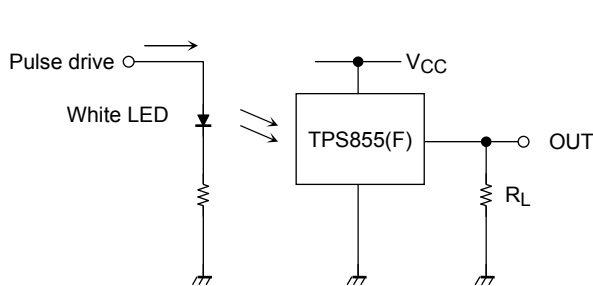
**Figure 2 Dark current measurement circuit**



**Figure 3 Consumption current measurement circuit**



**Figure 3 Output voltage measurement circuit**



**Figure 5 Switching measurement circuit and waveform**

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20070701-EN GENERAL

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