**Vishay Semiconductors** 



# **Optocoupler with Phototransistor Output**

### Description

The K814P/K824P/K844P consist of a phototransistor optically coupled to 2 gallium arsenide infrared-emitting diodes (reversed polarity) in an 4-lead up to 16-lead plastic dual inline package.

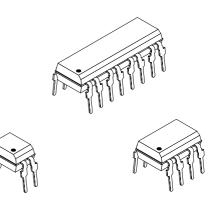
The elements are mounted on one leadframe using a **coplanar technique**, providing a fixed distance between input and output for highest safety requirements.

### Applications

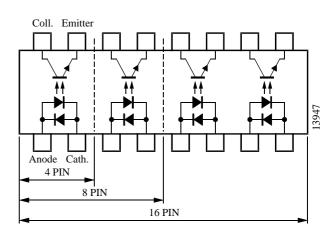
Feature phones, answering machines, PABX, fax machines

### Features

- Endstackable to 2.54 mm (0.1') spacing
- DC isolation test voltage V<sub>IO</sub> = 5 kV
- Low coupling capacitance of typical 0.3 pF
- Current Transfer Ratio (CTR) of typical 100%
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Underwriters Laboratory (UL) 1577 recognized, file number E-76222
- CSA (C–UL) 1577 recognized, file number E-76222 Double Protection
- Coupling System U



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#### **Order Instruction**

Ordering Code	CTR Ranking	Remarks
K814P	< 20%	4 Pin Single channel
K824P	< 20%	8 Pin Dual channel
K844P	< 20%	16 Pin Quad channel

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# **Absolute Maximum Ratings**

### Input (Emitter)

Parameter	Test Conditions	Symbol	Value	Unit
Reverse voltage		V <sub>R</sub>	6	V
Forward current		IF	±60	mA
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	±1.5	A
Power dissipation	$T_{amb} \le 25^{\circ}C$	P <sub>V</sub>	100	mW
Junction temperature		T <sub>i</sub>	125	°C

# Output (Detector)

Parameter	Test Conditions	Symbol	Value	Unit
Collector emitter voltage		V <sub>CEO</sub>	70	V
Emitter collector voltage		V <sub>ECO</sub>	7	V
Collector current		Ι <sub>C</sub>	50	mA
Peak collector current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA
Power dissipation	T <sub>amb</sub> ≤ 25 °C	P <sub>V</sub>	150	mW
Junction temperature		Тi	125	°C

### Coupler

Parameter	Test Conditions	Symbol	Value	Unit		
AC Isolation test voltage (RMS)	t = 1 min	V <sub>IO</sub> <sup>1)</sup>	5	kV		
Total power dissipation	T <sub>amb</sub> ≤ 25°C	P <sub>tot</sub>	250	mW		
Operating ambient temperature range		T <sub>amb</sub>	-40 to +100	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C		
Soldering temperature	2 mm from case, $t \le 10 s$	T <sub>sd</sub>	260	°C		
<sup>1)</sup> Related to standard climate 23/50 DIN 50014						



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# **Electrical Characteristics** (T<sub>amb</sub> = 25°C)

Input (Emitter)

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Forward voltage	I <sub>F</sub> = ±50 mA	V <sub>F</sub>		1.25	1.6	V
Reverse current	$V_R = \pm 6 V$	I <sub>R</sub>			10	μΑ

#### Output (Detector)

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Collector emitter voltage	I <sub>C</sub> = 100 μA	V <sub>CEO</sub>	70			V
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V
Collector dark current	$V_{CE} = 20 \text{ V}, \text{ I}_{F} = 0, \text{ E} = 0$	I <sub>CEO</sub>			100	nA

### Coupler

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Collector emitter saturation voltage	$I_F = \pm 10 \text{ mA}, I_C = 1 \text{ mA}$	V <sub>CEsat</sub>			0.3	V
Cut-off frequency	$    I_F = \pm 10 \text{ mA}, \text{ V}_{CE} = 5 \text{ V}, \\ R_L = 100 \Omega $	f <sub>c</sub>		100		kHz
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF

### Current Transfer Ratio (CTR)

Parameter	Test Conditions	Туре	Symbol	Min.	Тур.	Max.	Unit
I <sub>C</sub> /I <sub>F</sub>	$V_{CE}$ = 5 V, $I_F$ = ± 5 mA		CTR	0.2		3.0	

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#### **Switching Characteristics**

Parameter	Test Conditions	Symbol	Тур.	Unit
Delay time	$V_S = 5 \text{ V}, \text{ I}_C = 2 \text{ mA}, \text{ R}_L = 100 \Omega \text{ (see figure 1)}$	t <sub>d</sub>	3.0	μs
Rise time		t <sub>r</sub>	3.0	μs
Fall time		t <sub>f</sub>	4.7	μs
Storage time		t <sub>s</sub>	0.3	μs
Turn-on time		t <sub>on</sub>	6.0	μs
Turn-off time		t <sub>off</sub>	5.0	μs
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega \text{ (see figure 2)}$	t <sub>on</sub>	9.0	μs
Turn-off time		t <sub>off</sub>	18.0	μs

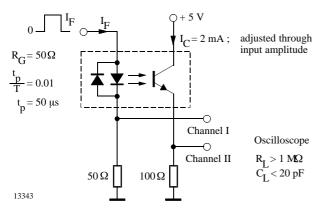


Figure 1. Test circuit, non-saturated operation

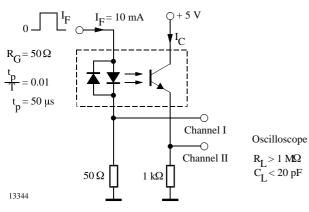
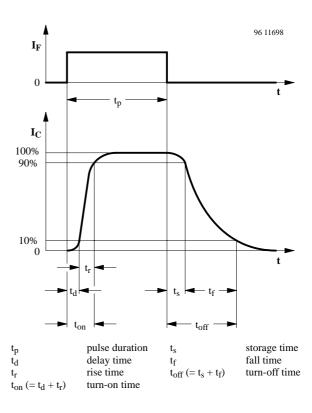


Figure 2. Test circuit, saturated operation







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# **Typical Characteristics** ( $T_{amb} = 25^{\circ}C$ , unless otherwise specified)

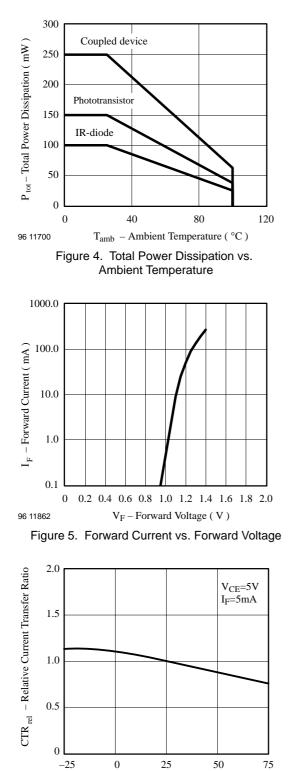
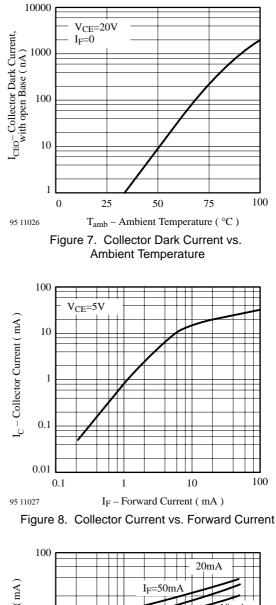


Figure 6. Relative Current Transfer Ratio vs. Ambient Temperature

 $T_{amb}$  – Ambient Temperature (  $^\circ C$  )



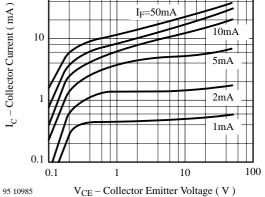


Figure 9. Collector Current vs. Collector Emitter Voltage

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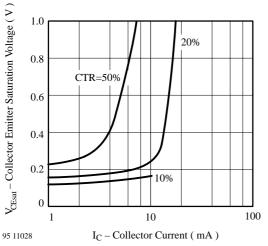


Figure 10. Collector Emitter Saturation Voltage vs. Collector Current

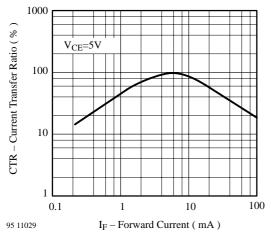
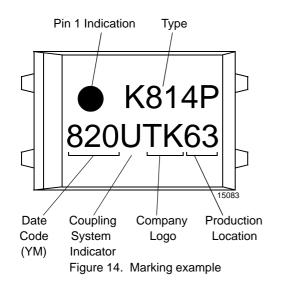
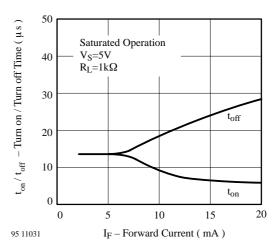


Figure 11. Current Transfer Ratio vs. Forward Current





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Figure 12. Turn on / off Time vs. Forward Current

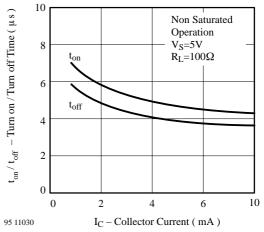
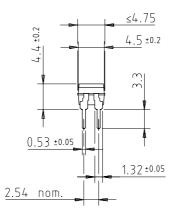


Figure 13. Turn on / off Time vs. Collector Current

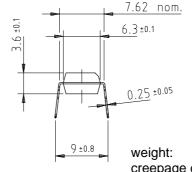
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### Dimensions of K814P in mm

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weight: ca. 0.25 g creepage distance:  $\geq 6 \text{ mm}$ air path:  $\geq 6 \text{ mm}$ 

after mounting on PC board

### E.g.: special Features: endstackable to 2.54mm ( .100" ) spacing 作 中个个个

2.54

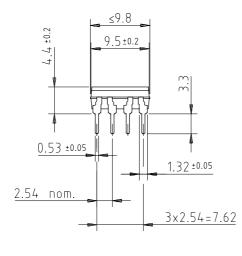
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#### Dimensions of K824P in mm

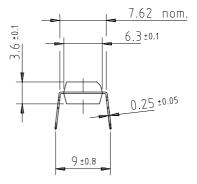


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weight:ca. 0.55 gcreepage distance: $\geq$  6 mmair path: $\geq$  6 mm

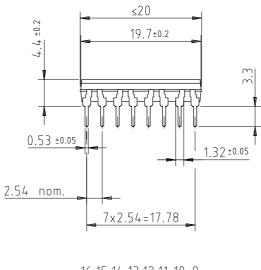
after mounting on PC board



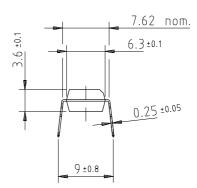
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# Dimensions of K844P in mm



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1	2	3	4	5	6	7	8	



weight:	ca. 1.0 g
creepage distan	ce:≧ 6 mm
air path:	≧ 6 mm

after mounting on PC board



technical drawings according to DIN specifications

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#### **Vishay Semiconductors**

### **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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Datasheets for electronics components.