TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

2SK2991

DC-DC Converter Relay Drive and Motor Drive Applications

 $\begin{array}{ll} \bullet & Low\ drain-source\ ON\ resistance & : RDS\ (ON) = 1.35\ \Omega\ (typ.) \\ \bullet & High\ forward\ transfer\ admittance & : |Y_{fs}| = 4.0\ S\ (typ.) \\ \bullet & Low\ leakage\ current & : IDSS = 100\ \mu A\ (max)\ (V_{DS} = 500\ V) \\ \bullet & Enhancement\ mode & : V_{th} = 2.0 {\sim} 4.0\ V\ (V_{DS} = 10\ V,\ ID = 1\ mA) \\ \end{array}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ΙD	5	Α	
	Pulse (Note 1)	I _{DP}	20	Α	
Drain power dissipation (Tc = 25°C)		P _D	50	W	
Single pulse avalanche energy (Note 2)		E _{AS}	180	mJ	
Avalanche current		I _{AR}	5	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	4	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.5	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

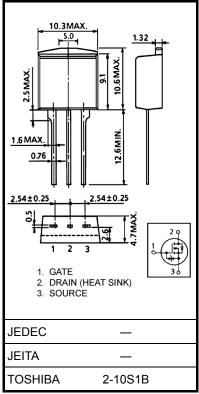
Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 12.2 mH, R_G = 25 Ω ,

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

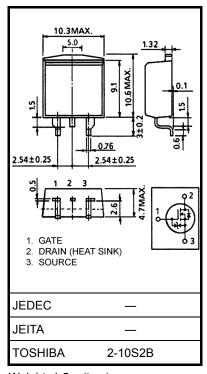
This transistor is an electrostatic-sensitive device.

Please handle with caution.

Unit: mm



Weight: 1.5 g (typ.)



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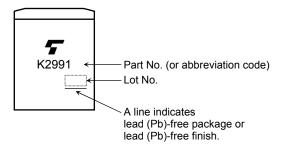
Electrical Characteristics (Ta = 25°C)

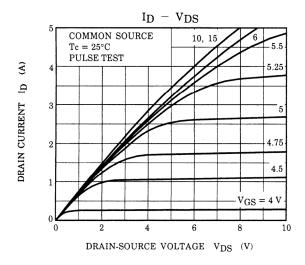
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cui	rrent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	٧
Gate threshold v	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 2.5 A	_	1.35	1.50	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2.5 A	2.5	4.0	_	S
Input capacitano	e	C _{iss}		_	780	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	60	_	pF
Output capacitar	utput capacitance C _{oss}		_	200	_		
Switching time	Rise time	t _r	$V_{GS} = 0V$ $I_{D} = 2.5A$ V_{OUT} $R_{L} = 90\Omega$	_	12	_	
	Turn-on time	t _{on}		_	25	_	- ns
	Fall time	t _f		1	15		
	Turn-off time	t _{off}	$V_{DD} \stackrel{\rightleftharpoons}{=} 225V$ Duty $\leq 1\%$, $t_w = 10 \mu s$		60	_	
Total gate charge (gate-source plus gate-drain)				17			
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5 \text{ A}$		11	_	nC
Gate-drain ("miller") charge		Q_{gd}		_	6	_	

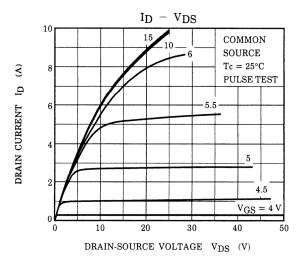
Source-Drain Ratings and Characteristics (Ta = 25°C)

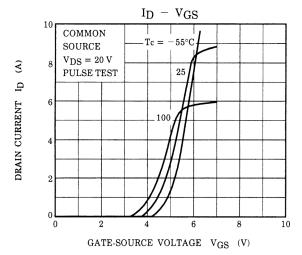
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	20	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 5 A, V _{GS} = 0 V, dI _{DR} / dt = 100 A / μs	1	1400	1	ns
Reverse recovery charge	Q _{rr}	10R - 3 A, vGS - 0 v, αιDR / αι - 100 A / μs		9	_	μC

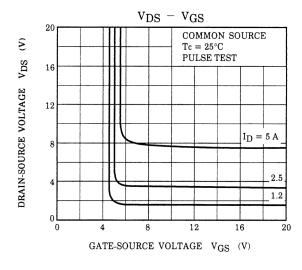
Marking

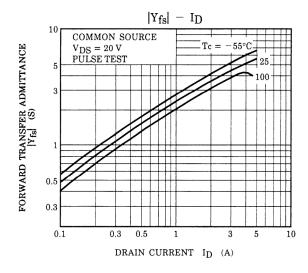


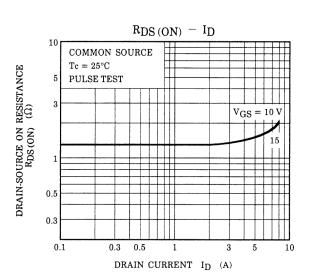


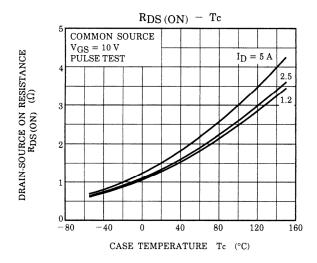


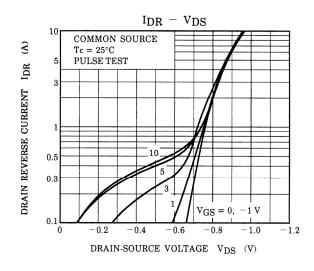


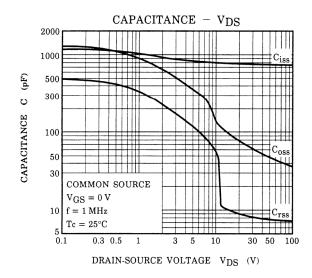


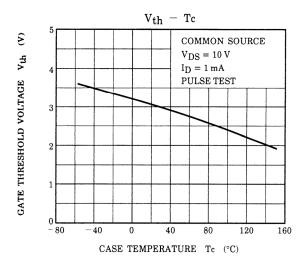


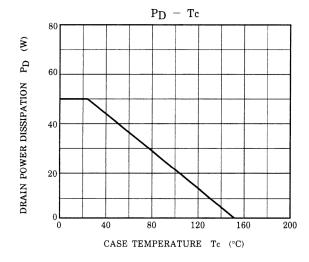


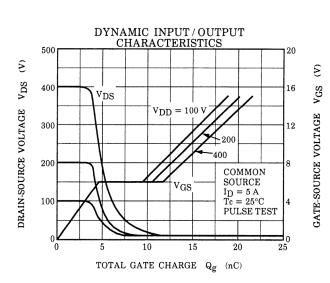


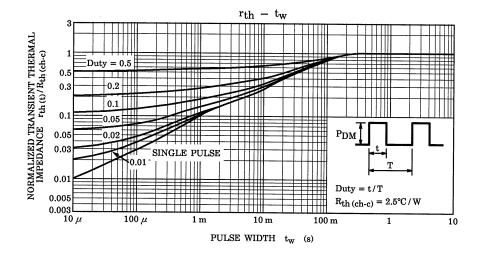


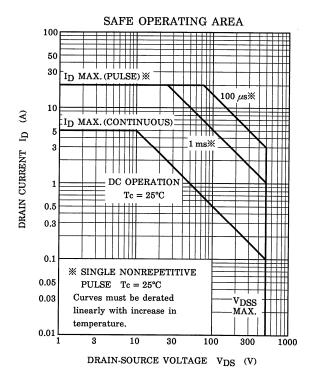


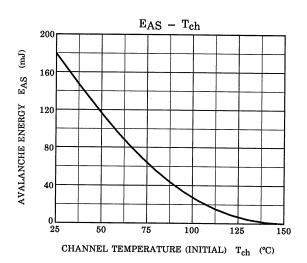


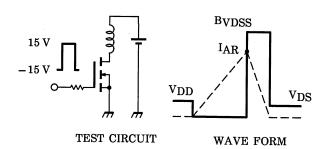












$$\begin{aligned} R_G &= 25 \ \Omega \\ V_{DD} &= 90 \ V, \ L = 12.2 \ mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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20070701-EN

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