TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSII)

## 2SK3662

# Switching Regulator, DC-DC Converter, Motor Drive Applications

• Low drain-source ON resistance: RDS (ON) =  $9.4 \text{ m}\Omega$  (typ.)

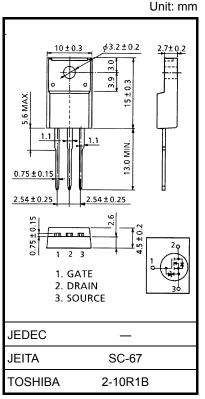
• High forward transfer admittance:  $|Y_{fs}| = 55 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 60 \text{ V)}$ 

• Enhancement mode:  $V_{th} = 1.3 \text{ to } 2.5 \text{ V (VDS} = 10 \text{ V, ID} = 1 \text{ mA)}$ 

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	60	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		$V_{DGR}$	60	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	I <sub>D</sub>	35	Α	
	Pulse (Note 1)	I <sub>DP</sub>	105		
Drain power dissipation (Tc = 25°C)		$P_{D}$	35	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	204	mJ	
Avalanche current		I <sub>AR</sub>	35	Α	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	3.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.57	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 25 \text{ V}$ ,  $T_{ch} = 25^{\circ}\text{C}$  (initial),  $L = 227 \mu\text{H}$ ,  $I_{AR} = 35 \text{ A}$ ,  $R_G = 25 \Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



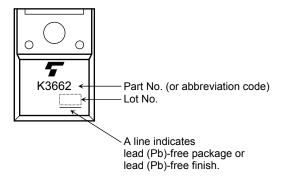
### **Electrical Characteristics (Ta = 25°C)**

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	40	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.3	_	2.5	V
Drain-source ON resistance		R <sub>DS</sub> (ON)	V <sub>GS</sub> = 4 V, ID = 18 A	_	12.5	19	- mΩ
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 18 A	_	9.4	12.5	
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 18 A	28	55	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	5120	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	300	_	
Output capacitance		Coss		_	500	_	
Switching time	Rise time	t <sub>r</sub>	10 V VGS 0 V 0 V 0 V 0 V 0 V 0 V 0 V 0 V 0 V 0 V	_	6	_	- ns
	Turn-on time	t <sub>on</sub>		_	19	_	
	Fall time	t <sub>f</sub>		_	20	_	
	Turn-off time	t <sub>off</sub>		_	115	_	
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ 48 V, V <sub>GS</sub> = 10 V,	_	91	_	nC
Gate-source charge		Q <sub>gs</sub>	$I_D = 35 \text{ A}$	_	70	_	
Gate-drain ("miller") charge		Q <sub>gd</sub>		_	21	_	

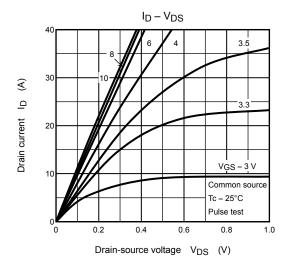
## Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

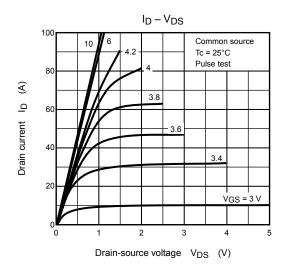
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1	I <sub>DR</sub>	_	_	_	35	Α
Pulse drain reverse current (Note 1	I <sub>DRP</sub>	_	_	_	105	Α
Forward voltage (diode)	V <sub>DS2F</sub>	I <sub>DR1</sub> = 35 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 35 \text{ A}, V_{GS} = 0 \text{ V},$		60	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> /dt = 50 A/μs	_	58	_	nC

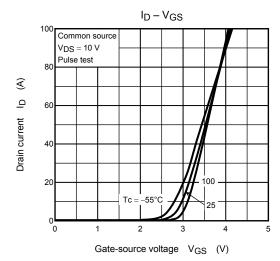
## Marking

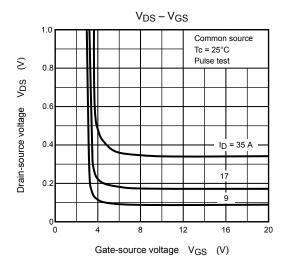


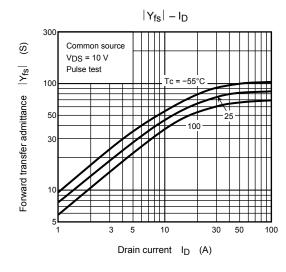
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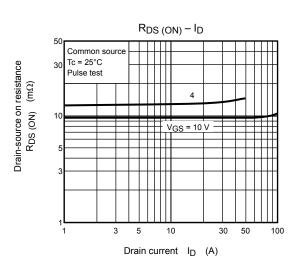




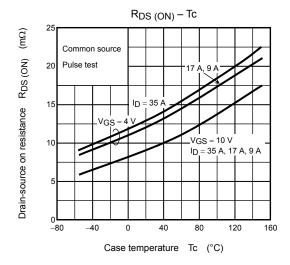


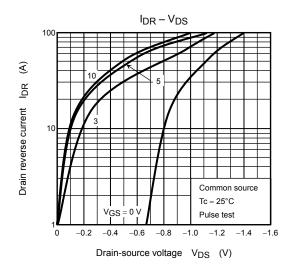


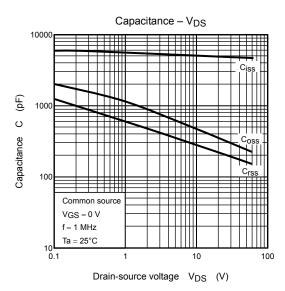


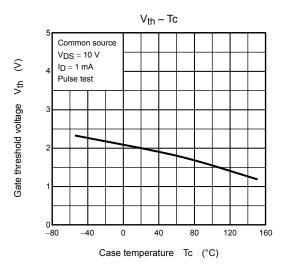


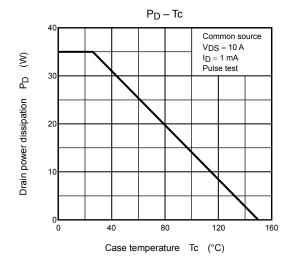
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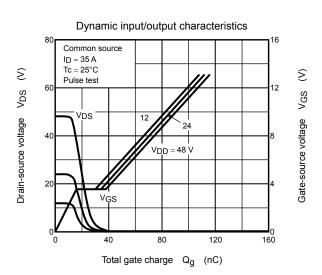


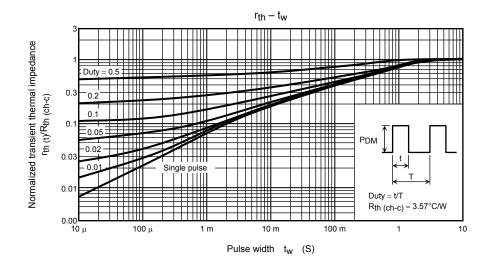


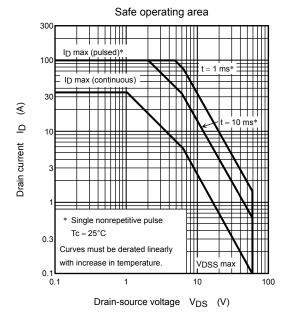


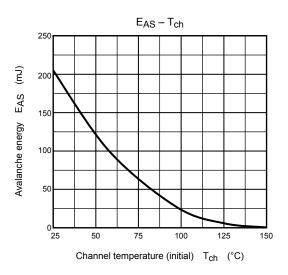


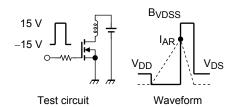












$$R_G = 25~\Omega$$
  $V_{DD} = 25~V,~L = 227~\mu H$ 

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{BVDSS}{BVDSS} - V_{DD} \right)$$

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