TOSHIBA Diode Silicon Epitaxial PIN Type

JDP2S02ACT

UHF~VHF Band RF Switch Applications

- Suitable for reducing set's size as a result from enabling high-density mounting due to 2-pin small packages.
- Low series resistance: r_S = 1.0 Ω (typ.)
- Low capacitance: C_T = 0.3 pF (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Reverse voltage	V_{R}	30	V
Forward current	lF	50	mA
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Weight: 0.00077 g(Typ.)

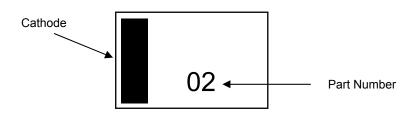
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse voltage	V_{R}	I _R = 10 μA	30	_	_	V
Reverse current	I _R	V _R = 30 V	_	_	0.1	μΑ
Forward voltage	V _F	I _F = 50 mA	_	0.9	0.94	V
Capacitance(Note2)	C _T	V _R = 1 V, f = 1 MHz	_	0.3	0.4	pF
Series resistance	r _S	I _F = 10 mA, f = 100 MHz	_	1.0	1.5	Ω

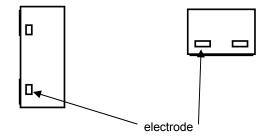
Note1: Signal level when capacitance is measured. V_{sig} = 100 mVrms

Marking

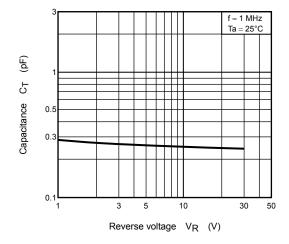


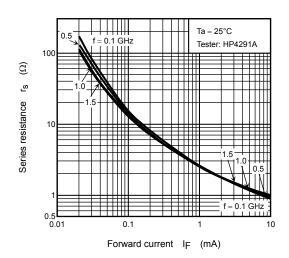
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Note2: This package has 2 exposed electrodes on each side because of the manufacturing process



TYPICAL PERFORMANCE CURVES (Ta = 25°C)





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RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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