TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74HCT04AP,TC74HCT04AF,TC74HCT04AFN

#### Hex Inverter

The TC74HCT04A is a high speed CMOS INVERTER fabricated with silicon gate C<sup>2</sup>MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

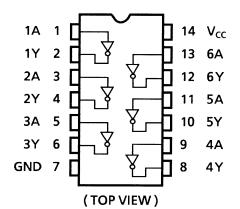
The internal circuit is composed of 3 stages including buffer output, which provide high noise immunity and stable output.

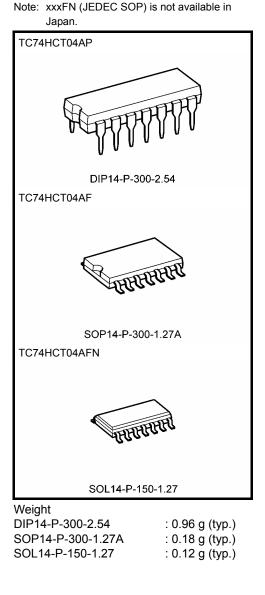
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### Features

- High speed:  $t_{pd} = 8 \text{ ns}$  (typ.) at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC}$  = 1  $\mu A$  (max) at Ta = 25°C
- Compatible with TTL outputs:  $V_{IH} = 2 V (min)$  $V_{IL} = 0.8 V (max)$
- Wide interfacing ability: LSTTL, NMOS, CMOS
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min)
- Balanced propagation delays:  $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74LS04

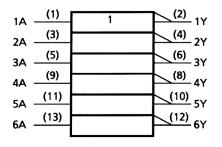
## **Pin Assignment**





## **TOSHIBA**

### **IEC Logic Symbol**



#### Truth Table

А	Y
L	Н
Н	L

## Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Supply voltage range	V <sub>CC</sub>	-0.5 to 7	V	
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V	
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5	V	
Input diode current	lıĸ	±20	mA	
Output diode current	Іок	±20	mA	
DC output current	IOUT	±25	mA	
DC V <sub>CC</sub> /ground current	Icc	±50	mA	
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

## **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	4.5 to 5.5	V
Input voltage	VIN	0 to V <sub>CC</sub>	V
Output voltage	Vout	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500	ns

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
			V <sub>CC</sub> (V)		Min	Тур.	Max	Min	Max	
High-level input voltage	V <sub>IH</sub>	—		4.5 to 5.5	2.0	_	_	2.0	_	V
Low-level input voltage	V <sub>IL</sub>	—		4.5 to 5.5	_	_	0.8	_	0.8	V
High-level output VOH	Veu	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -20 μA	4.5	4.4	4.5	_	4.4	_	v
	VОН		$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
Low-level output VOL	Max	V <sub>IN</sub>	$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	_	0.1	V
	$= V_{IH} \text{ or } V_{IL}$	$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	v	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	±0.1	_	±1.0	μA
	ICC	$V_{IN} = V_{CC}$ or GND		5.5	_	_	1.0	_	10.0	μA
Quiescent supply current	$I_{C} \qquad \begin{array}{c} \text{Per input: } V_{IN} = 0.\\ \text{Other input: } V_{CC} \end{array}$			5.5	_	—	2.0	_	2.9	mA

#### AC Characteristics ( $C_L = 15 \text{ pF}$ , $V_{CC} = 5 \text{ V}$ , $Ta = 25^{\circ}C$ , input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t <sub>TLH</sub>	—	_	6	12	ns
	t <sub>THL</sub>					110
Propagation delay time	t <sub>pLH</sub>	—	_	8	15	ns
	t <sub>pHL</sub>			,	.0	

#### AC Characteristics ( $C_L = 50 \text{ pF}$ , input: $t_r = t_f = 6 \text{ ns}$ )

Characteristics	Symbol Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit	
	- ,		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
Output transition time	t <sub>TLH</sub>		4.5	_	8	15		19	20
	t <sub>THL</sub>	—	5.5	_	7	13	_	16	ns
Propagation delay time	t <sub>pLH</sub>	_	4.5	_	11	18	_	23	20
	t <sub>pHL</sub>		5.5	—	9	16	—	20	ns
Input capacitance	CIN	—		_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>				20				۶E
	(Note)	_			20			_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

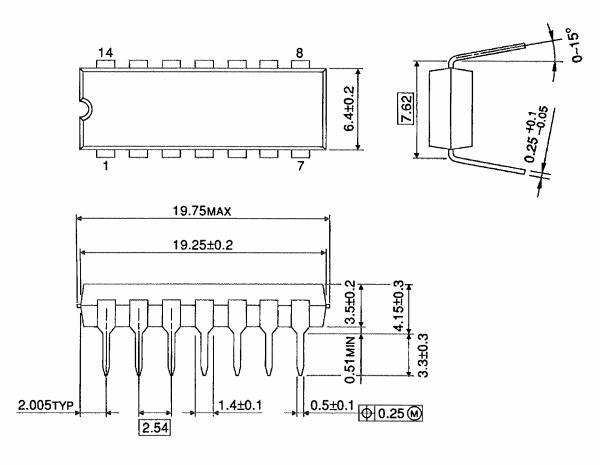
Average operating current can be obtained by the equation:

 $I_{CC}$  (opr) =  $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$  (per gate)

### **Package Dimensions**

DIP14-P-300-2.54

Unit : mm



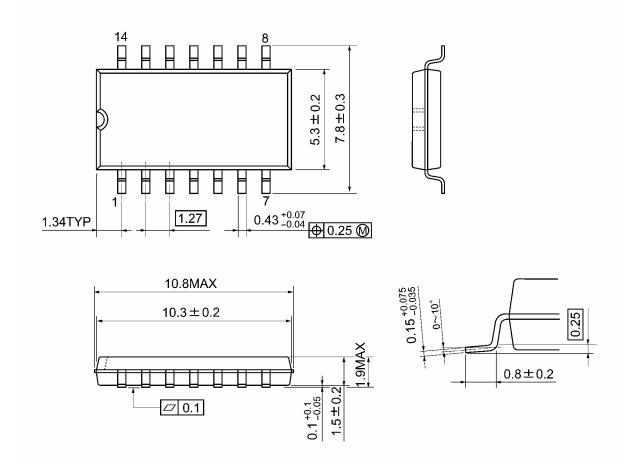
Weight: 0.96 g (typ.)



## **Package Dimensions**

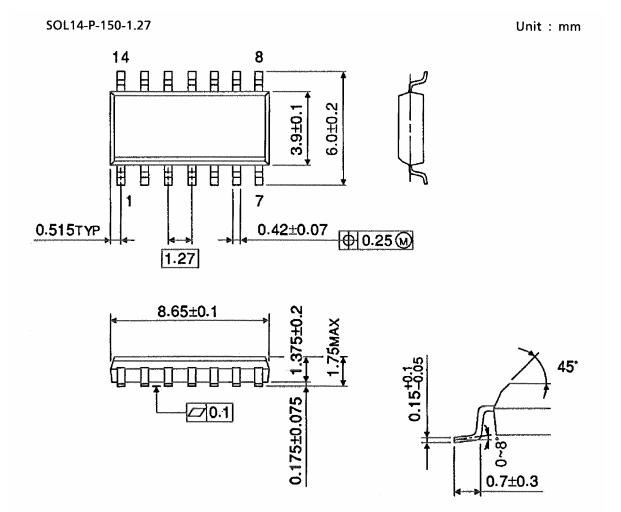
SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

## Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

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20070701-EN GENERAL

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