TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG86AFS

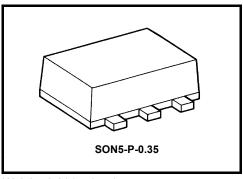
EXCLUSIVE OR GATE

Features

- High-level output current: $I_{OH}/I_{OL} = \pm 8$ mA (min) at $V_{CC} = 3.0$ V
- High-speed operation: $t_{pd} = 2.6 \text{ ns (typ.)}$

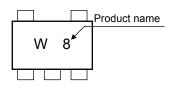
at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.5-V tolerant inputs.

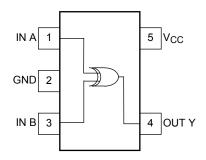


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~ V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20 (Note 1)	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65~150	°C

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

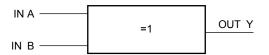
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

IEC Logic Symbol



Operating Ranges

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	0.9~3.6	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~V _{CC}	V
Output Current		±8.0 (Note 2)	
	I _{OH} /I _{OL}	±4.0 (Note 3)	
		±3.0 (Note 4)	mA
		±1.7 (Note 5)	MA
		±0.3 (Note 6)	
		±0.02 (Note 7)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dV	0~10 (Note 8)	ns/V

Note 2: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 3: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 4: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 5: $V_{CC} = 1.4 \sim 1.6 \text{ V}$

Note 6: V_{CC} = 1.1~1.3 V

Note 7: $V_{CC} = 0.9 \text{ V}$

Note 8: $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$

DC Electrical Characteristics

Characteristics	Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit	
Onaraciciosico Oyinboi 168		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
					V _{CC}		_	V _C C		V
High-level V _{IH} input voltage		V _{CC} × 0.7				_	V _{CC} × 0.7			
	_		1.4~1.6	V _{CC} × 0.65			V _{CC} × 0.65			
				V _{CC} × 0.65	l		V _{CC} × 0.65	١		
			2.3~2.7	1.7	_	—	1.7	_		
				3.0~3.6	2.0	_	_	2.0	_	
				0.9	_	_	GND	—	GND	
			1.1~1.3	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	٧	
Low-level	V_{IL}		_		_	_	V _{CC} × 0.35	_		V _{CC} × 0.35
input voltage				l		V _{CC} × 0.35	_	V _{CC} × 0.35		
							0.7		0.7	
				3.0~3.6			0.8		0.8	
		VIN = VIH	I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	V
			$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
High-level	V _{OH}		I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
output voltage	or VIL	I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45			
			I _{OH} = -4.0 mA	2.3~2.7	2.0		_	2.0		
			I _{OH} = -8.0 mA	3.0~3.6	2.48	_	_	2.48	_	
			I _{OL} = 0.02 mA	0.9	_	_	0.1	_	0.1	
Low-level Volument voltage		I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V	
	V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4~1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25		
	02	or VIL	I _{OL} = 3.0 mA	1.65~ 1.95	_	_	0.45	_	0.45	
			I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	
			I _{OL} = 8.0 mA	3.0~3.6	_	_	0.4	_	0.4	
Input leakage current	I _{IN}	V _{IN} = 0~5.5V		0~3.6			±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC}	or GND	3.6			1.0	_	10.0	μА

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AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns,}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
		$\begin{array}{l} C_L = 10 \ pF, \\ R_L = 1 \ M\Omega \end{array}$	0.9	_	19.2	_	_	_	
			1.1~1.3	_	9.8	19.5	1.0	36.3	
			1.4~1.6	_	5.7	9.0	1.0	10.6	
			1.65~ 1.95	_	4.4	6.6	1.0	7.1	
			2.3~2.7	_	3.0	4.1	1.0	4.7	
			3.0~3.6	_	2.4	3.3	1.0	3.9	
			0.9	_	21.5	_	_	_	
Propagation delay time	tplH tpHL	C_L = 15 pF, R_L = 1 M Ω	1.1~1.3	_	10.9	22.8	1.0	39.4	ns
			1.4~1.6	_	6.2	9.9	1.0	11.9	
			1.65~ 1.95	_	4.8	7.3	1.0	7.5	
			2.3~2.7	_	3.2	4.7	1.0	5.3	
			3.0~3.6	_	2.6	3.6	1.0	4.1	
		$C_L=30$ pF, $R_L=1$ M Ω	0.9	_	30.6	_		_	
			1.1~1.3	_	15.0	31.4	1.0	59.4	
			1.4~1.6	_	8.1	13.9	1.0	16.9	
			1.65~ 1.95	_	6.0	9.8	1.0	10.2	
			2.3~2.7	_	4.1	6.0	1.0	6.5	
			3.0~3.6	_	3.2	4.7	1.0	5.1	
Input capacitance	C _{IN}	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note9)	0.9~3.6	_	6	_	_	—	pF

Note 9: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

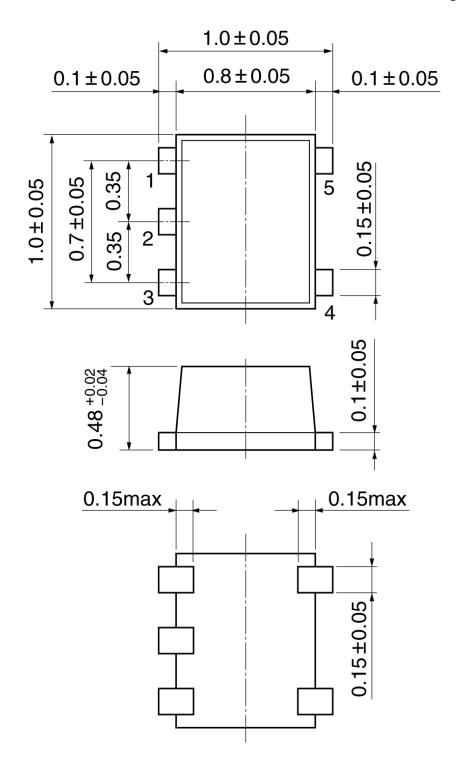
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Average operating current can be obtained by the equation:

 $I_{CC \ (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

SON5-P-0.35 Unit:mm



Weight: 0.001 g (typ.)

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20070701-EN GENERAL

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