TOSHIBA

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

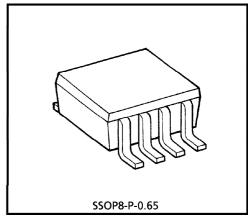
T C 7 W 1 2 6 F U

DUAL BUS BUFFER

The TC7W126FU is a high speed C²MOS DUAL BUS BUFFERS fabricated with silicon gate C²MOS technology. It achieve the high speed operation similar to equivalent LSTTL while maintaining the C²MOS low power dissipation.

The require 3-state control input G to be set low to place the output into the high impedance.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.



Weight: 0.02g (Typ.)

FEATURES

• High Speed \cdots $t_{pd} = 10$ ns (Typ.) at $V_{CC} = 5V$

• Low Power Dissipation……… $I_{CC} = 2\mu A$ (Max.) at $T_a = 25^{\circ}C$

• High Noise Immunity VNIH = VNIL = 28% VCC (Min.)

• Output Drive Capability 15 LSTTL Loads

• Symmetrical Output Impedance ··· |IOH| = IOL = 6mA (Min.)

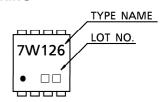
ullet Balanced Propagation Delays $\cdots \cdots t_{pLH} \dot{=} t_{pHL}$

Wide Operating Voltage Range ··· V_{CC} (opr) = 2∼6V

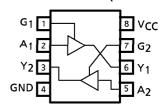
MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage Range	Vcc	-0.5~7	V
DC Input Voltage	VIN	-0.5~V _{CC} +0.5	V
DC Output Voltage	Vout	-0.5~V _{CC} +0.5	٧
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	^I ок	± 20	mA
DC Output Current	IOUT	± 35	mA
DC V _{CC} /Ground Current	lcc	± 37.5	mA
Power Dissipation	PD	300	mW
Storage Temperature	T _{stg}	- 65∼150	°C
Lead Temperature (10s)	TL	260	°C

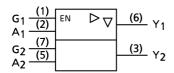
MARKING



PIN ASSIGNMENT (TOP VIEW)



LOGIC DIAGRAM



TRUTH TABLE

INP	UTS	OUTPUTS				
G	Α	Υ				
L	Х	Z				
Н	L	L				
Н	Н	Н				

X : Don't Care Z : High Impedance

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	Vcc	2~6	V
Input Voltage	VIN	0~V _{CC}	V
Output Voltage	VOUT	0~V _{CC}	V
Operating Temperature	T _{opr}	-40~85	°C
		$0 \sim 1000 \ (V_{CC} = 2.0V)$	
Input Rise and Fall Time	t _r , t _f	$0 \sim 500 \ (V_{CC} = 4.5V)$	ns
		$0 \sim 400 \ (V_{CC} = 6.0V)$	

DC ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CIR- CUIT	TEST CONDITION V			Ta = 25°C			Ta = −40~85°C		
PARAIVIETER	STIVIBUL				Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
High-Level Input					2.0	1.5	_	_	1.5	_	
Voltage	V _{IH}	—		_	4.5	3.15	_	—	3.15	—	V
Voltage					6.0	4.2	_	_	4.2	_	
Low-Level Input			_		2.0	_	_	0.5	—	0.5	
Voltage	V _{IL}	—			4.5	_	_	1.35	—	1.35	V
Voltage					6.0		_	1.8	_	1.8	
			V _{IN} = V _{IH}	I _{OH} = -20μA	2.0	1.9	2.0	<u> </u>	1.9	—	V
High-Level					4.5	4.4	4.5	 	4.4	—	
Output Voltage	Voн	—			6.0	5.9	6.0	_	5.9	_	
Output voitage				$I_{OH} = -6mA$	4.5	4.18	4.31	_	4.13	—	
				$I_{OH} = -7.8 mA$	6.0	5.68	5.80	_	5.63	_	
	VOL		V _{IN} = V _{IH}	I _{OL} = 20μA	2.0		0.0	0.1	—	0.1	
Low-Level Output Voltage					4.5		0.0	0.1	—	0.1	
		—			6.0		0.0	0.1	_	0.1	V
Output voltage			O VIL	$I_{OL} = 6mA$	4.5		0.17	0.26	—	0.33	
				$I_{OL} = 7.8 \text{mA}$	6.0		0.18	0.26	_	0.33	
3-State Output Off-State Current	loz	_	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		6.0	_	ı	± 0.5	_	± 5.0	
Input Leakage Current	I _{IN}	_	V _{IN} = V _{CC} or GND		6.0	_	_	± 0.1	_	± 1.0	μΑ
Quiescent Supply Current	lcc	_	V _{IN} = V _{CC} or GND		6.0		_	2.0		20.0	

2 2001-05-31

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 6ns$)

DADAMETED	CVMADOL	TEST	TEST			Ta = 25°C			Ta = −40~85°C		
PARAMETER SYMBO	SYMBOL	CIR- CUIT	CONDITION	C_L	VCC	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Output Transition	t				2.0	_	20	60	_	75	
Time	t _{TLH}	—	_	50	4.5	_	6	12	 	15	
	^t THL				6.0		5	10	_	13	
				50	2.0	<u> </u>	30	90	—	115	
					4.5	<u> </u>	11	18	—	23	
Propagation	^t PLH		_		6.0	_	10	15	_	20	
Delay Time	^t pHL				2.0	<u> </u>	42	130	—	165	
	·			150	4.5	_	14	26	—	33	
					6.0	_	12	22	_	28	ns
	^t pZL _ ^t pZH		— R _L = 1kΩ	50	2.0		30	90	_	115	113
Output Enable Time		_			4.5	_	11	18	—	23	
					6.0		10	15	_	20	
				150	2.0		42	130	_	165	
					4.5	_	14	26	—	33	
					6.0		12	22	_	28	
Output Disable	t _{pLZ} —		$ R_L = 1k\Omega$	50	2.0		24	100	_	125	
Time		—			4.5		12	20	_	25	
					6.0		10	17	_	21	
Input Capacitance	C _{IN}	_	_	_	_	_	5	10	_	10	
Output Capacitance	COUT	_	_				10	_			рF
Power Dissipation Capacitance	C _{PD}	_	Note (1)	_	_	_	32	_	_	_	

Note (1): CpD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

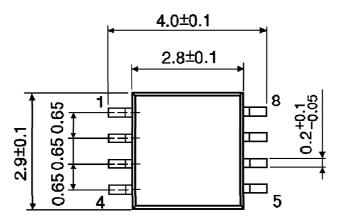
Average operating current can be obtained by the equation:

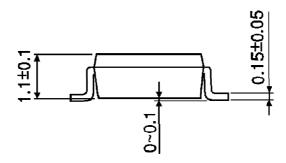
ICC (opr) = CpD·VCC·fIN + ICC / 2 (per Gate)

3 2001-05-31

PACKAGE DIMENSIONS

SSOP8-P-0.65 Unit: mm





Weight: 0.02g (Typ.)

4 2001-05-31

RESTRICTIONS ON PRODUCT USE

000707EBA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- ◆ The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.