

1200mW Audio Power Amp with Shutdown

Features

- Operating voltage: 2.2V to 5.5V
- High signal-to-noise ratio
- Low distortion
- · Large output voltage swing
- Low power consumption
- Output power 1200mW at 10% THD+N into 8Ω (V_{DD} =5V)
- Wide temperature operating range
- Low power-on and chip enable or disable POP noise.
- Low standby current
- Power off control
- · Direct drive speaker
- 8-pin DIP/SOP package

Applications

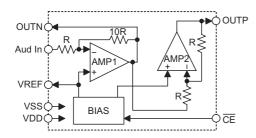
 Applied for HT36 series, HT86 series and other Holtek products

General Description

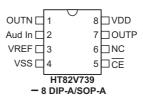
HT82V739 is an integrated class AB mono speaker driver contained in a 8-pin DIP/SOP package. The HT82V739 is capable of delivering 1200mW output power to an 8Ω load with less than 10% (THD+N) from a

5V power supply. The very low standby current in shutdown mode contributes to the reduction of power consumption of battery-powered equipments.

Block Diagram



Pin Assignment



Pin Description

Pin No.	Pin Name	I/O	Description	
1	OUTN	0	Negative output	
2	Aud In	I	Audio input	
3	VREF	0	Speaker non-inverting input voltage reference	
4	VSS	_	Negative power supply, ground	
5	CE	I	Chip enable, low active	
6	NC	_	Not connected	
7	OUTP	0	Positive output	
8	VDD	_	Positive power supply	



Absolute Maximum Ratings

Supply VoltageV _{SS} -0.3V to V _{SS} +6.0V	Storage Temperature50°C to 125°C
Input VoltageV _{SS} -0.3V to V _{DD} +0.3V	Operating Temperature40°C to 85°C

Note: These are stress ratings only. Stresses exceeding the range specified under "Absolute Maximum Ratings" may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Electrical Characteristics

V_{SS}=0V, Ta=25°C

Ch.al	Darameter		Test Conditions			T	Max	11-:4
Symbol	Parameter	V_{DD}	/ _{DD} Conditions		Min.	Тур.	Max.	Unit
D.C. Charac	cteristics	•						
V _{DD}	Supply Voltage		_		2.2	5.0	5.5	V
	Quiescent Power Supply	3V	V _{IN} =0V _{P-P} , No load		_	2.2	4.0	mA
I _{DD}	Current	5V			_	3.5	6.0	mA
I _{SD}	Shutdown Power Supply Current	5V	V_{IN} =0 V_{P-P} , \overline{CE} = V_{DD} , N	o load		_	1	μА
V _{IH}	Input High Voltage for CE	_	_		0.7V _{DD}	_	V _{DD}	V
V_{IL}	Input Low Voltage for CE	_	_		0	_	0.3V _{DD}	V
			(THD+N)/S≤1%, V _{IN} =1kHz sinewave	$R_L=4\Omega$	198	330	_	mW
				R _L =8Ω	180	300		
		2) /		R _L =16Ω	144	240	_	
	Output Power	3V	(THD+N)/S≤10%, V _{IN} =1kHz sinewave	$R_L=4\Omega$	270	450		
				R _L =8Ω	240	400	_	
Po				R _L =16Ω	168	280		
Γ0			(THD+N)/S≤1%, V _{IN} =1kHz sinewave	$R_L=4\Omega$	690	1150	_	mW
				R _L =8Ω	570	950		
		<i></i>		R _L =16Ω	390	650		
		5V	(THD+N)/S≤10%, V _{IN} =1kHz sinewave	$R_L=4\Omega$	840	1400	_	
				R _L =8Ω	720	1200		
			- 110	R _L =16Ω	480	800		
A.C. Charac	cteristics							
t	Enable Time	3V	V _{IN} =1kHz sinewave, No load		_	145		μS
t _{ON}	Chable fille	5V			_	105		μS
(THD+N)/S	Total Harmonic Distortion Plus Noise-to-signal Ratio			$R_L=4\Omega$	_	0.3	_	%
		5V	Power output=500mW, V _{IN} =1kHz sinewave	R _L =8Ω	_	0.18		%
	Troise to digital realio		in i	R _L =16Ω	_	0.13	_	%
	Signal to Noise Ratio	5V		$R_L=4\Omega$	_	66		dB
S/N			V _{IN} =1Vrms 1kHz sinewave	R _L =8Ω	_	70	_	dB
				R _L =16Ω	_	72	_	dB

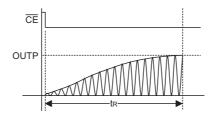
Rev. 1.20 2 January 17, 2008



Functional Description

OUTP Rising Time (t_R)

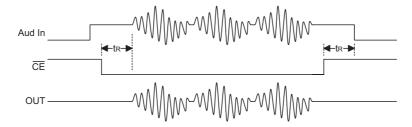
When $\overline{\text{CE}}$ is active low, the HT82V739 needs rising time to output fully on OUTP pin. However, the rising time depends on C1. (*see the application circuits)



Capacitor t _R	0.1 μF	1μF	4.7 μF	10 μF
2.2V	15ms	30ms	90ms	185ms
3V	15ms	30ms	90ms	185ms
4V	15ms	30ms	90ms	185ms

For battery based applications, power consumption is a key issue, therefore the amplifier should be turned off when in the standby state. In order to eliminate any speaker sound bursts while turning the amplifier on, the application circuit, which will incorporate a capacitance value of C1, should be adjusted in accordance with the speaker's audio frequency response. A greater value of C1 will improve the noise burst while turning on the amplifier. The recommended operation sequence is:

Turn On: "Aud In" signal standby (1/2 VDD) \rightarrow enable amplifier \rightarrow wait t_R for amplifier ready \rightarrow "Aud In" signal start Turn Off: "Aud In" signal finish \rightarrow disable amplifier \rightarrow wait t_R for amplifier off \rightarrow "Aud In" signal off



If the application is not powered by batteries and there is no problem with amplifier On/Off issue, a capacitor value of $0.1\mu F$ for C1 is recommended.

Rev. 1.20 3 January 17, 2008



THD+N VS. Output Power

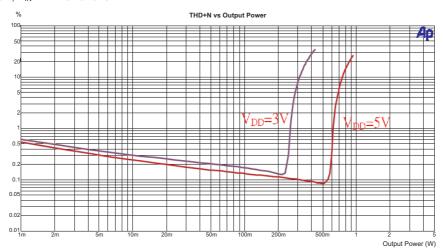
• R_{LOAD} =4 Ω , V_{IN} =1kHz sinewave



• R_{LOAD} =8 Ω , V_{IN} =1kHz sinewave

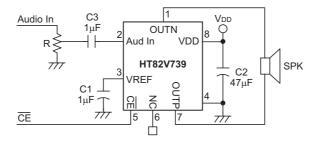


• R_{LOAD} =16 Ω , V_{IN} =1kHz sinewave





Application Circuits



Rev. 1.20 5 January 17, 2008



Package Information

8-pin DIP (300mil) Outline Dimensions





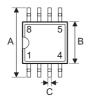


Cumbal	Dimensions in mil				
Symbol	Min.	Nom.	Max.		
А	355	_	375		
В	240	_	260		
С	125	_	135		
D	125	_	145		
E	16	_	20		
F	50	_	70		
G	_	100	_		
Н	295	_	315		
I	335	_	375		
α	0°	_	15°		

Rev. 1.20 6 January 17, 2008



8-pin SOP (150mil) Outline Dimensions





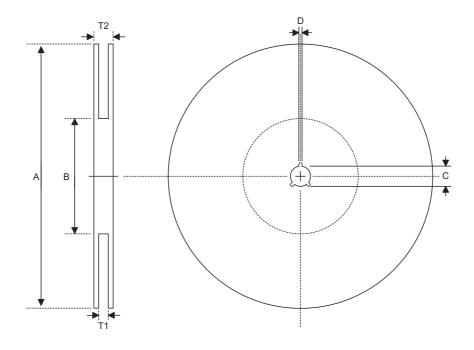


Symbol	Dimensions in mil				
	Min.	Nom.	Max.		
Α	228	_	244		
В	149	_	157		
С	14	_	20		
C'	189	_	197		
D	53	_	69		
E	_	50			
F	4	_	10		
G	22	_	28		
Н	4	_	12		
α	0°	_	10°		



Product Tape and Reel Specifications

Reel Dimensions

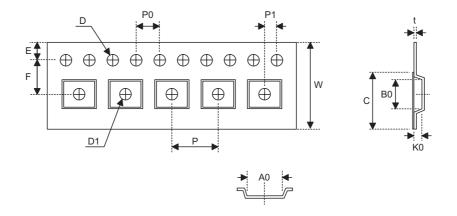


SOP 8N

Symbol	Description	Dimensions in mm
Α	Reel Outer Diameter	330±1.0
В	Reel Inner Diameter	62±1.5
С	Spindle Hole Diameter	13.0+0.5 -0.2
D	Key Slit Width	2.0±0.15
T1	Space Between Flange	12.8+0.3 -0.2
T2	Reel Thickness	18.2±0.2



Carrier Tape Dimensions



SOP 8N

Symbol	Description	Dimensions in mm
W	Carrier Tape Width	12.0+0.3 -0.1
Р	Cavity Pitch	8.0±0.1
Е	Perforation Position	1.75±0.1
F	Cavity to Perforation (Width Direction)	5.5±0.1
D	Perforation Diameter	1.55±0.1
D1	Cavity Hole Diameter	1.5+0.25
P0	Perforation Pitch	4.0±0.1
P1	Cavity to Perforation (Length Direction)	2.0±0.1
A0	Cavity Length	6.4±0.1
В0	Cavity Width	5.20±0.1
K0	Cavity Depth	2.1±0.1
t	Carrier Tape Thickness	0.3±0.05
С	Cover Tape Width	9.3



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Rev. 1.20 January 17, 2008