

RS-485 Transceiver with Integrated Low-Dropout Regulator and AutoDirection Control

General Description

The MAX13410E–MAX13415E are half-duplex RS-485-/RS-422-compatible transceivers optimized for isolated applications. These devices feature an internal low-dropout regulator (LDO), one driver, and one receiver. The internal LDO allows the part to operate from an unregulated power supply of up to 28V. The AutoDirection feature reduces the number of optical isolators needed in isolated applications. Other features include enhanced ESD protection, fail-safe circuitry, slew-rate limiting, and fullspeed operation.

The MAX13410E–MAX13415E internal LDO generates a 5V \pm 10% power supply that is used to power its internal circuitry. The MAX13412E–MAX13415E bring the 5V to an output V_{REG} that allows the user to power additional external circuitry with up to 20mA to further reduce external components. The MAX13410E/MAX13411E do not have a 5V output and come in industry-compatible pinouts. This allows easy replacement in existing designs.

The MAX13410E–MAX13415E feature a 1/8-unit load receiver input impedance, allowing up to 256 transceivers on the bus. All driver outputs are ESD protected using the Human Body Model. These devices also include fail-safe circuitry (MAX13410E/MAX13411E/MAX13414E/MAX13415E only), guaranteeing a logic-high receiver output when the receiver inputs are open or shorted. The receiver outputs a logic-high when the transmitter on the terminated bus is disabled (high impedance).

The MAX13412E/MAX13413E feature Maxim's proprietary AutoDirection control. This architecture eliminates the need for the DE and RE control signals. In isolated applications, this reduces the cost and size of the system by reducing the number of optical isolators required.

The MAX13410E/MAX13412E/MAX13414E feature reduced slew-rate drivers that minimize EMI and reduce reflections caused by improperly terminated cables, allowing error-free transmission up to 500kbps. The MAX13411E/MAX13413E/MAX13415E are not slew-rate limited, allowing transmit speeds up to 16Mbps.

The MAX13410E–MAX13415E are available in an 8-pin SO package with an exposed paddle to improve power dissipation, and operate over the extended -40°C to +85°C temperature range.

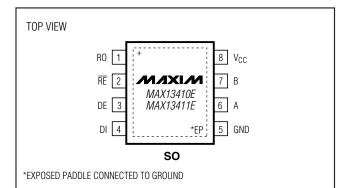
_Features

- Wide +6V to +28V Input Supply Range
- +5V Output Supplies Up to 20mA to External Circuitry
- Internal LDO
- ♦ Low 65µA (typ) Shutdown Supply Current
- Extended ESD Protection ±15kV Human Body Model (MAX13412E/ MAX13413E) ±14kV Human Body Model (MAX13410E/ MAX13411E)
- 1/8-Unit Load, Allowing Up to 256 Transceivers on the Bus
- ◆ -40°C to +85°C Operating Temperature Range
- ♦ Fail-Safe
- Slew-Rate Limited and Full-Speed Versions
- Up to 16Mbps Data Rate on Full-Speed Versions

Applications

Isolated RS-485 InterfacesIndustrial EquipmentUtility MetersTelecomm Equipment

Pin Configurations



Pin Configurations continued at end of data sheet.

Ordering Information/Selector Guide

PART	ART PIN-PACKAGE AutoDirection DATA RA		DATA RATE (max)	SLEW-RATE LIMITED	PKG CODE
MAX13410EESA+	8 SO-EP*	No	500kbps	Yes	S8E+14
MAX13411EESA+	8 SO-EP*	No	16Mbps	No	S8E+14

Note: All devices operate over the -40°C to +85°C operating temperature range.

+Denotes a lead-free package.

*EP = Exposed paddle.

Ordering Information/Selector Guide continued at end of data sheet.

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

(All voltages referenced to GND.)

V _{CC} 0.3V to +30V
RE, DE/RE, DE, DI, RO, V _{REG} 0.3V to +6V
A, B8V to +13V
Short-Circuit Duration (RO, A, B) to GND Continuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
8-Pin SO-EP (derate 19.2mW/°C above +70°C)1539mW

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Junction Temperature	+150°C
θ _{JA} (Note 1)	52.0°C/W
θ _{JC} (Note 1)	6.0°C/W
Lead Temperature (soldering, 10s)	+300°C

Note 1: Package thermal resistances were obtained using the method described in JEDEC specificactions JESD51-7 using a four layer board. For detailed information on package consitencies refer to www.maxim-ic/thermal-tutorial.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu\text{F}, \text{ and } T_A = +25^{\circ}\text{C.})$ (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Supply Voltage	Vcc	(Note 3)	6.0		28.0	V
		$V_{CC} = +7.5V, I_{LOAD} = 20mA$	4.5	5	5.5	N
LDO Output Voltage	VREG	$V_{CC} = +28V$, $I_{LOAD} = 0mA$	4.5	5	5.5	V
LDO Output Current	I _{REG}	$V_{CC} > +7.5V$			20	mA
LDO Dropout Voltage	V _{DO}	$V_{CC} = +5V$, $I_{OUT} = 20mA$		0.5		V
Minimum Bypass Capacitor on V _{REG}	CS	Guaranteed by design, MAX13412E-MAX13415E	1			μF
Supply Current		RE, DE = high/no load (MAX13410E/MAX13411E)			10	
	ICC	RE, DE/RE = high, DI = low/no load (MAX13412E-MAX13415E)			10	mA
Shutdown Current	ISHDN	DE = low,		45		μA
Thermal-Shutdown Threshold	T _{TS}			+150		°C
Thermal-Shutdown Threshold Hysteresis	T _{TSH}			15		°C
DRIVER			•			•
		$R_{DIFF} = 100\Omega$, Figure 1	2.0		5.5	
Differential Driver Output	Vod	$R_{DIFF} = 54\Omega$, Figure 1	1.5		5.5	V
		No load			5.5	
Change in Magnitude of Differential Output Voltage	ΔV _{OD}	$R_{DIFF} = 100\Omega$ or 54Ω , Figure 1			0.2	V
Driver Common-Mode Output Voltage	Voc	$R_{DIFF} = 100\Omega$ or 54Ω , Figure 1	1		3	V
Change In Magnitude of Common- Mode Voltage	ΔV _{OC}	$R_{DIFF} = 100\Omega \text{ or } 54\Omega, Figure 1$			0.2	V
Input High Voltage	VIH	DI, DE, RE, DE/RE	2.0			V
Input Low Voltage	VIL	DI, DE, RE, DE/RE			0.8	V
Input Current	I _{IN}	DI, DE, RE, DE/RE			±1	μA
Driver-Disable Threshold	VDT	T _A = +25°C (MAX13412E/MAX13413E)	0.6		1.0	V

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	COND	ITIONS	MIN	ΤΥΡ	MAX	UNITS
Driver Chart Circuit Output Ourrent	10.00	$0V \le V_{OUT} \le +12V$				+250	mA
Driver Short-Circuit Output Current	IOSD	$-7V \le V_{OUT} \le 0V$		-250			ШA
Driver Short-Circuit-Foldback Output	10005	$(V_{CC} - 1V) \leq V_{OUT} \leq$	<u><</u> +12V	20			~ ^
Current	IOSDF	$-7V \le V_{OUT} \le 0V$				-20	mA
RECEIVER							
Input Current (A and B)		RE, DE, DE/RE =	$V_{IN} = +12V$			125	μA
	IA, B	$GND, V_{CC} = GND$	$V_{IN} = -7V$	-100			μΑ
Receiver Differential Threshold Voltage		-7V <u><</u> V _{CM} <u><</u> +12V (MAX13410E/MAX13	3411E)	-200		-50	mV
	V _{TH}	$-7V \le V_{LM} \le +12V$ (MAX13412E/MAX13	3413E)	-100		100	
Receiver Input Hysteresis	ΔV_{TH}	$V_A + V_B = 0V$			15		mV
Output High Voltage	Vон	$I_{O} = -1mA, V_{A} - V_{B}$	≥ V _{TH}	V _{REG} - 0.6	i		V
Output Low Voltage	V _{OL}	$I_{O} = +1mA$, $V_{A} - V_{B}$	≤ -V _{TH}			0.4	V
Three-State Output Current at Receiver	Iozr	$0 \le V_O \le V_{REG}$			0.01	±1	μΑ
Receiver-Input Resistance	R _{IN}	$-7V \leq V_{CM} \leq +12V$		96			kΩ
Receiver-Output Short-Circuit Current	IOSR	$0V \leq V_{RO} \leq V_{REG}$		±8		±95	mA
ESD PROTECTION							
ESD Protection (A, B)		Human Body Model (MAX13412E/MAX13			±15		kV
ESD Protection (A, B)		Human Body Model (MAX13410E/MAX1			±14		kV
ESD Protection (All Other Pins)		Human Body Model			±2		kV

MAX13410E-MAX13415E

SWITCHING CHARACTERISTICS-MAX13410E

(V_{CC} = +6.0V to +28V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +7.5V, C_S = 1µF, and T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
DRIVER						
Driver Propagation Dalay	t _{DPLH}	$R_{DIFF} = 54\Omega$, $C_L = 50pF$,	150		1000	20
Driver Propagation Delay	t _{DPHL}	Figures 2a and 3a	150		1000	ns
Driver Differential Output	t _{HL}	$R_{DIFF} = 54\Omega$, $C_L = 50pF$,	250		900	
Rise or Fall Time	tLH	Figures 2a and 3a	250		900	ns
Driver Differential Output Skew tDPLH - tDPHL	^t DSKEW	$R_{DIFF} = 54\Omega$, $C_L = 50pF$, Figures 2a and 3a			140	ns
Maximum Data Rate	fMAX		500			kbps
Driver Enable from Shutdown to Output High	tdzh(SHDN)	S2 closed, Figure 4, $R_L = 500\Omega$, $C_L = 100pF$			11	μs
Driver Enable from Shutdown to Output Low	tdzl(SHDN)	S2 closed, Figure 4, $R_L = 500\Omega$, $C_L = 100pF$			6	μs



3

SWITCHING CHARACTERISTICS-MAX13410E (continued)

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
Driver Enable to Output High	^t DZH	S2 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			2500	ns
Driver Enable to Output Low	tdzl	S1 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			2500	ns
Driver Disable from Output High	t _{DHZ}	S2 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			100	ns
Driver Disable from Output Low	tDLZ	S1 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			100	ns
Time to Shutdown	t SHDN		50	340	700	ns
RECEIVER						
Receiver Propagation Delay	t _{RPLH}	$C_{\rm L} = 15 \rm nE (at PO)$ Eiguroa 5 and 6			200	ns
neceiver Fropagation Delay	t _{RPHL}	$C_L = 15pF$ (at RO), Figures 5 and 6			200	115
Receiver Output Skew	t RSKEW	C _L = 15pF (at RO), Figures 5 and 6			30	ns
Maximum Data Rate	fMAX		500			kbps
Receiver Enable to Output High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable to Output Low	t _{RZL}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Disable Time from High	trzh	S2 closed, Figure 7, $C_L = 15pF$			50	ns
Receiver Disable Time from Low	t _{RLZ}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable from Shutdown to Output High	^t RZH(SHDN)	S2 closed, Figure 7, $C_L = 15pF$			14	μs
Receiver Enable from Shutdown to Output Low	^t RZL(SHDN)	S1 closed, Figure 7, $C_L = 15pF$			3.5	μs

SWITCHING CHARACTERISTICS-MAX13411E

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
DRIVER						
Driver Propagation Delay	t DPLH	$R_{DIFF} = 54\Omega$, $C_L = 50pF$, Figures 2a			50	ns
Driver Propagation Delay	t DPHL	and 3a			50	115
Driver Differential Output	tHL	$R_{DIFF} = 54\Omega$, $C_L = 50pF$, Figures 2a			15	
Rise or Fall Time	t _{LH}	and 3a			15	ns
Driver Differential Output Skew tDPLH - tDPHL	^t DSKEW	R_{DIFF} = 54 Ω , C_{L} = 50pF, Figures 2a and 3a			8	ns
Maximum Data Rate	fMAX		16			Mbps
Driver Enable from Shutdown to Output High	tDZH(SHDN)	S2 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			11	μs
Driver Enable from Shutdown to Output Low	^t DZL(SHDN)	S2 closed, Figure 4, R _L = 500 Ω , C _L = 100pF			6	μs
Driver Enable to Output High	tdzh	S2 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			70	ns

M/IXI/M

SWITCHING CHARACTERISTICS-MAX13411E (continued)

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
Driver Enable to Output Low	tDZL	S1 closed, Figure 4, R _L = 500 Ω , C _L = 100pF			70	ns
Driver Disable from Output High	t _{DHZ}	S2 closed, Figure 4, $R_L = 500\Omega$, $C_L = 100pF$			50	ns
Driver Disable from Output Low	tDLZ	S1 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			50	ns
RECEIVER						
	t _{RPLH}	C 15pE (at DO) Figures 5 and 6			75	
Receiver Propagation Delay	t RPHL	$C_L = 15 pF$ (at RO), Figures 5 and 6			75	ns
Receiver Output Skew	t RSKEW	$C_L = 15 pF$ (at RO), Figures 5 and 6			8	ns
Maximum Data Rate	fMAX		16			Mbps
Receiver Enable to Output High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable to Output Low	t _{RZL}	S1 closed, Figure 7, $C_L = 15pF$			50	ns
Receiver Disable Time from High	trzh	S2 closed, Figure 7 , $C_L = 15 pF$			50	ns
Receiver Disable Time from Low	t _{RLZ}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable from Shutdown to Output High	trzh(SHDN)	S2 closed, Figure 7, $C_L = 15pF$			14	μs
Receiver Enable from Shutdown to Output Low	^t RZL(SHDN)	S1 closed, Figure 7, C _L = 15pF			3.5	μs

SWITCHING CHARACTERISTICS-MAX13412E

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
DRIVER						
Driver Propagation Delay	t _{DPLH}	$R_L = 110\Omega$, $C_L = 50pF$, Figures 2b	200		1000	20
	t DPHL	and 3b	200		1000	ns
Driver Differential Output	tHL	$R_L = 110\Omega$, $C_L = 50pF$, Figures 2b	250		900	ns
Rise or Fall Time	t _{LH}	and 3b	250		900	
Maximum Data Rate	fMAX		500			kbps
Driver Disable Delay	tDDD	$R_L = 110\Omega$, $C_L = 50pF$, Figure 3b			2500	ns
RECEIVER						
Resolver Propagation Delay	t _{RPLH}	C: 15pE Figures 5 and 6			200	
Receiver Propagation Delay	t _{RPHL}	$C_L = 15 pF$, Figures 5 and 6			200	ns
Receiver Output Skew	t RSKEW	C_L = 15pF, Figures 5 and 6			30	ns
Maximum Data Rate	fMAX		500			kbps
Receiver Enable to Output High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable to Output Low	t _{RZL}	S1 closed, Figure 7, $C_L = 15pF$			50	ns

SWITCHING CHARACTERISTICS-MAX13412E (continued)

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Receiver Disable Time from Low	t _{RLZ}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Disable Time from High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable Delay	tRED	$R_L = 110\Omega$, $C_L = 50pF$, Figure 3			2500	ns

SWITCHING CHARACTERISTICS-MAX13413E

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
DRIVER						
Driver Propagation Delay	t _{DPLH}	$R_L = 110\Omega$, $C_L = 50pF$, Figures 2b			50	ns
	t DPHL	and 3b			50	115
Driver Differential Output	t _{HL}	$R_L = 110\Omega$, $C_L = 50pF$, Figures 2b			15	ns
Rise or Fall Time	tLH	and 3b			15	115
Maximum Data Rate	fMAX		16			Mbps
Driver Disable Delay	tDDD	$R_L = 110\Omega$, $C_L = 50pF$, Figure 3b			70	ns
RECEIVER						
Receiver Propagation Delay	t _{RPLH}	$C_{\rm L} = 15 \rm pF$, Figures 5 and 6			80	20
	t RPHL	$C_{L} = 13 \text{pr}, \text{Figures 5 and 6}$			80	ns
Receiver Output Skew	t RSKEW	$C_L = 15 pF$, Figures 5 and 6			13	ns
Maximum Data Rate	fMAX		16			Mbps
Receiver Enable to Output High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable to Output Low	t _{RZL}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Disable Time from Low	t _{RLZ}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Disable Time from High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns
Receiver Enable Delay	tRED	$R_L = 110\Omega$, Figure 3, $C_L = 50pF$			70	ns

SWITCHING CHARACTERISTICS-MAX13414E

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	SYMBOL CONDITIONS		ТҮР	MAX	UNITS
DRIVER						
Driver Propagation Delay	t _{DPLH}	$R_{DIFF} = 54\Omega$, $C_{L} = 50pF$, Figures 2a	200		1000	ns
	t DPHL	and 3a	200		1000	
Driver Differential Output	tHL	$R_{DIFF} = 54\Omega$, $C_{L} = 50pF$, Figures 2a	250		900	
Rise or Fall Time	tLH	and 3a	250		900	ns
Driver Differential Output Skew tDPLH - tDPHL	^t DSKEW	R_{DIFF} = 54 $\Omega,$ C_{L} = 50pF, Figures 2a and 3a			140	ns

SWITCHING CHARACTERISTICS-MAX13414E (continued)

 $(V_{CC} = +6.0V \text{ to } +28V, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } V_{CC} = +7.5V, C_S = 1\mu$ F, and $T_A = +25^{\circ}$ C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
Maximum Data Rate	fMAX		500			kbps	
Driver Enable to Output High	t _{DZH}	S2 closed, Figure 4, RL = 500Ω CL = $100pF$			2500	ns	
Driver Enable to Output Low	t _{DZL}	S1 closed, Figure 4, RL = 500Ω CL = $100pF$			2500	ns	
Driver Disable from Output High	t _{DHZ}	S2 closed, Figure 4, $R_L = 500\Omega$, $C_L = 100pF$			100	ns	
Driver Disable from Output Low	tDLZ	S1 closed, Figure 4, R _L = 500Ω , C _L = $100pF$			100	ns	
RECEIVER							
Pageiver Propagation Dalay	t _{RPLH}	C: 15pE (at DO) Figures 5 and 6			200		
Receiver Propagation Delay	t _{RPHL}	$C_L = 15$ pF (at RO), Figures 5 and 6			200	ns	
Receiver Output Skew	t RSKEW	C_L = 15pF (at RO), Figures 5 and 6			30	ns	
Maximum Data Rate	fMAX		500			kbps	
Receiver Enable to Output High	trzh	S2 closed, Figure 7, $C_L = 15pF$			50	ns	
Receiver Enable to Output Low	trzl	S1 closed, Figure 7, $C_L = 15pF$			50	ns	
Receiver Disable Time from Low	trlz	S1 closed, Figure 7, $C_L = 15pF$			50	ns	
Receiver Disable Time from High	t _{RZH}	S2 closed, Figure 7, $C_L = 15 pF$			50	ns	

SWITCHING CHARACTERISTICS-MAX13415E

(V_{CC} = +6.0V to +28V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +7.5V, C_S = 1µF, and T_A = +25°C.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
DRIVER						
Driver Propagation Delay	t _{DPLH}	$R_{DIFF} = 54\Omega$, $C_{L} = 50pF$, Figures 2a			50	ns
Driver Propagation Delay	t DPHL	and 3a			50	115
Driver Differential Output	t _{HL}	$R_{DIFF} = 54\Omega$, $C_{L} = 50pF$, Figures 2a			15	ns
Rise or Fall Time	tLH	and 3a			15	115
Driver Differential Output Skew tDPLH - tDPHLI	t DSKEW	R_{DIFF} = 54 $\Omega,$ C_{L} = 50pF, Figures 2a and 3a			8	ns
Maximum Data Rate	fMAX		16			Mbps
Driver Enable to Output High	tdzh	S2 closed, Figure 4, R _L = 500Ω , C _L = $15pF$			70	ns
Driver Enable to Output Low	tdzl	S1 closed, Figure 4, R _L = 500Ω , C _L = $15pF$			70	ns
Driver Disable from Output High	t _{DHZ}	S2 closed, Figure 4, R _L = 500Ω , C _L = $15pF$			50	ns

SWITCHING CHARACTERISTICS-MAX13415E (continued)

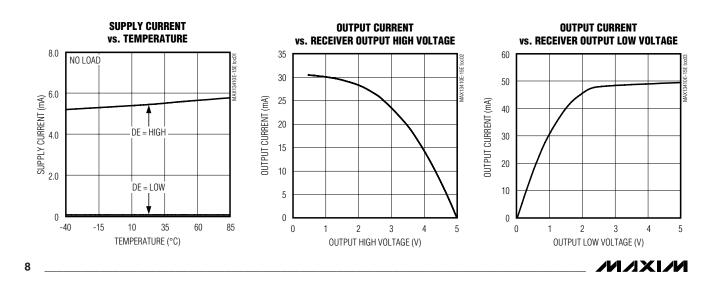
(V_{CC} = +6.0V to +28V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +7.5V, C_S = 1µF, and T_A = +25°C.) (Note 2)

PARAMETER SYMBOL		MIN	ТҮР	MAX	UNITS	
t _{DLZ}	S1 closed, Figure 4, R _L = 500Ω , C _L = $15pF$			50	ns	
t _{RPLH}				75		
t RPHL	$C_L = 13pF$ (at RO), Figures 5 and 6			75	ns	
t _{RSKEW}	C_L = 15pF (at RO), Figures 5 and 6			8	ns	
fMAX		16			Mbps	
trzh	S2 closed, Figure 7, $C_L = 15 pF$			50	ns	
t _{RZL}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns	
t _{RLZ}	S1 closed, Figure 7, $C_L = 15 pF$			50	ns	
trzh	S2 closed, Figure 7, $C_L = 15 pF$			50	ns	
	tDLZ tRPLH tRPHL tRSKEW fMAX tRZH tRZL tRLZ	tDLZS1 closed, Figure 4, $R_L = 500\Omega$, $C_L = 15pF$ tRPLH tRPHL $C_L = 15pF$ (at RO), Figures 5 and 6tRSKEW $C_L = 15pF$ (at RO), Figures 5 and 6fMAXtRZHtRZHS2 closed, Figure 7, $C_L = 15pF$ tRZLS1 closed, Figure 7, $C_L = 15pF$ tRLZS1 closed, Figure 7, $C_L = 15pF$	tDLZS1 closed, Figure 4, $R_L = 500\Omega$, $C_L = 15pF$ tRPLH tRPHL $C_L = 15pF$ (at RO), Figures 5 and 6tRSKEW $C_L = 15pF$ (at RO), Figures 5 and 6fMAX16tRZHS2 closed, Figure 7, $C_L = 15pF$ tRZLS1 closed, Figure 7, $C_L = 15pF$ tRLZS1 closed, Figure 7, $C_L = 15pF$	tDLZS1 closed, Figure 4, $R_L = 500\Omega$, $C_L = 15pF$ tRPLH tRPHL $C_L = 15pF$ (at RO), Figures 5 and 6tRSKEW $C_L = 15pF$ (at RO), Figures 5 and 6fMAX16tRZHS2 closed, Figure 7, $C_L = 15pF$ tRZLS1 closed, Figure 7, $C_L = 15pF$ tRLZS1 closed, Figure 7, $C_L = 15pF$	tDLZS1 closed, Figure 4, $R_L = 500\Omega$, $C_L = 15pF$ 50 t_{RPLH} $C_L = 15pF$ (at RO), Figures 5 and 675 t_{RSKEW} $C_L = 15pF$ (at RO), Figures 5 and 68 f_{MAX} 16 t_{RZH} S2 closed, Figure 7, $C_L = 15pF$ 50 t_{RZL} S1 closed, Figure 7, $C_L = 15pF$ 50 t_{RLZ} S1 closed, Figure 7, $C_L = 15pF$ 50 t_{RLZ} S1 closed, Figure 7, $C_L = 15pF$ 50	

Note 2: C_S is the compensation capacitor on V_{REG} for the MAX13412E–MAX13415E versions. C_S must have an ESR value of 20m Ω or less. **Note 3:** Parameters are guaranteed for +6.0V $\leq V_{CC} \leq +28V$.

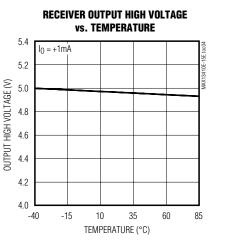
Typical Operating Characteristics

(V_{CC} = +7.5V, T_A = $+25^{\circ}$ C, unless otherwise noted.)

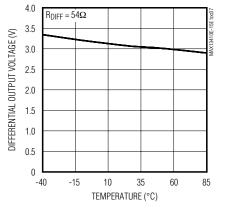




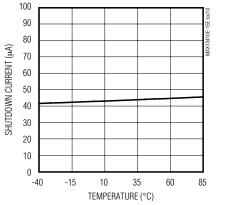
 $(V_{CC} = +7.5V, T_A = +25^{\circ}C, unless otherwise noted.)$



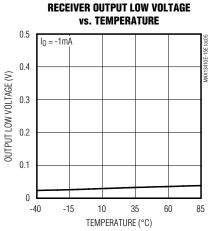
DRIVER DIFFERENTIAL OUTPUT VOLTAGE vs. TEMPERATURE



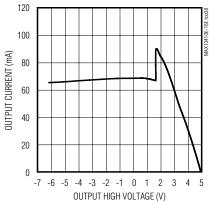
SHUTDOWN CURRENT vs. TEMPERATURE



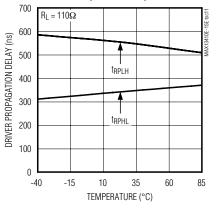
///XI///



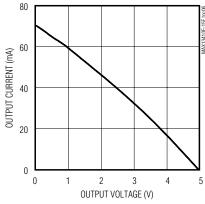
OUTPUT CURRENT vs. TRANSMITTER OUTPUT HIGH VOLTAGE



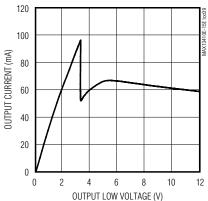
DRIVER PROPAGATION vs. TEMPERATURE (MAX13412E)



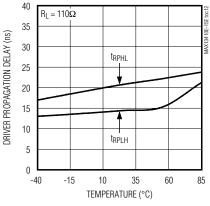
DIFFERENTIAL OUTPUT CURRENT vs. DIFFERENTIAL OUTPUT VOLTAGE



OUTPUT CURRENT vs. TRANSMITTER OUTPUT LOW VOLTAGE



DRIVER PROPAGATION vs. TEMPERATURE (MAX13413E)



9

 $(V_{CC} = +7.5V, T_A = +25^{\circ}C, unless otherwise noted.)$ **RECEIVER PROPAGATION vs.TEMPERATURE** (MAX13410E/MAX13412E) 60 RECEIVER PROPAGATION DELAY (ns) **t**RPHL 45 30 **t**_{RPLH} 15 0 -40 -15 10 35 60 TEMPERATURE (°C) **DRIVER PROPAGATION (16kbps)** (MAX13413E)

DI 2V/div A - B 5V/div 20ns/div

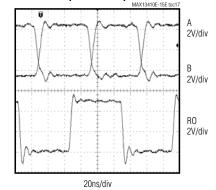
85

60 RECEIVER PROPAGATION DELAY (ns) 45 **t**RPHL 30 15 tRPLH 0 -40 -15 35 60 85 10 TEMPERATURE (°C)

RECEIVER PROPAGATION vs.TEMPERATURE

(MAX13411E/MAX13413E)

RECEIVER PROPAGATION (16kbps) (MAX13413E)

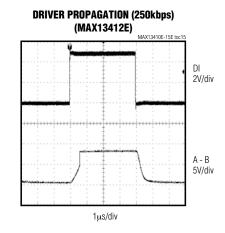


DI

2V/div

A - R

5V/div

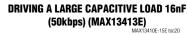


(19.2kbps) (MAX13412E) DI 2V/div A - B 2V/div 10µs/div

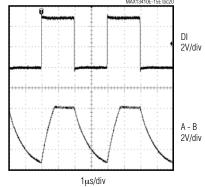
DRIVING A LARGE CAPACITIVE LOAD 16nF



400ns/div



Typical Operating Characteristics (continued)





Test Circuits and Waveforms

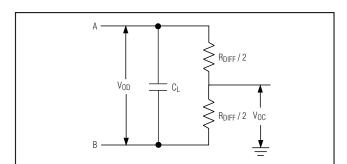
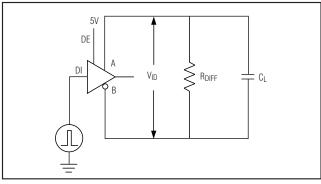


Figure 1. Driver DC Test Load



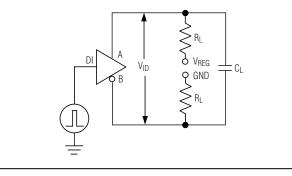
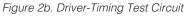


Figure 2a. Driver-Timing Test Circuit



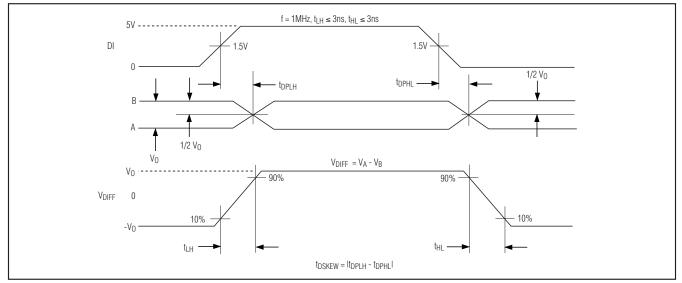
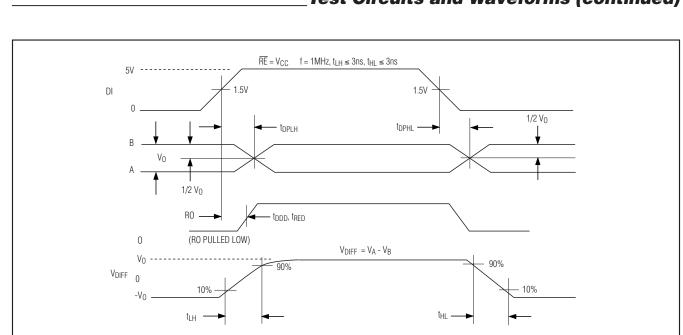


Figure 3a. Driver Propagation Delays





Test Circuits and Waveforms (continued)

Figure 3b. Driver Propagation Delays

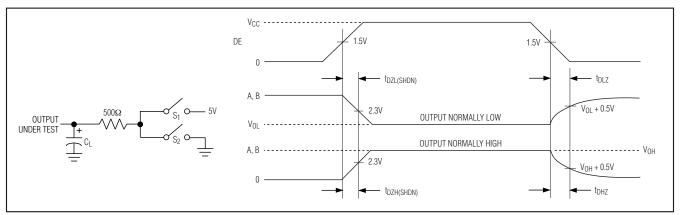


Figure 4. Driver Enable and Disable Times

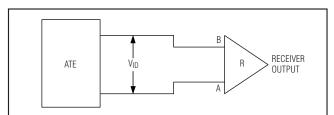


Figure 5. Receiver-Propagation-Delay Test Circuit

MAX13410E-MAX13415E

_Test Circuits and Waveforms (continued)

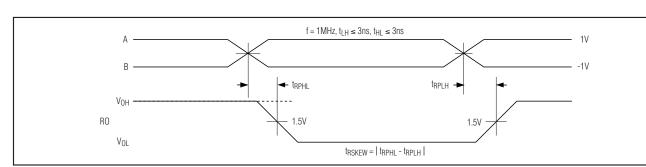


Figure 6. Receiver Propagation Delays

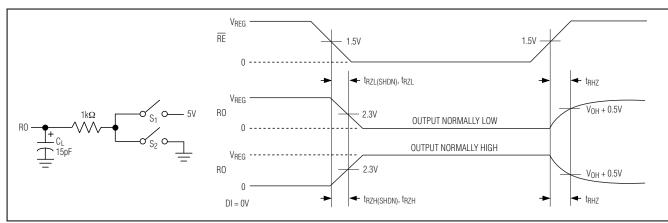


Figure 7. Receiver Enable and Disable Times

Pin Description

	PIN			
MAX13410E/ MAX13411E	MAX13412E/ MAX13413E	MAX13414E/ MAX13415E	NAME	FUNCTION
1	_	1	RO	Receiver Output. When receiver is enabled and V _A - V _B \ge -50mV, RO is high. If V _A - V _B \le -200mV, RO is low. Note: RO is referenced to the LDO output (V _{REG}).
2	_	_	RE	Receiver Output Enable. Drive $\overline{\text{RE}}$ low to enable RO. Drive $\overline{\text{RE}}$ high to disable the RO output and put the RO output in a high-impedance state.
3	_	—	DE	Driver Output Enable. Drive DE low to put the driver output in three-state. Drive DE high to enable the driver.
4	4	4	DI	Driver Input. Drive DI low to force the noninverting output low and the inverting output high. Drive DI high to force the noninverting output high and inverting output low. DI is an input to the internal state machine that automatically enables and disables the driver (for the MAX13412E/MAX13413E). See the <i>Function Tables</i> and <i>General Description</i> for more information.
5	5	5	GND	Ground
6	6	6	А	Noninverting Receiver Input and Noninverting Driver Output
7	7	7	В	Inverting Receiver Input and Inverting Driver Output
8	8	8	V _{CC}	Positive Supply. Bypass V _{CC} with a 0.1µF ceramic capacitor to GND.
_	1		RO	Receiver Output. When receiver is enabled and V _A - V _B \ge -100mV, RO is high. If V _A - V _B \le -100mV, RO is low. Note: RO is referenced to the LDO output (V _{REG}).
_	2		RE	Receiver Output Enable. Drive $\overline{\text{RE}}$ low to force the RO output to be enabled. Drive $\overline{\text{RE}}$ high to let the AutoDirection circuit control RO.
_	3	3	VREG	LDO Output. V _{REG} is fixed at +5V. Bypass V _{REG} with a low ESR (20m Ω or less) and a 1µF (min) ceramic capacitor.
_	_	2	DE/RE	Receiver and Driver Output Enable. Drive DE/RE low to enable RO and disable the driver. Drive DE/RE high to disable RO and enable the driver.
EP	EP	EP	EP	Exposed Pad. EP is internally connected to GND. For enhanced thermal dissipation, connect EP to a copper area as large as possible. Do not use EP as a sole ground connection.

Function Tables for the MAX13410E/MAX13411E

TRANSMITTING								
INPUT			OUTPUT					
RE	DE	DI	B A					
Х	1	1	0	1				
Х	1	0	1 0					
0	0	Х	High impedance High impedance					
1	0	Х	High impedance (shutdown)					

	RECEIVING							
	INP	UT	OUTPUT					
RE	DE	A - B	RO					
0	Х	<u>></u> -50mV	1					
0	Х	<u><</u> -200mV	0					
0	Х	Open/Short	1					
1	1	Х	High impedance					
1	0	Х	High impedance (shutdown)					

X = Don't care, shutdown mode, driver, and receiver outputs are in high impedance.

Function Tables for the MAX13412E/MAX13413E

	TRANSMITTING								
		INPUTS	OUTPUTS						
DI	DI A - B > V _{DT} ACTION		А	В					
0	Х	Turn driver ON	0	1					
1	False	If driver was OFF, keep it OFF	High impedance	High impedance					
1	False	If driver was ON, keep it ON	1	0					
1	True	Turn driver OFF	High impedance	High impedance					

	RECEIVING								
			OUTPUT						
RE	A - B	DRIVER STATE	RECEIVER STATE	RO					
0	<u>></u> -100mV	Х	ON	1					
0	<u><</u> -100mV	Х	ON	0					
1	Х	ON	OFF	High impedance					
1	<u>></u> -100mV	OFF	ON	1					
1	<u><</u> -100mV	OFF	ON	0					

X = Don't care, shutdown mode, driver, and receiver outputs are in high impedance.

Function Tables for the MAX13414E/MAX13415E

TRANSMITTING							
INPUT OUTPUT							
DE/RE	DI	B A					
0	Х	High impedance	High impedance				
1	1	0	1				
1	0	1	0				

RECEIVING						
IN	OUTPUT					
DE/RE	A - B	RO				
0	<u>></u> -50mV	1				
0	<u><</u> -200mV	0				
0	Open/Short	1				
1	Х	High impedance				

X = Don't care, shutdown mode, driver, and receiver outputs are in high impedance.



Detailed Description

The MAX13410E–MAX13415E are half-duplex RS-485/ RS-422-compatible transceivers optimized for isolated applications. These devices feature an internal LDO regulator, one driver, and one receiver. The internal LDO allows the part to operate from an unregulated +6V to +28V power supply. The AutoDirection feature reduces the number of optical isolators needed in isolated applications. Other features include \pm 15kV ESD protection (MAX13412E/MAX13413E only), \pm 14kV (MAX13410E/ MAX13411E only) fail-safe circuitry, slew-rate limiting, and full-speed operation.

The MAX13410E–MAX13415E internal LDO generates a 5V \pm 10% power supply that is used to power its internal circuitry. The MAX13412E–MAX13415E bring the 5V to an output V_{REG} that allows the user to power additional external circuitry with up to 20mA to further reduce external components. The MAX13410E/MAX13411E do not have a 5V output and come in industry-compatible pinouts. This allows easy replacement in existing designs.

The MAX13412E/MAX13413E feature Maxim's proprietary AutoDirection control. This architecture eliminates the need for the DE and $\overline{\text{RE}}$ control signals. In isolated applications, this reduces the cost and size of the system by reducing the number of optical isolators required.

The MAX13410E/MAX13412E/MAX13414E feature reduced slew-rate drivers that minimize EMI and reduce reflections caused by improperly terminated cables, allowing error-free transmission up to 500kbps. The MAX13411E/MAX13413E/MAX13415E are not slew-rate limited, allowing transmit speeds up to 16Mbps.

The MAX13410E–MAX13415E feature a 1/8-unit load receiver input impedance, allowing up to 256 transceivers on the bus. All driver outputs are protected to ± 15 kV ESD using the Human Body Model. These devices also include fail-safe circuitry, MAX13410E/MAX13411E/MAX13414E/MAX13415E, guaranteeing a logic-high receiver output when the receiver inputs are open or shorted. The receiver outputs a logic-high when the transmitter on the terminated bus is disabled (high impedance).

Internal Low-Dropout Regulator

The MAX13410E–MAX13415E include an internal lowdropout regulator that allows it to operate from input voltages of up to +28V. The internal LDO has a set output voltage of 5V ±10% that is used to power the internal circuitry of the device. The MAX13412E–MAX13415E offer the LDO output at the V_{REG} output. This allows additional external circuitry to be powered without the need for additional external regulators. The V_{REG} output can source up to 20mA. When using these devices with high input voltages and heavily loaded networks, special care must be taken that the power dissipation rating of the package and the maximum die temperature of the device is not exceeded. Die temperature of the part can be calculated using the equation:

 $T_{DIE} = [(\theta_{JC} + \theta_{CA}) \times P_{DISS}] + T_{AMBIENT}$, where

T_{DIE} = Temperature of the Die

 θ_{JC} = 6.0°C/W = Junction-to-Case Thermal Resistance

 θ_{CA} = Case-to-Ambient Thermal Resistance

 $\theta_{JA} = \theta_{JC} + \theta_{CA} = 52.0^{\circ}\text{C/W} =$ Junction-to-Ambient Thermal Resistance

 $\begin{array}{l} P_{DISS} = (I_{CC} - V_{CC}) + [(V_{CC} - V_{REG}) \times I_{REG})] + [(V_{CC} - V_{OD}) \times I_{DRIVER}] = Power Dissipation of the Part \end{array}$

TAMBIENT = Ambient Temperature

V_{CC} = Voltage on the V_{CC} Input

ICC = Current in to VCC

VREG = Voltage on the VREG Output

IREG = Current Drawn from the VREG Output

 V_{OD} = Voltage at the Driver Output (IVA - VBI)

I_{DRIVER} = Current Driven Out of the Driver. Typically, this is the current through the termination resistor.

The absolute maximum rating of the die temperature of the MAX13410E–MAX13415E is +150°C. To protect the part from overheating, there is an internal thermal shutdown that shuts down the part when the die temperature reaches +150°C. To prevent damage to the part, and to prevent the part from entering thermal shutdown, keep the die temperature below 150°C, plus some margin. The circuit designer can minimize the die temperature by controlling the following parameters:

- Vcc
- IREG
- θ_{CA}

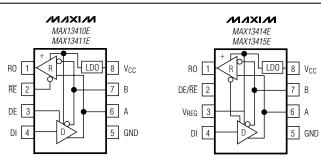
Measuring the V_CC Current

Measured current at the V_{CC} pin is a function of the quiescent current of the part, the amount of current that the drivers must supply to the load, and in the case of the MAX13412E–MAX13415E, the load on the V_{REG} output. In most cases, the load that the drivers must supply will be the termination resistor(s). Ideally, the termination resistance should match the characteristic impedance of the cable and is usually not a parameter the circuit designer can easily change. In some low-speed, short-cable applications, proper termination

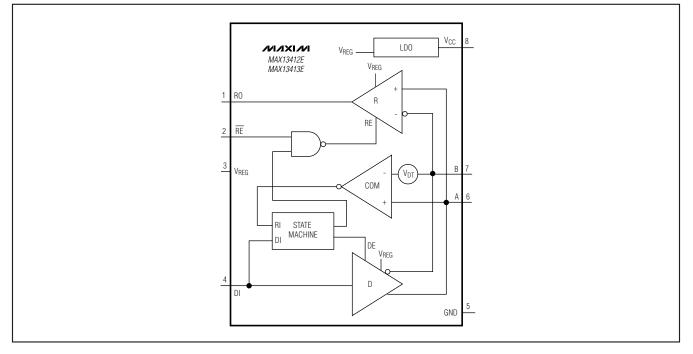


Functional Diagrams

Functional Diagram for the MAX13410E/MAX13411E/MAX13414E/MAX13415E



Functional Diagram for the MAX13412E/MAX13413E



may not be necessary. In these cases, the drive current can be reduced to minimize the die temperature.

Minimizing the load on the V_{REG} output lowers the power dissipation of the part and ultimately reduces the maximum die temperature.

 θ_{CA} is the thermal resistance from case to ambient and is independent of the MAX13410E–MAX13415E. θ_{CA} is primarily a characteristic of the circuit-board design. The

largest contributing factor of θ_{CA} will be the size and weight of the copper connected to the exposed paddle of the MAX13410E-MAX13415E. Lower the thermal resistance by using as large a pad as possible. Additionally, vias can be used to connect the pad to other ground planes in the circuit board.

Note that θ_{JC} is the thermal resistance of the part from junction-to-case temperature and is fixed at 6.0°C/W. It is solely based on the die and package characteristics of



the MAX13410E–MAX13415E. The circuit-board designer has no control over this parameter.

Fail Safe The MAX13410E/MAX13411E/MAX13414E/MAX13415E guarantee a logic-high receiver output when the receiver inputs are shorted or open, or when they are connected to a terminated transmission line with all drivers disabled. This is done by setting the receiver input threshold between -50mV and -200mV. If the differential receiver input voltage (A - B) is greater than or equal to -50mV. RO is logic-high. If (A - B) is less than or equal to -200mV, RO is logic-low. In the case of a terminated bus with all transmitters disabled, the receiver's differential input voltage is pulled to 0 by the termination. With the receiver thresholds of the MAX13410E/ MAX13411E/MAX13414E/MAX13415E, the result is a logic-high with a 50mV minimum noise margin. Unlike previous fail-safe devices, the -50mV to -200mV threshold complies with the ±200mV EIA/TIA-485 standard.

AutoDirection Circuitry

The AutoDirection circuitry in the MAX13412E/ MAX13413E is a technique to minimize the number of signals needed to drive the part. This is especially useful in very low cost, isolated systems. In a typical isolated system, an optocoupler is used for each control signal to cross the isolation barrier. These optocouplers add cost, size and consume power. Without the AutoDirection circuitry, three to four optocouplers may be required for each transceiver. With the AutoDirection circuitry, the number of optocouplers can be reduced to two.

Typical RS-485 transceivers have four signals on the control side of the part. These are RO (receiver output), RE (receiver enable), DE (driver enable), and DI (driver input). In some cases, DE and RE may be connected together to reduce the number of control signals to three. In half-duplex systems, the RE and DE signals determine if the part is transmitting or receiving. When the part is receiving, the transmitter is in a high-impedance state. In a fully compliant RS-485 system, all three or four signals are required. However, with careful design and Maxim's AutoDirection feature, the number of control signals can be reduced to just RO and DI in an RS-485 compatible system. This feature assumes the DI input idles in the high state while the receiver portion of the MAX13412E/MAX13413E is active. It also requires an external pullup resistor on A and pulldown resistor on B (see the typical application circuit, Figure 10). The following is a description of how AutoDirection works.

When DI is low, the MAX13412E/MAX13413E always drive the bus low. When DI transitions from a low to a

high, the drivers actively drive the output until (A - B) > V_{DT}. Once (A - B) is greater than V_{DT}, the drivers are disabled, letting the pullup/pulldown resistors hold the A and B lines in the correct state. This allows other transmitters on the bus to pull the bus low.

Pullup and Pulldown Resistors

The pullup and pulldown resistors on the A and B lines are required for proper operation of the MAX13412E and MAX13413E, although their exact value is not critical. They function to hold the bus in the high state (A - B > 200mV) when all the transmitters are in a high-impedance state due to either a shutdown condition or AutoDirection. Determining the best value to use for these resistors depends on many factors, such as termination resistor values, noise, number of transceivers on the bus, etc. Size these resistors so that, under all conditions, (A - B) > 200mV for ALL receivers on the bus.

Idle State

When not transmitting data, the MAX13412E/ MAX13413E require the DI input to be driven high to remain in the idle state. A conventional RS-485 transceiver has DE and $\overline{\text{RE}}$ inputs that are used to enable and disable the driver and receiver. However, the MAX13412E/MAX13413E do not have a DE input, and instead use an internal state machine to enable and disable the drivers. DI must be driven high to go to the idle state.

Enhanced ESD Protection

As with all Maxim devices, ESD-protection structures are incorporated on all pins to protect against electrostatic discharges encountered during handling and assembly. The driver outputs and receiver inputs of the MAX13410E– MAX13415E have extra protection against static electricity. Maxim's engineers have developed state-of-the-art structures to protect these pins against ESD of ±15kV (MAX13412E/MAX13413E) and ±14kV (MAX13410E/ MAX13411E) without damage. The ESD structures withstand high ESD in all states: normal operation, shutdown, and powered down. After an ESD event, the MAX13410E– MAX13415E keep working without latchup or damage.

ESD protection can be tested in various ways. The transmitter outputs and receiver inputs of the MAX13410E– MAX13415E are characterized for protection to the following limits:

±15kV using the Human Body Model (MAX13412E/ MAX13413E)

±14kV using the Human Body Model (MAX13410E/ MAX13411E)

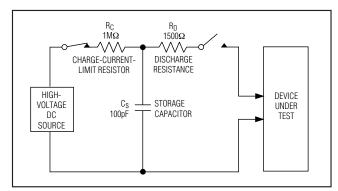


Figure 8a. Human Body ESD Test Model

ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

Human Body Model

Figure 8a shows the Human Body Model, and Figure 8b shows the current waveform it generates when discharged into a low impedance. This model consists of a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a $1.5k\Omega$ resistor.

Applications Information

Typical Applications

The MAX13410E–MAX13415E transceivers are designed for half-duplex, bidirectional data communications on multipoint bus transmission lines. To minimize reflections, terminate the line at both ends in its characteristic impedance, and keep stub lengths off the main line as short as possible. The slew-rate-limited MAX13410E/ MAX13412E/MAX13414E are more tolerant of imperfect termination.

Typical Application Circuit for the MAX13410E and MAX13411E

This application circuit shows the MAX13410E/ MAX13411E being used in an isolated application (see Figure 9). The MAX13410E/MAX13411E use the industrystandard pin out but do not have a V_{REG} output for biasing external circuitry. The positive temperature coefficient (PTC) and transient voltage suppressor (TVS) clamp circuit on the RS-485 outputs are intended to provide overvoltage fault protection and are optional based on the requirements of the design.

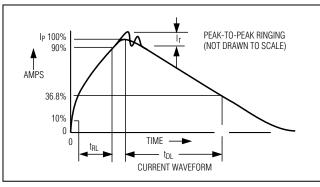


Figure 8b. Human Body Current Waveform

Typical Application Circuit for the MAX13412E and MAX13413E

This application circuit shows the MAX13412E and MAX13413E being used in an isolated application where the AutoDirection feature is implemented to reduce the number of optical isolators to two (see Figure 10). The MAX13412E/MAX13413E provide a V_{REG} output that can be used to power external circuitry up to 20mA.

Typical Application Circuit for the MAX13414E and MAX13415E

This application circuit shows the MAX13414E/ MAX13415E being used in an isolated application using an unregulated power supply with three optical isolators (see Figure 11). The MAX13414E/MAX13415E provide a VREG output that can be used to power external circuitry up to 20mA.

256 Transceivers on the Bus

The RS-485 standard specifies the load each receiver places on the bus in terms of unit loads. An RS-485compliant transmitter can drive 32 one-unit load receivers when used with a 120 Ω cable that is terminated on both ends over a -7V to +12V common-mode range. The MAX13410E–MAX13415E are specified as 1/8 unit loads. This means a compliant transmitter can drive up to 256 devices of the MAX13410E–MAX13415E. Reducing the common mode, and/or changing the characteristic impedance of the cable, changes the maximum number of receivers that can be used. Refer to the TIA/EIA-485 specification for further details.

Proper Termination and Cabling/ Wiring Configurations

When the data rates for RS-485 are high relative to the cable length it is driving, the system is subject to prop-



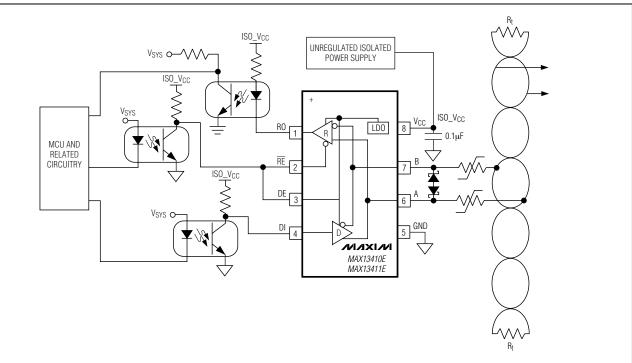


Figure 9. Typical Application Circuit for the MAX13410E/MAX13411E

er transmission line design. In most cases, a single, controlled-impedance cable or trace should be used and should be properly terminated on both ends with the characteristic impedance of the cable/trace. RS-485 transceivers should be connected to the cable/ traces with minimum-length wires to prevent stubs. Star configurations and improperly terminated cables can cause data loss. Refer to the *Applications* section of the Maxim website or to TIA/EIA publication TSB89 for further information. While proper termination is always desirable, in some cases, such as when data rates are very low, it may be desirable and advantageous to not properly terminate the cables. In such cases, it is up to the designer to ensure that the improper termination and resultant reflections (etc.) will not corrupt the data.

Reduced EMI and Reflections

The MAX13410E/MAX13412E/MAX13414E feature reduced slew-rate drivers that minimize EMI and reduce reflections caused by improperly terminated cables, allowing error-free data transmission up to 500kbps.

Low-Power Shutdown Mode

Low-power shutdown mode is initiated in the MAX13410E/MAX13411E by driving DE low and driving

RE high. In shutdown, the devices draw 65µA (typ) of supply current.

The devices are guaranteed not to enter shutdown if DE is low (while RE is high) for less than 50ns. If the inputs are in this state for at least 700ns, the devices are guaranteed to enter shutdown.

Enable times t_{ZH} and t_{ZL} (see the *Switching Characteristics* table) assume the devices were not in a low-power shutdown state. Enable times $t_{ZH(SHDN)}$ and $t_{ZL(SHDN)}$ assume the devices were in shutdown state. It takes drivers and receivers longer to become enabled from low-power shutdown mode ($t_{ZH(SHDN)}$), $t_{ZL(SHDN)}$) than from driver/receiver disable mode (t_{ZH} , t_{ZL}).

Line Length

The Telecommunications Industry Association (TIA) published the document TSB-89: *Application Guidelines for TIA/EIA-485-A*, which is a good reference for determining maximum data rate vs. line length.

Isolated RS-485 Interface

An isolated RS-485 interface electrically isolates different nodes on the bus to protect the bus from problems due to high common-mode voltages that exceed the RS-485 common-mode voltage range, conductive noise, and



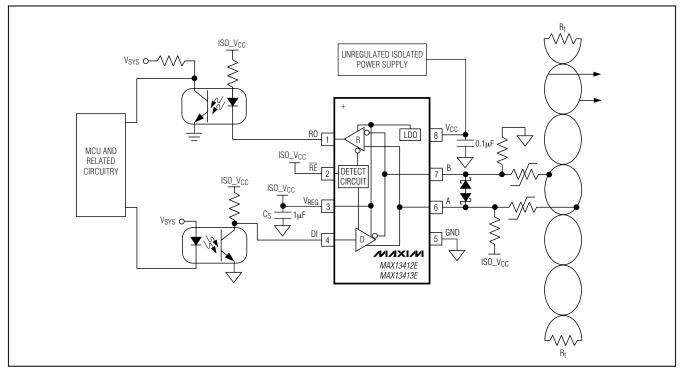


Figure 10. Typical Application Circuit for the MAX13412E/MAX13413E

ground loops. The typical application circuits show an isolated RS-485 interface using the MAX13410E–MAX13415E. The transceiver is powered separately from the controlling circuitry. The AutoDirection feature of the MAX13412E/MAX13413E (see the *AutoDirection Circuitry* section) requires only two optocouplers to electrically isolate the transceiver.

An isolated RS-485 interface electrically isolates different nodes on the bus to protect the bus from problems due to high common-mode voltages that exceed the RS-485 common-mode voltage range. An isolated RS-485 interface has two additional design challenges not normally associated with RS-485 design. These are 1) isolating the control signals and 2) getting isolated power to the transceiver. Optical isolators are the most common way of getting the control signals across the isolation barrier.

Isolated power is typically done using a transformer in either a push-pull or flyback configuration. The MAX845 is an example of an inexpensive, unregulated push-pull converter. (See Figure 12.) While in theory, the output of an unregulated push-pull converter is predictable, the output voltage can vary significantly due to the non-ideal characteristics of the transformer, load variations, and temperature drift of the diodes, etc. Variances of $\pm 20\%$ or more would not be uncommon. This would require the addition of a linear regulator to get standard RS-485 transceivers to work. Since the MAX13410E– MAX13415E have the linear regulator built in, this external regulator and its associated cost and size penalties are not necessary. A nominal +7.5V output with a $\pm 20\%$ tolerance would provide a +6V to +9V supply voltage. This is well within the operating range of the MAX13410E–MAX13415E. If the output tolerance is even greater than $\pm 20\%$, adjust the design of the power supply for a higher output voltage to ensure the minimum input voltage requirements are met.

Flyback converters are typically regulated. A TL431 type error amplifier and an optical isolator usually close the loop. The MAX5021 is an example of a small, inexpensive, flyback controller (see Figure 13). While the primary output of the flyback converter is tightly regulated, secondary outputs will not be. As with the unregulated push-pull converter, the MAX13410E–MAX13415E are ideally suited for use with these secondary outputs.

MAX13410E-MAX13415E

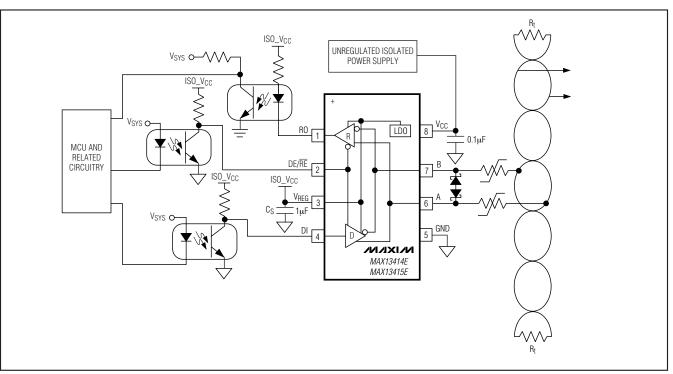


Figure 11. Typical Application Circuit for the MAX13414E/MAX13415E

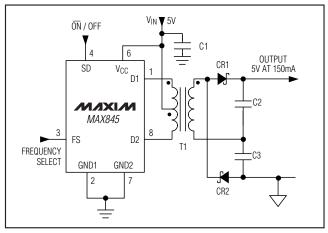


Figure 12. Using the MAX845 to Obtain an Isolated Power Supply

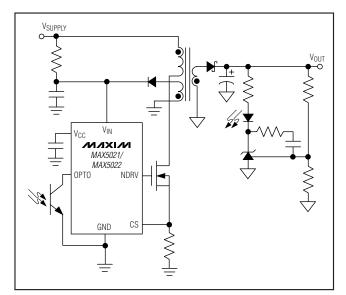
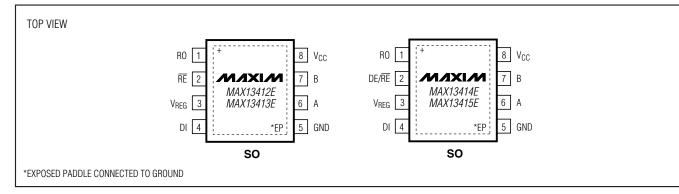


Figure 13. The MAX5021 and MAX5022 provide an isolated power supply with tighter regulation due to feedback using an opto-isolator coupler.



_Pin Configurations (continued)



Ordering Information/Selector Guide (continued)

PART	PIN-PACKAGE	AutoDirection	DATA RATE (max)	SLEW-RATE LIMITED	PKG CODE
MAX13412EESA+	8 SO-EP*	Yes	500kbps	Yes	S8E+14
MAX13413EESA+	8 SO-EP*	Yes	16Mbps	No	S8E+14
MAX13414EESA+**	8 SO-EP*	No	500kbps	Yes	S8E+14
MAX13415EESA+**	8 SO-EP*	No	16Mbps	No	S8E+14

Note: All devices operate over the -40°C to +85°C operating temperature range.

*EP = Exposed paddle.

** Future product—contact factory for availability.

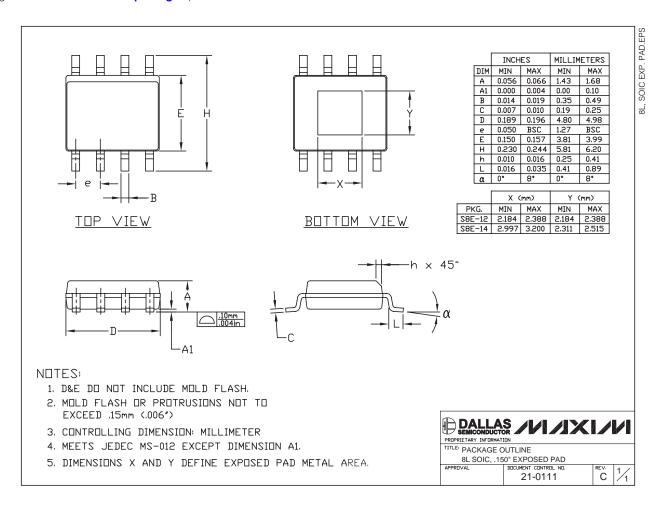
+Denotes a lead-free package.

Chip Information

PROCESS TECHNOLOGY: BICMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

24

MAX13410E-MAX13415E

MAXIM is a registered trademark of Maxim Integrated Products, Inc.