TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TC4SU11F**

### 2 INPUT NAND GATE

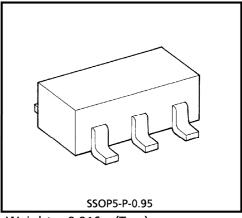
TC4SU11F is 2 input NAND gate respectively.

The internal circuit of only basic NAND circuit without the waveform shaping inverter.

Therefore, this is suitable for the applications in liner circuits such as oscillator circuits and amplifier circuits, and this has advantage in the applications of Logical processing systems with faster operating speed.

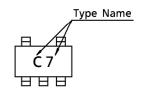
#### **MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	$V_{DD}$	$V_{SS} - 0.5 \sim V_{SS} + 20$	V
Input Voltage	V <sub>IN</sub>	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	٧
Output Voltage	Vout	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
DC Input Current	IN	± 10	mA
Power Dissipation	PD	200	mW
Operating Temperature Range	T <sub>opr</sub>	- 40~85	°C
Storage Temperature Range	T <sub>stg</sub>	- 65~150	°C
Lead Temperature (10s)	TL	260	°C

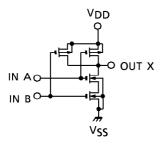


Weight: 0.016g (Typ.)

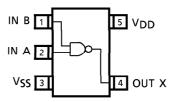
### Marking



#### **LOGIC DIAGRAM**



### PIN ASSIGNMENT (TOP VIEW)



### **RECOMMENDED OPERATING CONDITIONS** $(V_{SS} = 0V)$

CHARACTERISTIC	SYMBOL		MIN.	TYP.	MAX.	UNIT
DC Supply Voltage	$V_{DD}$	_	3	_	18	V
Input Voltage	VIN		0	_	$V_{DD}$	V

### STATIC ELECTRICAL CHARACTERISTICS $(V_{SS} = 0V)$

CHARACTERISTIC		SYM- BOL	TEST CONDITION	(S) ADD	– 40°C		25°C			85°C		UNIT
					MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	ONLL
High-Level			llour / 1,40	5	4.95	_	4.95	5.00	_	4.95	_	
Output Voltage	Vон	I <sub>OUT</sub>  <1μΑ  V <sub>IN</sub> =V <sub>SS</sub>	10	9.95		9.95	10.00		9.95			
Output voi	itage		vIN - v22	15	14.95		14.95	15.00	_	14.95	_	v
Low-Level			  lout <1μΑ	5	—	0.05	—	0.00		—	0.05	Ů
Output Vol	ltage	VOL	$V_{IN} = V_{DD}$	10	_	0.05		0.00		—	0.05	
Output voi	itage		טטי – אוי	15	_	0.05	_	0.00	0.05	_	0.05	
			V <sub>OH</sub> = 4.6V	5	- 0.61		- 0.51	- 1.0	_	- 0.42		
Output Hig	ıh		$V_{OH} = 2.5V$	5	- 2.5		- 2.1	- 4.0		- 1.7	_	
Current	J11	ЮН	V <sub>OH</sub> = 9.5V	10	<b>–</b> 1.5		- 1.3	- 2.2		- 1.1		
Current			V <sub>OH</sub> = 13.5V	15	- 4.0	_	- 3.4	- 9.0	_	- 2.8	_	
			$V_{IN} = V_{SS}$ , $V_{DD}$									mA
			$V_{OL} = 0.4V$	5	0.61	_	0.51	1.2		0.42	_	IIIA
Output Lov	N		$V_{OL} = 0.5V$	10	1.5		1.3	3.2		1.1		
Current		lOL	V <sub>OL</sub> = 1.5V	15	4.0	_	3.4	12.0	_	2.8	_	
			$V_{IN} = V_{DD}$									
			V <sub>OUT</sub> = 0.5V	5	4.0	_	4.0	3.0	_	4.0	_	
Input High	Valtaga	<b> </b>	V <sub>OUT</sub> = 1.0V	10	8.0	_	8.0	6.5	_	8.0	_	
Input nigh	voitage	VIH	V <sub>OUT</sub> = 1.5V	15	12.0	_	12.0	9.5	_	12.0	_	
			l <sub>OUT</sub>  <1μΑ									
		,	V <sub>OUT</sub> = 4.5V	5	_	1.0	_	2.0	1.0	_	1.0	V
loonist Lave	\/al+aaa		V <sub>OUT</sub> = 9.0V	10	<b> </b>	2.0	—	3.5	2.0	—	2.0	
Input Low Voltage	VIL	V <sub>OUT</sub> = 13.5V	15	_	3.0	—	5.5	3.0	—	3.0		
		l <sub>OUT</sub>  <1μΑ	1									
Input	H Level	ΊΗ	V <sub>IH</sub> = 18V	18	_	0.1	_	<b>10</b> <sup>- 5</sup>	0.1	_	1.0	
Current	L Level	կլ	V <sub>IL</sub> = 0V	18	_	- 0.1	_	<b>–</b> 10 <sup>– 5</sup>	- 0.1	_	- 1.0	$\mu$ A
Quiescent Device Current				5	_	0.25	_	0.001	0.25	_	7.5	
		lDD	$V_{IN} = V_{SS}, V_{DD}$	10	-	0.5	_	0.001	0.5	—	15	$\mu$ A
				15		1.0	_	0.002	1.0		30	

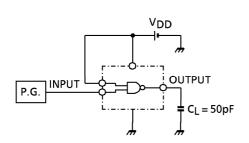
2 2001-05-31

### **DYNAMIC ELECTRICAL CHARACTERISTICS** (Ta = 25°C, V<sub>SS</sub> = 0V, C<sub>L</sub> = 50pF)

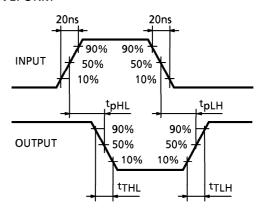
CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>DD</sub> (V)	MIN.	TYP.	MAX.	UNIT	
Output Transition Time			5	_	70	200		
(Low to High)	tTLH	_	10	_	35	100		
(Low to High)			15	_	30	80		
Output Transition Time			5	_	60	200	ns	
Output Transition Time (High to Low)	tTHL	_	10	_	25	100		
(High to Low)			15	_	20	80		
			5	_	50	110		
Propagation Delay Time	t <sub>pLH</sub>	_	10	_	28	60		
			15	_	22	50		
			5	_	50	110	ns	
Propagation Delay Time	t <sub>pHL</sub>	_	10	_	28	60		
			15	_	22	50		
Input Capacitance	CIN	_	_	5	7.5	pF		

### CIRCUIT AND WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

CIRCUIT



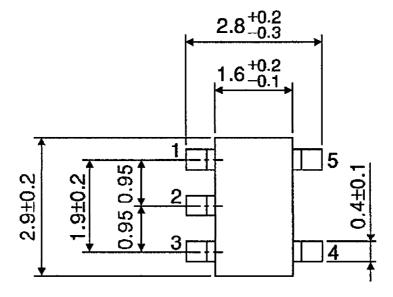
### **WAVEFORM**

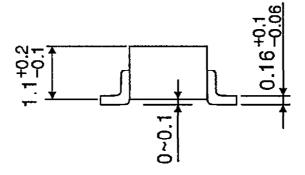


## PACKAGE DIMENSIONS

SSOP5-P-0.95

Unit: mm





Weight: 0.016g (Typ.)

4 2001-05-31

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