TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74VCX02FT,TC74VCX02FK

Low-Voltage Quad 2-Input NOR Gate with 3.6-V Tolerant Inputs and Outputs

The TC74VCX02FT/FK is a high-performance CMOS 2-input NOR gate which is guaranteed to operate from 1.2-V to 3.6-V. Designed for use in 1.5V, 1.8V, 2.5V or 3.3V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

It is also designed with overvoltage tolerant inputs and outputs up to  $3.6\ V.$ 

All inputs are equipped with protection circuits against static discharge.

#### **Features**

- Low-voltage operation:  $V_{CC} = 1.2 \sim 3.6 \text{ V}$
- High-speed operation:  $t_{pd} = 2.8 \text{ ns (max) (V}_{CC} = 3.0 \sim 3.6 \text{ V})$

:  $t_{pd} = 3.7 \text{ ns (max) (V}_{CC} = 2.3 \sim 2.7 \text{ V})$ 

 $t_{pd} = 7.4 \text{ ns (max) (V}_{CC} = 1.65 \sim 1.95 \text{ V}$ 

 $t_{pd} = 14.8 \text{ ns (max) (V}_{CC} = 1.4 \sim 1.6 \text{ V}$ 

 $t_{pd} = 37.0 \text{ ns (max) (VCC} = 1.2 \text{ V)}$ 

• Output current: I<sub>OH</sub>/I<sub>OL</sub> = ±24 mA (min) (V<sub>CC</sub> = 3.0 V)

 $: I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$ 

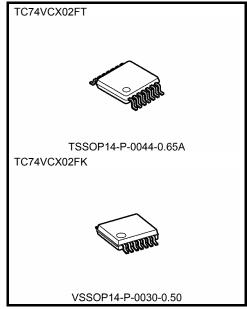
 $: I_{OH}/I_{OL} = \pm 6 \text{ mA (min) (V}_{CC} = 1.65 \text{ V)}$ 

:  $I_{OH}/I_{OL} = \pm 2$  mA (min) ( $V_{CC} = 1.4$  V)

- Latch-up performance: -300 mA
- ESD performance: Machine model  $\geq \pm 200 \text{ V}$

Human body model  $\geq \pm 2000 \text{ V}$ 

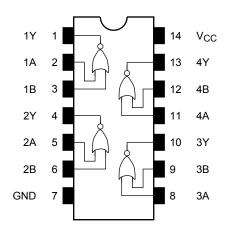
- Package: TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs



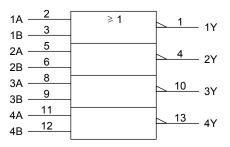
Weight

TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

### Pin Assignment (top view)



### **IEC Logic Symbol**



#### **Truth Table**

Inp	uts	Outputs
Α	В	Υ
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V	
DC input voltage	V <sub>IN</sub>	-0.5~4.6	٧	
DC output voltage	\/a	-0.5~4.6 (Note 2)	V	
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5(Note 3)		
Input diode current	I <sub>IK</sub>	-50	mA	
Output diode current	I <sub>OK</sub>	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	P <sub>D</sub>	180	mW	
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature	T <sub>stg</sub>	-65~150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 \text{ V}$ 

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 



### **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	1.2~3.6	V	
Input voltage	V <sub>IN</sub>	-0.3~3.6	٧	
Output voltage	Vour	0~3.6 (Note 2)	V	
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub> (Note 3)	V	
		±24 (Note 4)		
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±18 (Note 5)		
Output current	IOH/IOL	±6 (Note 6)	mA	
		±2 (Note 7)		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dv	0~10 (Note 8)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state

Note 4:  $V_{CC} = 3.0 \sim 3.6 \text{ V}$ 

Note 5:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 6:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 7:  $V_{CC} = 1.4 \sim 1.6 \text{ V}$ 

Note 8:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

### **Electrical Characteristics**

### DC Characteristics (Ta = -40 to $85^{\circ}$ C, $2.7 \text{ V} < \text{V}_{CC} \le 3.6 \text{ V}$ )

Characteristi	ice.	Symbol	Test (	Condition		Min	Max	Unit
Onaracteristi	03	Cymbol	1031	rest condition		IVIIII	Wax	Offic
Input voltage	H-level	V <sub>IH</sub>		_	2.7~3.6	2.0	_	V
L-level		V <sub>IL</sub>		_	2.7~3.6	_	0.8	V
Output voltage				I <sub>OH</sub> = -100 μA	2.7~3.6	V <sub>CC</sub> - 0.2	_	
	H-level	Voh	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	_	V
				I <sub>OH</sub> = -24 mA	3.0	2.2	_	
	L-level	V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 100 μA	2.7~3.6	_	0.2	
				I <sub>OL</sub> = 12 mA	2.7	_	0.4	
	L-level			I <sub>OL</sub> = 18 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		2.7~3.6	_	±5.0	μА
Power-off leakage curr	ent	l <sub>OFF</sub>	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6	V	0	_	10.0	μΑ
Quiescent supply current		laa	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7~3.6	_	20.0	
Increase in L		Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		2.7~3.6	_	±20.0	μΑ
Increase in I <sub>CC</sub> per inp	uı	Δl <sub>CC</sub>	$V_{IH} = V_{CC} - 0.6 V$		2.7~3.6	_	750	

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# DC Characteristics (Ta = -40 to 85°C, 2.3 V $\leq$ V<sub>CC</sub> $\leq$ 2.7 V)

Characteristics		Symbol	Test Condition			Min	Max	Unit
		Cymbol	1000			14		
Input voltage	H-level	V <sub>IH</sub>		_	2.3~2.7	1.6	_	V
input voitage	L-level	V <sub>IL</sub>		_	2.3~2.7	_	0.7	V
H-leve				I <sub>OH</sub> = -100 μA	2.3~2.7	V <sub>CC</sub> – 0. 2	_	
	H-level	V <sub>OH</sub>	$V_{IN} = V_{IL}$	$I_{OH} = -6 \text{ mA}$	2.3	2.0	_	V
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	
Output voltage				$I_{OH} = -18 \text{ mA}$	2.3	1.7	_	
			$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 100 \ \mu A$	2.3~2.7	_	0.2	
	L-level	$V_{OL}$		I <sub>OL</sub> = 12 mA	2.3	_	0.4	
				$I_{OL} = 18 \text{ mA}$	2.3	_	0.6	
Input leakage current	Input leakage current		V <sub>IN</sub> = 0 to 3.6 V		2.3~2.7	_	±5.0	μА
Power-off leakage current		l <sub>OFF</sub>	$V_{IN}$ , $V_{OUT} = 0$ to 3.6	V <sub>IN</sub> , V <sub>OUT</sub> = 0 to 3.6 V		_	10.0	μА
Quiacaant aunnly aurr	Outroped to work assessed		V <sub>IN</sub> = V <sub>CC</sub> or GND		2.3~2.7	_	20.0	
Quiescent supply curr	CIIL	Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		_	±20.0	μА

# DC Characteristics (Ta = -40 to $85^{\circ}$ C, 1.65 V $\leq$ V<sub>CC</sub> < 2.3 V)

Characteristics		Symbol	Test Co	ondition		Min	Max	Unit
		Cymbol	1000	rest condition		141111	Max	O
Input voltage	H-level	V <sub>IH</sub>	_	_		0.65 × V <sub>CC</sub>	ı	V
L-level		V <sub>IL</sub>	_		1.65~2.3	l	0.2 × V <sub>CC</sub>	V
	H-level V <sub>C</sub>	Voh	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -100 μA	1.65~2.3	V <sub>CC</sub> - 0.2	_	V
Output voltage				$I_{OH} = -6 \text{ mA}$	1.65	1.25	_	
	1. 11		$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 100 \mu A$	1.65~2.3	_	0.2	
	L-level	V <sub>OL</sub>		I <sub>OL</sub> = 6 mA	1.65	_	0.3	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		1.65~2.3	_	±5.0	μА
Power-off leakage current		loff	$V_{IN}$ , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
Quiescent supply current		1	V <sub>IN</sub> = V <sub>CC</sub> or GND		1.65~2.3	_	20.0	^
Quiescent supply curre	iiit.	Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		1.65~2.3	_	±20.0	μА



# DC Characteristics (Ta = -40 to 85°C, 1.4 V $\leq$ V<sub>CC</sub> < 1.65 V)

Characteristi	cs	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
Input voltage	H-level	V <sub>IH</sub>	-	_	1.4~1.65	0.65 × V <sub>CC</sub>	_	V
mput voltage	L-level	V <sub>IL</sub>	_		1.4~1.65		0.05 × V <sub>CC</sub>	V
H-I	H-level	Voh	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -100 μA	1.4~1.65	V <sub>CC</sub> - 0.2	_	
Output voltage				$I_{OH} = -2 \text{ mA}$	1.4	1.05	_	- V
Output Voltage	L-level	V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 100 μA	1.4~1.65	ı	0.05	
				$I_{OL} = 2 \text{ mA}$	1.4		0.35	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 3.6 V		1.4~1.65		±5.0	μΑ
Power-off leakage current		loff	$V_{IN}$ , $V_{OUT} = 0$ to 3.6 V		0		10.0	μΑ
Quiescent supply curre	Ouisseent supply surrent		V <sub>IN</sub> = V <sub>CC</sub> or GND		1.4~1.65		20.0	Δ
Quiescent supply curre	:iit	Icc	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		1.4~1.65		±20.0	μА

# DC Characteristics (Ta = -40 to 85°C, 1.2 V $\leq$ V $_{CC}$ < 1.4 V)

Characteristi	Characteristics Symbol Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit		
Input voltage	H-level	V <sub>IH</sub>			1.2~1.4	0.8 × V <sub>CC</sub>	_	V
input voltage	L-level	V <sub>IL</sub>			1.2~1.4	_	0.05 × V <sub>CC</sub>	V
H-level		VoH	$V_{IN} = V_{IL}$	I <sub>OH</sub> = -100 μA	1.2	V <sub>CC</sub> - 0.1	_	V
	L-level	V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	$I_{OL} = 100 \mu A$	1.2		0.05	
Input leakage current	I <sub>IN</sub> V <sub>IN</sub> = 0 to 3.6 V		1.2		±5.0	μΑ		
Power-off leakage curre	ent	l <sub>OFF</sub>	$V_{IN}$ , $V_{OUT} = 0$ to 3.6 V		0	_	10.0	μΑ
Quiescent supply current		loo	$V_{IN} = V_{CC}$ or GND		1.2	_	20.0	μА
Quiescent supply curre	111	Icc	$V_{CC} \leqq V_{IN} \leqq 3.6 \ V$	$V_{CC} \le V_{IN} \le 3.6 \text{ V}$		_	±20.0	μΑ

### AC Characteristics (Ta = -40 to 85°C, input: $t_r = t_f = 2.0$ ns) (Note 1)

Characteristics	Symbol	Test Condition			Min	Max	Unit
			Г	V <sub>CC</sub> (V)			
			$C_{I} = 15 pF, R_{I} = 2 k\Omega$	1.2	3.0	37.0	
	t		OL = 10 β1 , RL = 2 R22	1.5 ± 0.1	2.0	14.8	
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2		1.8 ± 0.15	1.5	7.4	ns
	-\$PΓ1⊑		$C_L = 30 \text{ pF}, R_L = 500 \Omega$	$2.5 \pm 0.2$	8.0	3.7	
				$3.3 \pm 0.3$	0.6	2.8	
			C <sub>L</sub> = 15 pF, R <sub>L</sub> = 2 kΩ	1.2	_	1.5	
	<b>.</b>			1.5 ± 0.1	_	1.5	
Output to output skew	t <sub>osLH</sub>	(Note 2)		1.8 ± 0.15	_	0.5	ns
	USHL		$C_L = 30$ pF, $R_L = 500$ $\Omega$	$2.5 \pm 0.2$	_	0.5	
				$3.3 \pm 0.3$	_	0.5	

Note 1: For  $C_L = 50 \ pF$ , add approximately 300 ps to the AC maximum specification.

Note 2: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$ 

### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition		Тур.	Unit
Characteristics	Cymbol	rest condition	V <sub>CC</sub> (V)	Typ.	Onit
		$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 1.8	0.25	
Quiet output maximum dynamic V <sub>OL</sub>	$V_{OLP}$	$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 2.5	0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 3.3	8.0	
	V <sub>OLV</sub>	$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 1.8	-0.25	
Quiet output minimum dynamic V <sub>OL</sub>		$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 2.5	-0.6	V
		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 3.3	-0.8	
Quiet output minimum dynamic	V <sub>OHV</sub>	$V_{IH} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 1.8	1.5	
		$V_{IH} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 2.5	1.9	V
···		$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (No	e) 3.3	2.2	

Note: Parameter guaranteed by design.

### **Capacitive Characteristics (Ta = 25°C)**

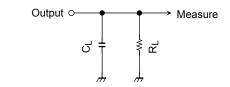
Characteristics	Symbol		Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>		_		1.8, 2.5, 3.3	6	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz		(Note)	1.8, 2.5, 3.3	20	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$ 

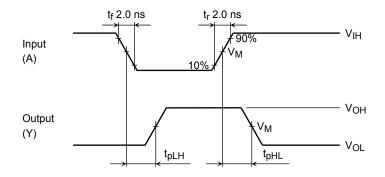
### **AC Test Circuit**



	V <sub>CC</sub>	
Symbol	$\begin{array}{c} 3.3 \pm 0.3 \text{ V} \\ 2.5 \pm 0.2 \text{ V} \\ 1.8 \pm 0.15 \text{ V} \end{array}$	1.5 ± 0.1 V 1.2V
RL	500 Ω	2 kΩ
CL	30 pF	15 pF

Figure 1

### **AC Waveform**

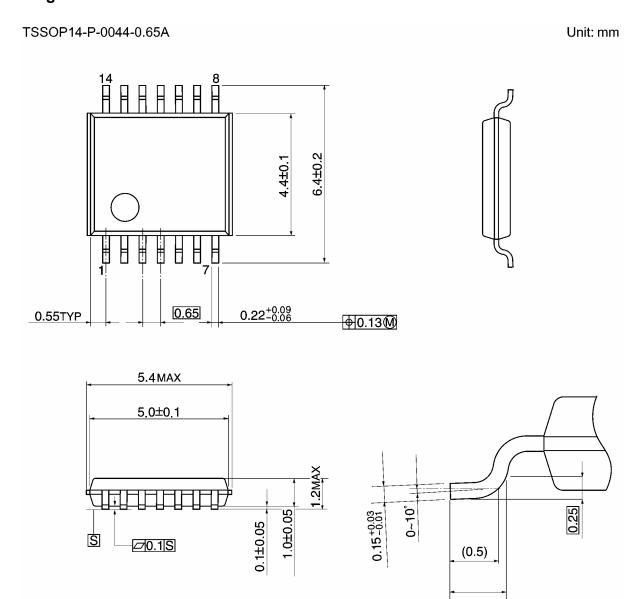


Symbol	V <sub>CC</sub>				
	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 ± 0.15 V	$1.5\pm0.1~\textrm{V}$	1.2 V
V <sub>IH</sub>	2.7 V	V <sub>CC</sub>	V <sub>CC</sub>	V <sub>CC</sub>	V <sub>CC</sub>
V <sub>M</sub>	1.5 V	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2	V <sub>CC</sub> /2

Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>



# **Package Dimensions**

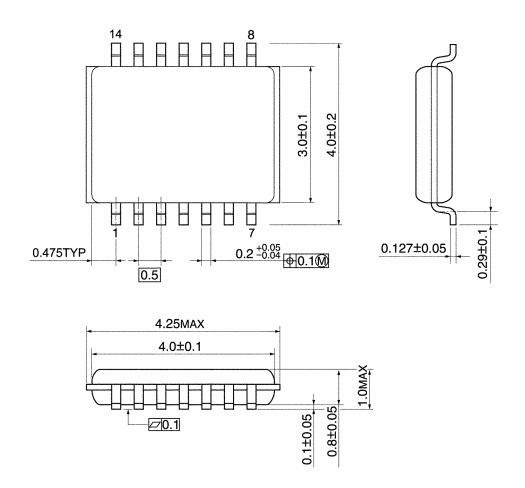


Weight: 0.06 g (typ.)

0.45~0.75

# **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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20070701-EN GENERAL

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