<u>TOSHIBA</u>

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74VHCU04F,TC74VHCU04FN,TC74VHCU04FT

Hex Inverter

The TC74VHCU04 is an advanced high speed CMOS INVERTER fabricated with silicon gate C²MOS technology.

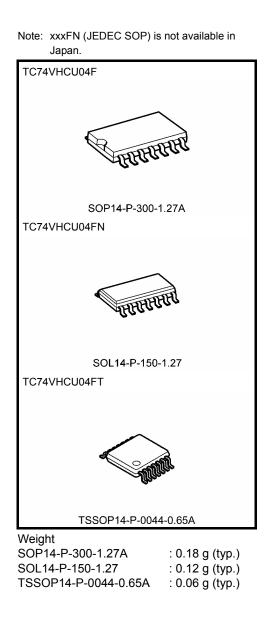
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Since the internal circuit is composed of a single stage inverter, it can be used in analog applications such as crystal oscillators.

An input protection circuit ensures that 0 to 5.5 V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5 V to 3 V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

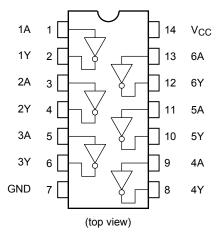
Features

- High speed: t_{pd} = 3.5 ns (typ.) at V_{CC} = 5 V
- Low power dissipation: $I_{\rm CC}$ = 2 μA (max) at Ta = 25°C
- High noise immunity: $V_{NIH} = V_{NIL} = 10\% V_{CC}$ (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 V to 5.5 V
- Low noise: VOLP = 0.8 V (max)
- Pin and function compatible with 74ALS04



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Pin Assignment



Truth Table

_	
А	Y
L	Н
Н	L

IEC Logic Symbol

1A <u>(1)</u>	1	(<u>2)</u> 1Y
2A <u>(3)</u>		(4) 2Y
3A <u>(5)</u>		<u>(6)</u> 3Y
4A(9)		<u>(8)</u> 4Y
5A		(10) 5Y
6A <u>(13)</u>		(<u>12)</u> 6Y

Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	IIК	-20	mA
Output diode current	IOK	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol		Test Condition		٦	Γa = 25°(0	-	a = 0 85°C	Unit
	,			$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
High-level input voltage	VIH	V _{OUT} = V _{OL}		2.0 3.0 to	1.70 V _{CC} ×	_	_	1.70 V _{CC} ×	_	V
				5.5	0.8			0.8	_	
Low-level input				2.0	_	_	0.30	_	0.30	
voltage	VIL	V _{OUT} = V _{OH}		3.0 to 5.5	—	_	V _{CC} × 0.2	_	V _{CC} × 0.2	V
		$V_{IN} = V_{IL}$		2.0	1.8	2.0	_	1.8	_	
	V _{OH}		I _{OH} = -50 μA	3.0	2.7	3.0	—	2.7	—	
High-level output voltage				4.5	4.0	4.5	_	4.0	—	V
renage			I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
		V _{IN} = GND	I _{OH} = -8 mA	4.5	3.94	—	—	3.80	—	
				2.0	_	0.0	0.2		0.2	
		$V_{IN} = V_{IH}$	$I_{OL} = 50 \ \mu A$	3.0	—	0.0	0.3	_	0.3	
Low-level output voltage	V _{OL}			4.5	—	0.0	0.5	—	0.5	V
Ũ		$V_{IN} = V_{CC}$	$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5	—	—	0.36	—	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or GND		5.5			2.0	_	20.0	μΑ

AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max		
			3.3 ± 0.3	15	_	5.0	8.9	1.0	10.5		
Propagation delay ^t pLH	_	5.5 <u>+</u> 0.5	50	_	7.5	11.4	1.0	13.0	ns		
time	t _{pHL}		5.0 ± 0.5	15	_	3.5	5.5	1.0	6.5	115	
		—		— 5.0±0.5	50		5.0	7.0	1.0	8.0	
Input capacitance	C _{IN}		_			4	10	_	10	pF	
Power dissipation capacitance	C _{PD}			(Note)	_	9		_		pF	

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

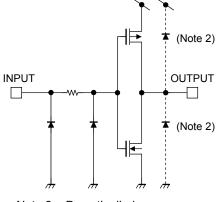
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6$ (per gate)

Noise Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		- Unit
	Symbol		V _{CC} (V)	Тур.	Max	Unit
Quiet output maximum dynamic V_{OL}	V _{OLP}	$C_L = 50 \text{ pF}$	5.0	0.5	0.8	V
Quiet output minimum dynamic V_{OL}	V _{OLV}	$C_L = 50 \text{ pF}$	5.0	-0.5	-0.8	V
Minimum high level dynamic input voltage	VIHD	$C_L = 50 \text{ pF}$	5.0	_	4.0	V
Maximum low level dynamic input voltage	V _{ILD}	$C_L = 50 \text{ pF}$	5.0	_	1.0	V

Input Equivalent Circuit



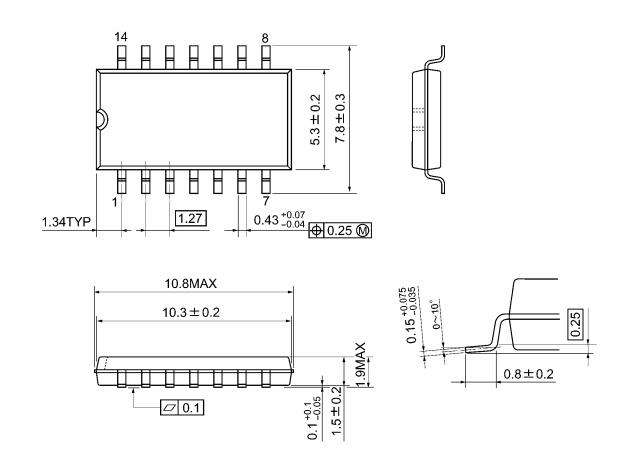
Note 2: Parastic diode



Package Dimensions

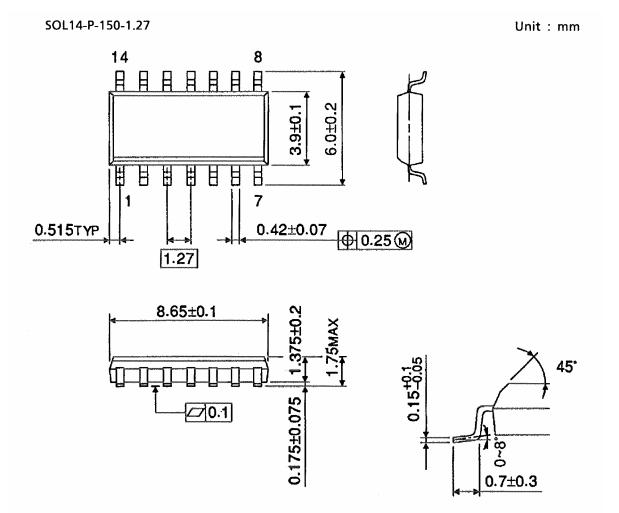
SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



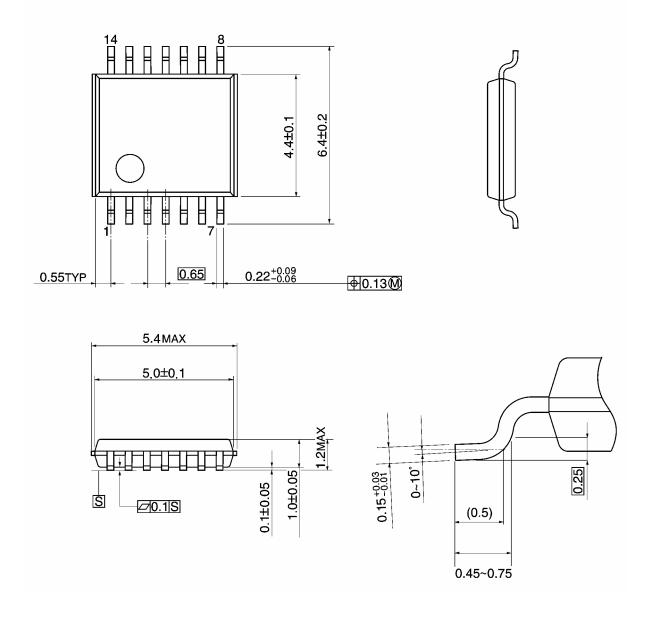
Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm



Weight: 0.06 g (typ.)

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20070701-EN GENERAL

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