TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

# TC75S59AFE,TC75S59AFC

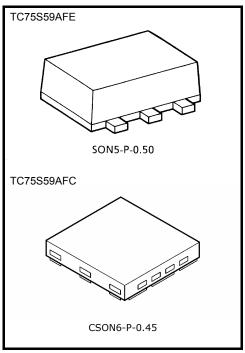
Single Comparator (Open-Drain Output)

The TC75S59AFE and TC75S59AFC are CMOS general-purpose single comparators. The devices can operate from a single supply voltage and are designed for a lower supply-current than conventional general-purpose bipolar comparators. The output is designed for Open-Drain Output and can supply a higher voltage than the power supply. Therefore, it is possible to pull-up the voltage to a level higher than that of the power supply. The Open-Drain Output can be wired-OR with another Open-Drain Output circuit.

\* Output voltage should not exceed the maximum rating.

#### **Features**

- Low Supply Current: I<sub>DD</sub> = 100 μA (typ.)
- Single Power Supply Operation
- Wide Common Mode Input Voltage Range: VSS~VDD 0.9 V
- Open-Drain Output Circuit
- Low Input Bias Current
- Small Package

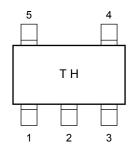


Weight

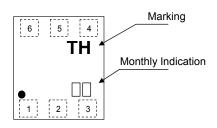
SON5-P-0.50 : 0.003 g (typ.) CSON6-P-0.45 : 0.002 g (typ.)

#### Marking (top view)

#### TC75S59AFE

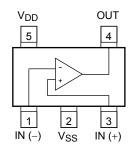


#### TC75S59AFC

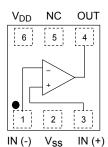


#### Pin Assignment (top view)

#### TC75S59AFE



#### TC75S59AFC





## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit
Supply Voltage	$V_{DD}, V_{SS}$	±3.5 or 7		V
Differential Input Voltage	DV <sub>IN</sub>	±7		V
Input Voltage	V <sub>IN</sub>	V <sub>SS</sub> ~V <sub>DD</sub>		V
Output Current	IO	±35		mA
Output Voltage	VO	V <sub>SS</sub> ~ V <sub>SS</sub> + 7		V
Power Dissipation	PD	TC75S59AFE	100	mW
		TC75S59AFC	100 (Note 1)	IIIVV
Operating Temperature	T <sub>opr</sub>	-40~85		°C
Strage Temperature	T <sub>stg</sub>	-55~125		°C

Note: Due to the CMOS structure, this device may be susceptible to latch-up. To prevent latch-up, please take the following precautions;

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- Ensure that no I/O pin's voltage level ever exceeds Vdd or drops below Vss. In addition, check the power-on timing.
- Do not subject the device to excessive noise.

(Note 1): FR4 in board implementation

 $(25.4 \text{mm} \times 25.4 \text{mm} \times 1.6 \text{t}, \text{Cu Pad}: 0.4 \text{mm}^2)$ 



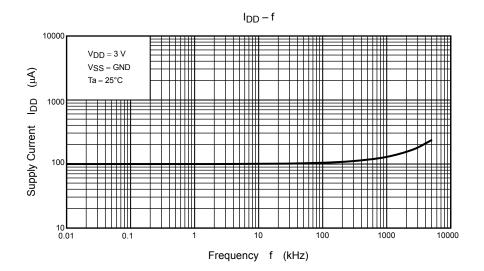
# Electrical Characteristics ( $V_{DD} = 5 V$ , $V_{SS} = GND$ , $Ta = 25^{\circ}C$ )

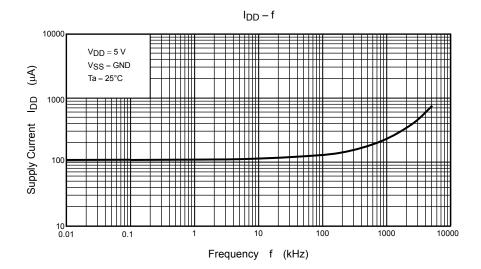
Characteristics	Symbol	Test Circuit	Test Condition	Min.	Тур.	Max.	Unit
Input Offset Voltage	$V_{IO}$	_	_	_	±1	±7	mV
Input Offset Current	I <sub>IO</sub>	_	_	_	1	_	pА
Input Bias Current	lı	_	_	_	1	_	pА
Common Mode Input Voltage	CMV <sub>IN</sub>	_	_	0	_	4.1	V
Supply Current	I <sub>DD</sub> (Note1)	_	_	_	110	220	μА
Voltage Gain	G <sub>V</sub>	_	_	_	94	_	dB
Sink Current	I <sub>sink</sub>	_	V <sub>OL</sub> = 0.5 V	13	25	_	mA
Output Leakage Current	I <sub>LEAK</sub>	_	V <sub>DD</sub> = 5 V , V <sub>O</sub> = 5 V	_	5	_	nA
Off-state Leakage Current	I <sub>OFF</sub>	_	V <sub>DD</sub> = 0 V , V <sub>O</sub> = 5 V	_	5	_	nA
Output Low Voltage	V <sub>OL</sub>	_	I <sub>sink</sub> = 5.0 mA	_	0.1	0.3	V
Operating Supply Voltage Range	$V_{DD}$	_	_	1.8	_	7.0	V
Propagation Delay (Turn on)	t <sub>PLH</sub> (1)	_	Over Drive = 100 mV	_	200	_	no
	t <sub>PLH</sub> (2)	_	TTLStep Input	_	140	_	ns
Propagation Delay (Turn off)	t <sub>PHL (1)</sub>	_	Over Drive = 100 mV	_	80	_	20
	t <sub>PHL</sub> (2)	_	TTL Step Input	_	60	_	ns
Response Time	t <sub>TLH</sub>		Over Drive = 100 mV	_	160	_	- ns
	t <sub>THL</sub>	_	Over Drive = 100 mV		3		

# Electrical Characteristics ( $V_{DD} = 3 V$ , $V_{SS} = GND$ , Ta = 25°C)

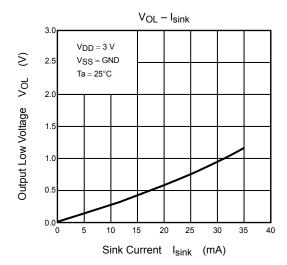
Characteristics	Symbol	Test Circuit	Test Condition	Min.	Тур.	Max.	Unit
Input Offset Voltage	V <sub>IO</sub>	_	_	_	±1	±7	mV
Input Offset Current	I <sub>IO</sub>	_	_	_	1	_	pА
Input Bias Current	lį	_	_	_	1	_	pA
Common Mode Input Voltage	CMV <sub>IN</sub>	_	_	0	_	2.1	V
Supply Current	I <sub>DD</sub> (Note1)	_	_		100	200	μΑ
Sink Current	I <sub>sink</sub>		V <sub>OL</sub> = 0.5 V	6	18	_	mA
Output Leakage Current	I <sub>LEAK</sub>		$V_{DD} = 3 V$ , $V_{O} = 3 V$		5	_	nA
Off-State Leakage Current	loff		$V_{DD} = 0 V$ , $V_{O} = 3 V$		5	_	nA
Output Low Voltage	V <sub>OL</sub>	_	I <sub>sink</sub> = 5.0 mA		0.15	0.35	>
Propagation Delay (Turn On)	t <sub>PLH</sub>	_	Over Drive = 100 mV	ı	160		ns
Propagation Delay (Turn Off)	t <sub>PHL</sub>	_	Over Drive = 100 mV	ı	70	_	ns
Response Time	t <sub>TLH</sub>	_	Over Drive = 100 mV	_	170	_	ns
	t <sub>THL</sub>	_	Over Drive = 100 mV	_	3	_	115

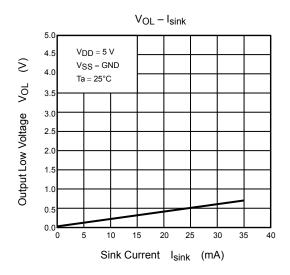
Note1: The current consumption of this device increases as its operating frequency increases. Note that the power dissipation should not exceed the allowable power.

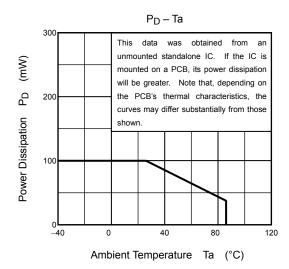




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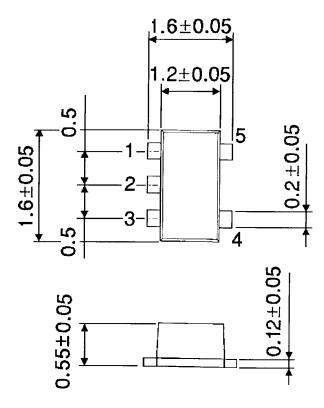




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# **Package Dimensions**

SON5-P-0.50 Unit: mm



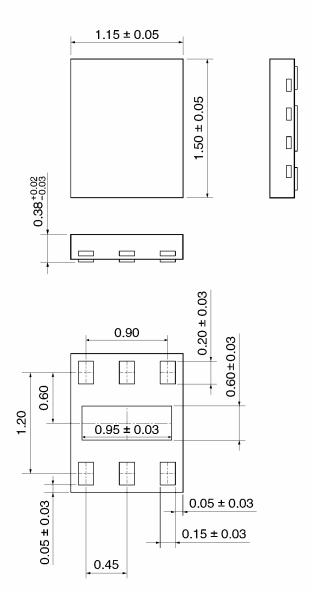
Weight: 0.003 g (Typ.)

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# **Package Dimensions**

CSON6-P-0.45

Unit: mm



Weight: 0.002 g (Typ.)

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