TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

SSM6K07FU

DC-DC Converters High Speed Switching Applications

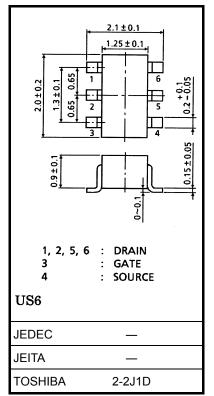
- Small package
- Low on resistance : $R_{on} = 130 \text{ m}\Omega \text{ max} (@V_{GS} = 10 \text{ V})$: $R_{on} = 220 \text{ m}\Omega \text{ max} (@V_{GS} = 4 \text{ V})$
 - Low input capacitance : $C_{iss} = 102 \text{ pF typ.}$

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: C_{rss} = 22 \text{ pF typ.}
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Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DS}	30	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC	۱ _D	1.5	А	
	Pulse	I _{DP}	3.0	~	
Drain power dissipation		PD	300	mW	
		(Note 1)	500		
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.

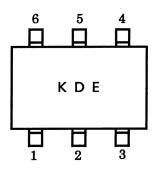


Weight: 6.8 mg (typ.)

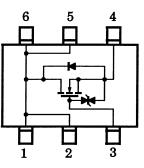
operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on FR4 board. (25.4 mm \times 25.4 mm \times 1.6 t, Cu pad: 0.32 mm2 \times 6)

Marking



Equivalent Circuit (top view)



Handling Precaution

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

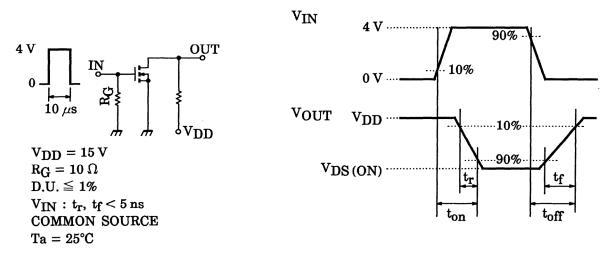
Unit: mm

Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	akage current I_{GSS} $V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$		—	_	±1	μA	
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	30		_	V
Drain cut-off curre	ent	I _{DSS}	$V_{DS} = 30 V, V_{GS} = 0$			1	μA
Gate threshold vo	Itage	V _{th}	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	1.1		1.8	V
Forward transfer a	admittance	Y _{fs}	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 0.75 \text{ A}$ (Note 2	2) 1.0		_	S
Drain-source ON resistance		R _{DS (ON)}	$I_D = 0.75 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 2	2) —	105	130	mΩ
		R _{DS (ON)}	$I_D = 0.75 \text{ A}, V_{GS} = 4 \text{ V}$ (Note 2	2) —	170	220	
		R _{DS (ON)}	$I_D = 0.75 \text{ A}, V_{GS} = 3.3 \text{ V}$ (Note 2	2) —	230	500	
Input capacitance		C _{iss}	$V_{DS}=15~V,~V_{GS}=0,~f$ = 1 MHz		102	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$		22	_	pF
Output capacitance		C _{oss}	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$		57	_	pF
Switching time	Turn-on time	t _{on}	$V_{DD} = 15 \text{ V}, \text{ I}_{D} = 0.75 \text{ A}, \text{ V}_{GS} = 0 \sim 4 \text{ V},$	—	46	_	ns
	Turn-off time	t _{off}	$R_{G} = 10 \Omega$		65	_	

Note 2: Pulse test

Switching Time Test Circuit

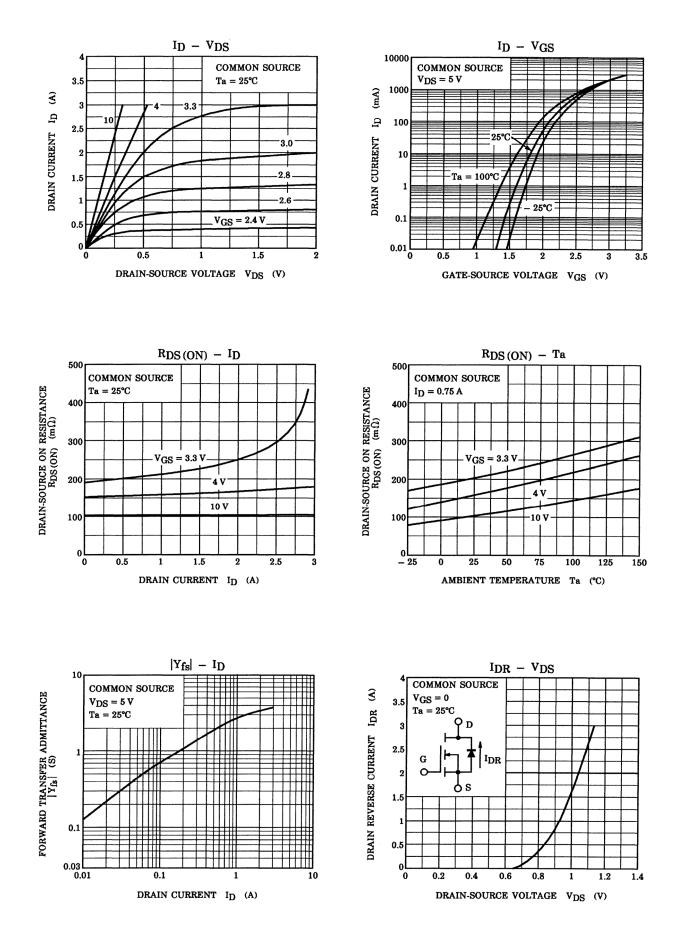


Precaution

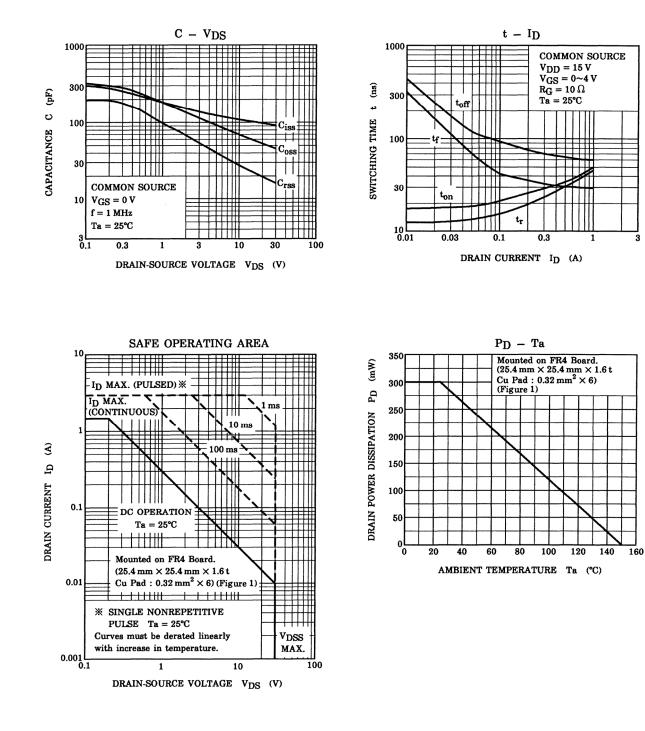
 V_{th} can be expressed as voltage between gate and source when low operating current value is I_D = 100 μA for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

(Relationship can be established as follows: V_{GS} (off) $< V_{th} < V_{GS}$ (on)) Please take this into consideration for using the device.

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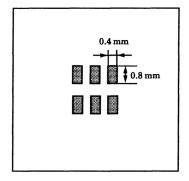


Figure 1 25.4 mm \times 25.4 mm \times 1.6 t, Cu Pad: 0.32 mm² \times 6

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20070701-EN GENERAL

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