

### 1.24V programmable shunt voltage reference

#### **Features**

- Adjustable output voltage: 1.24 to 24V
- Several precision levels @ 25°C ±2%, ±1%, ±0.5% and ±0.25%
- Sink current capability: 0.4 to 100mA
- Industrial temperature range: -40°C to +125°C
- Performance compatible with industry standard TL431

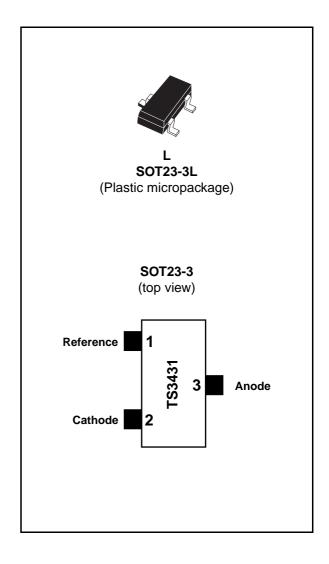
#### **Applications**

- Computers
- Instrumentation
- Battery chargers
- Switch mode power supply
- Battery operated equipment

#### Description

The TS3431 is a programmable shunt voltage reference with guaranteed temperature stability over the entire operating temperature range (-40°C to +125°C). The output voltage can be set to any value between 1.24V and 24V with an external resistor bridge.

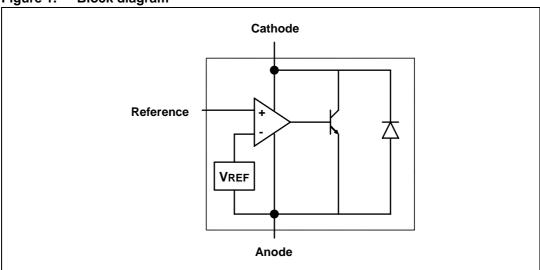
Available in SOT23-3 surface mount package, it can be used in application designs where space saving is critical.



Block diagram TS3431

## 1 Block diagram

Figure 1. Block diagram



## 2 Absolute maximum ratings

Table 1. Absolute maximum ratings (AMR)

Symbol	Parameter	Value	Unit
$V_{KA}$	Cathode to anode voltage	25	V
I <sub>K</sub>	Reverse breakdown current	-100 to +150	mA
I <sub>REF</sub>	Reference current	-0.05 to10	mA
P <sub>d</sub>	Power dissipation <sup>(1)</sup> SOT23-3L	360	mW
T <sub>stg</sub>	Storage temperature	-65 to +150	°C
ESD	Human body model (HBM)	2	kV
	Machine model (MM)	200	V
T <sub>lead</sub>	Lead temperature (soldering, 10 seconds)	250	°C

<sup>1.</sup>  $P_d$  is calculated with  $T_{amb}$  = 25°C,  $T_j$  = 150°C,  $R_{thjc}$  = 110°C/W,  $R_{thja}$  = 340°C/W.

Table 2. Operating conditions

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Symbol	Parameter	Value	Unit
I <sub>K</sub>	Cathode operating current	0.5 to 100	mA
V <sub>K</sub>	Cathode operating voltage	1.24 to 24	V
T <sub>oper</sub>	Operating free air temperature range	-40 to +125	°C

## 3 Electrical characteristics

Table 3.  $T_{amb} = 25^{\circ}C \text{ (unless otherwise specified)}^{(1)}$ 

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit		
V <sub>K</sub>	Reference input voltage I <sub>K</sub> = 10mA	TS3431 (2%)	1.215		1.265	V		
		TS3431A (1%)	1.228	1.24	1.252			
		TS3431B (0.5%)	1.234	1.24	1.246			
		TS3431C (0.25%)	1.237		1.243			
		0°C < T < +70°C			10			
$\Delta V_{K}$	Variation of reference input voltage over temperature	-40°C < T < +105°C			18	mV		
	over temperature	-40°C < T < +125°C			21			
T <sub>C</sub>	Temperature coefficient	-40°C < T < +125°C			100	ppm/°C		
	Minimum operating current	T = 25°C		0.35	0.4	mA		
I <sub>Kmin</sub>		-40°C < T < +125°C			0.5			
∆Vref	Ratio of change in reference input voltage to change in cathode to anode	I <sub>K</sub> =10mA V <sub>K</sub> = 24 to 1.24V		1.2	1.5	mV/V		
∆Vka	voltage	-40°C < T < +125°C			2			
	Reference input current $I_K=10\text{mA}$ , R1=10K $\Omega$ , R2=+ $\infty$	T= 25°C		0.9	1.5	μА		
I <sub>REF</sub>		-40°C < T < +125°C			2			
$\Delta I_{REF}$	Reference input current deviation	0°C < T < +70°C		0.5	1			
	$I_K$ =10mA, R1=10KΩ, R2=+	-40°C < T < +125°C		0.9	1.5	μΑ		
I <sub>OFF</sub>	Off-state cathode current V <sub>K</sub> =24V	T= 25°C		35	500			
		-40°C < T < +105°C			1000	nA		
		-40°C < T < +125°C			2000			
R <sub>KA</sub>	Reverse static impedance	I <sub>K</sub> = 1 to 100mA		0.2	0.4	W		
E <sub>N</sub>	Wideband noise	I <sub>K</sub> = 10mA 1kHz < f < 100kHz		100		nV/√Hz		

<sup>1.</sup> Limits are 100% production tested at 25°C. Behavior at the temperature range limits is guaranteed through correlation and by design.

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Electrical characteristics TS3431

Figure 2. Reference voltage vs. temperature Figure 3. Test circuit for  $V_K = Vref$ 

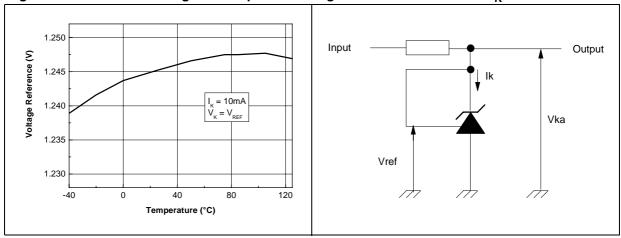


Figure 4. Cathode voltage vs cathode current Figure 5. Minimum operating current vs temperature

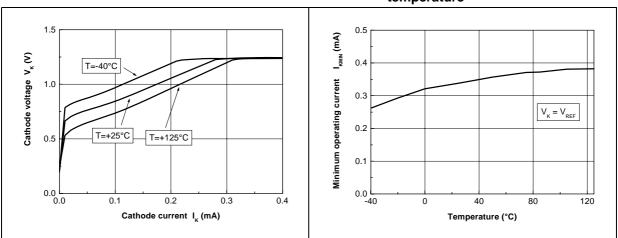
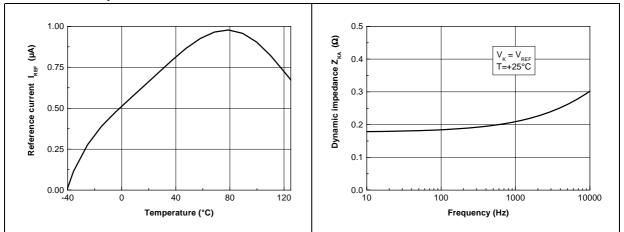


Figure 6. Reference input current vs temperature

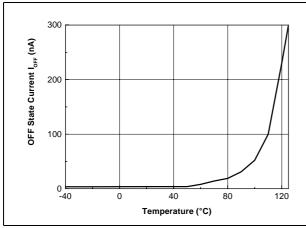
Figure 7. Dynamic impedance vs frequency



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Figure 8. Off-state current vs temperature

Figure 9. Test circuit for off-state current measurement



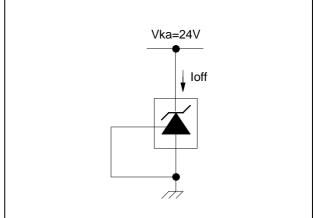
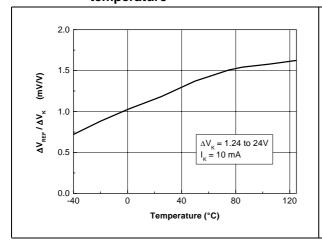


Figure 10. Ratio of change in reference input Figure 11. Test circuit for  $V_K > V_{REF}$ voltage to change in  $V_{KA}$  voltage vs temperature



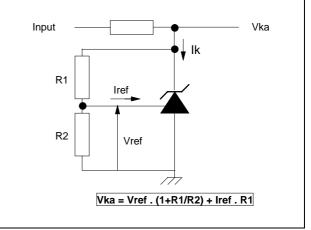
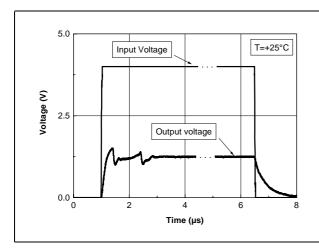
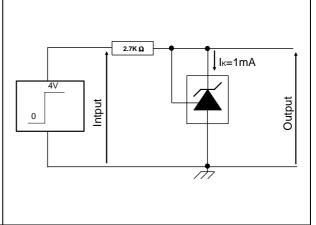


Figure 12. Pulse response at  $I_K=1mA$ 

Figure 13. Test circuit for pulse response at  $I_K = 1mA$ 

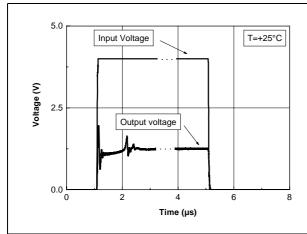




Electrical characteristics TS3431

Figure 14. Pulse response at  $I_K = 10mA$ 

Figure 15. Test circuit for pulse response at  $I_K = 10 \text{mA}$ 



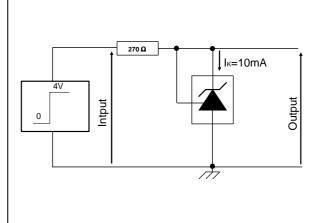
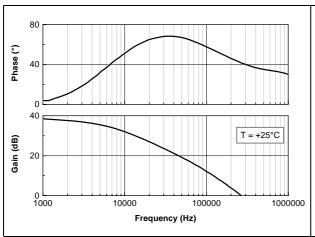


Figure 16. Phase and gain vs frequency

Figure 17. Equivalent input noise vs frequency



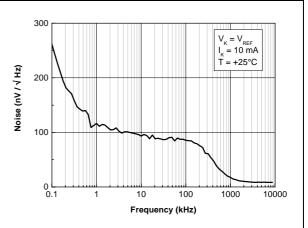
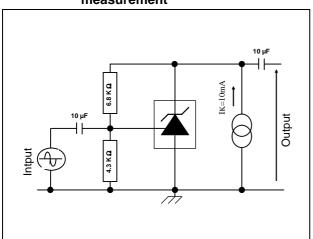


Figure 18. Test circuit for phase and gain measurement



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TS3431 Package information

### 4 Package information

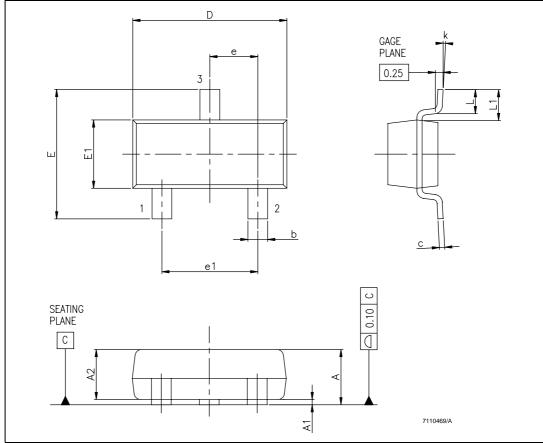
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Package information TS3431

Figure 19. SOT23-3L package mechanical data

	Dimensions						
Ref.	Millimeters			Mils			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
А	0.890		1.120	35.05		44.12	
A1	0.010		0.100	0.39		3.94	
A2	0.880	0.950	1.020	34.65	37.41	40.17	
b	0.300		0.500	11.81		19.69	
С	0.080		0.200	3.15		7.88	
D	2.800	2.900	3.040	110.26	114.17	119.72	
Е	2.100		2.64	82.70		103.96	
E1	1.200	1.300	1.400	47.26	51.19	55.13	
е		0.950			37.41		
e1		1.900			74.82		
L	0.400		0.600	15.75		23.63	
L1		0.540			21.27		
k	0°		8°	0°		8°	



TS3431 Ordering information

# 5 Ordering information

Table 4. Order codes

Temperature range	Package	Packaging	Marking
	SOT23-3L	Tape & reel	L280 L281
-40°C, +125°C			L282 L283
	range	range Package	range Package Packaging

# 6 Revision history

Date	Revision	Changes
1-Jan-2004	1	Initial release.
1-Dec-2004	2	Specific content changes as follows:  - CI version added in <i>Table 4: Order codes</i> .  - R <sub>thjc</sub> information added in <i>Table 1: Absolute maximum ratings</i> (AMR).  - Test condition added in electrical characteristics <i>Table 3</i> .
26-Jun-2007 3		Removed TO-92 package information and associated order codes.  Re-ordered electrical characteristics figures.

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