

SANYO Semiconductors DATA SHEET



Monolithic Linear IC Separately-excited Step-down Switching Regulator (5V)

Overview

The LA5777MP is a Separately-excited step-down switching regulator (5V).

Functions

- High efficiency.
- Six external parts.
- Time-base generator (160kHz) incorporated.
- Current limiter incorporated.
- Thermal shutdown circuit incorporated.
- ON/OFF function.

Specifications Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Input voltage	V _{IN} max		30	V
Maximum Output current	I _O max		3	А
SW pin application reverse voltage	V _{SW}		-1	V
Allowable power dissipation	Pd max	Mounted on a substrate.*	3.9	W
Operating temperature	Topr		-30 to +125	°C
Storage temperature	Tstg		-40 to +150	°C

* Specified substrate : 76.1×114.3×1.6mm³ : Copper foil ratio 60% FR4

Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage range	VIN		8 to 28	V

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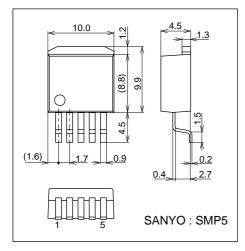
Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{O} = 3.3V$

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Reference voltage	VO	V _{IN} = 15V, I _O = 1.0A	4.80	5.00	5.20	V
Efficiency	η	V _{IN} = 15V, I _O = 1.0A		84		%
Switching frequency	f	V _{IN} = 15V, I _O = 1.0A	128	160	192	kHz
Switching frequency when short-circuit protection is active	fshort	V _{IN} = 15V, V _{OS} = 0V	15	30	45	kHz
Line regulation	$\Delta V_O LINE$	$V_{IN} = 8 \text{ to } 20V, I_{O} = 1.0A$		40	100	mV
Load regulation	$\Delta V_O LOAD$	$V_{IN} = 15V, I_{O} = 0.5 \text{ to } 1.5A$		10	30	mV
Output voltage temperature coefficient	∆V _O /∆Ta	Designed target value. *		±0.5		mV/°C
Ripple attenuation factor	RREJ	f = 100 to 120Hz		45		dB
Output leak current	lOleak	V _{IN} = 15V, SW _{OUT} = -1V			50	μA
Current limiter operating voltage	۱ _S	V _{IN} = 15V	3.1			А
Operating current	IVIN	V _{IN} = 15V		5.6		mA
Standby current	ISTBY	V _{IN} = 15V, ENA = 5V		50	100	μA
ENA pin LOW voltage range	VENAL				0.6	V
ENA pin HIGH voltage range	V _{ENA} H		2.4		V _{IN}	V
Thermal shutdown operating temperature	TSD	Designed target value. *		165		°C
Thermal shutdown Hysteresis width	ΔTSD	Designed target value. *		15		°C

* Design target value: No measurement made.

Package Dimensions

unit : mm (typ) 3275



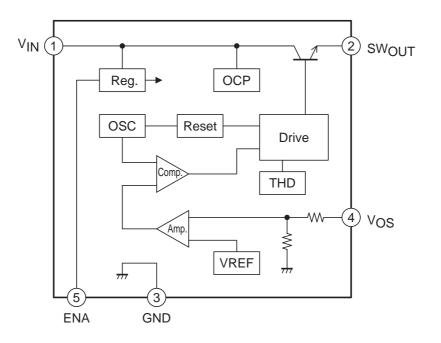
4.5 Specified sbstrate: 76.1×114.3×1.6mm³ Allowable power dissipation, Pd max - W - Copper foil ratio 60% FR4-4.0 3.90 3.5 3.0 2.5 2.0 1.5 1.0 0.5 -30-20 20 40 60 80 100 120 140 160 0 Ambient temperature, Ta - °C MSG06072

Pd max - Ta

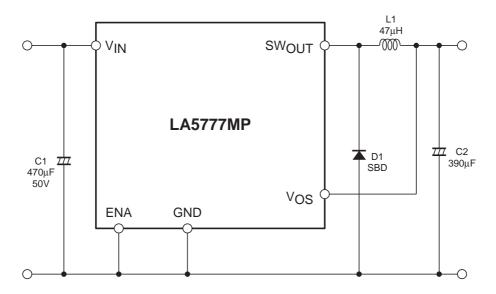
Pin Assignment

(1) $V_{\mbox{IN}}$ (2) $SW_{\mbox{OUT}}$ (3) GND (4) $V_{\mbox{OS}}$ (5) ENA

Block Diagram



Application Circuit Example



Note: ENA pin starts operation with LOW voltage input.

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