

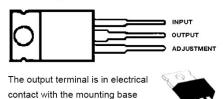
### **Description**

The 3 17 are adjustable 3 - terminal positive -voltage regulators capable of supplying 1.5A over a differential voltage range of 3Vto require only two external resistors to set the output voltage. 40V. They are exceptionally easy to use and Both input and output regulation are better than standard fixed regulators. In addition to higher performance than fixed regulators, these regulators offer full overload protection available only in integrated circuits. Included on the chip are current limit, thermal overload protection, and safe-area protection. All overload protection circuitry remains fully functional even if the adjustment terminal is disconnected. Normally, no capacitors are needed unless the device is situated far from the input filter capacitors in which case an input bypass is needed. An optional output capacitor can be added to improve transient response. The primary applications of each of these regulators is that of a programmable output regulator, but by connecting a fixed resistor between adjustment terminal and the output terminal, each device can precision current regulator. Even though the regulator is floating and sees only the input-to-output differential voltage, use of these devices to regulate output voltages that would cause the maximum-rated differential voltage to be exceeded if the output became shorted to ground is not recommended.

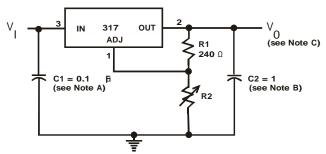
#### **Features**

- Output Voltage Range Adjustable from 1.2 V to 37 V
- Output Current Capability of 1.5 A Max
- Input Regulation Typically 0.01 % Per Input Volt Change
- Output Regulation Typically 0.1 %
- Peak Output Current Constant Over Temperature Range of Regulator
- Popular 3-Lead TO-220 Package
- Ripple Rejection Typically 80 dB

#### Package Information



# Typical application data



Note A: Use of an input bypass capacitor is recommended if regulator is far from filter capacitors. Note B: Use of an output capacitor improves transient response but is optional.

V<sub>ref</sub> equals the difference between the output and adjustment terminal



## **Absolute Maximum Ratings**

OVER OPERATING TEMPERATURE RANGE (UNLESS OTHERWISE NOTED)

Parameter	Maximum	
Units		
Input-to-output differential voltage, Vi - Vo	40	V
Continuous total dissipation at 25 °C free-air temperature	2	W
Continuous total dissipation at (or below) 25 ℃ case temperature	15	W
Operating free-air, case, or virtual junction temperature range	0 to 125	~
Storage temperature range	-65 to 150	<del>- &amp;</del>

## **Recommended Operating Conditions**

Parameter	LM 317		Units
	min	max	
Output current, lo	10	1500	mA
Operating virtual junction temperature, TJ	0	125	&

### **Electrical Characteristics**

Electrical Characteristics over recommended ranges of operating virtual junction temperature (unless other wise noted) (Note 1)

Parameter Test Conditions*		317			Units	
			Min	Тур	Max	
Input regulation (Note 2)	V-V0=3 V to 40 V,	TJ = MIN to MAX		0.01	0.04	%/ V
	(Note 3)	$I_0 = 10 \text{ mA to } 1.5 \text{ A}$		0.02	0.07	
Ripple rejection	Vo=10 V, f=120 Hz			65		dB
	Vo=10 V, f=120 Hz 10-µF capacitor between ADJ and ground		66	80		
Output regulation	lo = 10 mA to 1.5 A,	Vo ≤5 V		5	25	mV
	T <sub>J</sub> = 25 ℃, (See Note 3)	Vo > 5 V		0.1	0.5	%
	lo = 10 mA to 1.5 A,	Vo ≤ 5 V		20	70	mV
	(Note 3)	Vo > 5 V		0.3	1.5	%
Output voltage change with temperature	T <sub>J</sub> = MIN to MAX	•		1		%
Output voltage long-term drift (Note 4)	After 1000 h at T <sub>J</sub> = MAX and V <sub>I</sub> -Vo =40 V			0.3	1	%
Output noise voltage	f= 10 Hz to 10 kHz, T₃ =25℃			0.003		%
Minimum output current to maintain regulation	V <sub>I</sub> -V <sub>O</sub> = 40 V			3.5	10	mA
Peak output current	V <sub>I</sub> -Vo ≤ 15 V		1.5	2.2		Α
V <sub>I</sub> -V <sub>O</sub> ≤ 40 V, T <sub>J</sub> =25℃		0.15	0.4			
Adjustment-terminal current				50	100	μA
Change in adjustment-terminal current	V-Vo=2.5 V to 40 V, lo = 10 mA to 1.5 A			0.2	5.0	μA
Reference voltage (output to ADJ)	$V_FVO = 3 V \text{ to } 40 V, \text{ lo=}10 \text{ mA to } 1.5 \text{ A},$ $P \le 15W$		1.2	1.25	1.3	V





- \* Unless otherwise noted, these specifications apply for the following test conditions:  $V_1 V_0 = 5V$  and  $I_0 = 0.5$  A. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.
- Note 1: All characteristics are measured with a 0.1 µF capacitor across the input and a 1 µF capacitor across the output.
- Note 2: Input regulation is expressed here as the percentage change in output voltage per 1 V change at the input.
- Note 3: Pulse testing techniques are used to maintain the junction temperature as close to the ambient tempe rature as possible. Thermal effects must be taken into account separately.
- Note 4: Since long-term drift cannot be measured on the individual devices prior to shipment, this specification is not intended to be a guarantee or warranty. It is an engineering estimate of the average drift to be expected from lot to lot.

### **Ordering Information**

ORDERING NUMBER	PACKAGE	MARKING
LM317	TO - 220	LM317

Address: 北京市海淀区永定路 88 号长银大厦 6A06--6A07

Rm 6A07, Changyin Office Building , No. 88, Yong Ding Road, Hai Dian District , Beijing

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