N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY			
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)[a]	Q_g (Typ)
30	0.0094 at V_{GS} = 10 V	16	14 nC
	0.0115 at V_{GS} = 4.5 V	14	

FEATURES

- Extremely Low Q_{gd} WFET® Technology for Low Switching Losses
- TrenchFET® Power MOSFET
- 100 % R_g Tested

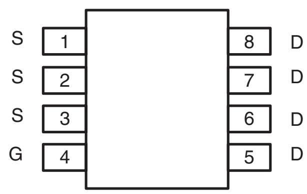
Pb-free

RoHS COMPLIANT

APPLICATIONS

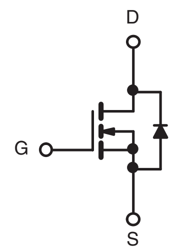
- High-Side DC/DC Conversion
  - Notebook
  - Server

SO-8



Top View

Ordering Information: Si4684DY-T1-E3 (Lead (Pb)-free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 12	
Continuous Drain Current (T_J = 150 °C)	T_C = 25 °C	I_D	16	A
	T_C = 70 °C		12.9	
	T_A = 25 °C		12[b,c]	
	T_A = 70 °C		9.5[b,c]	
Pulsed Drain Current		I_{DM}	50	
Continuous Source-Drain Diode Current	T_C = 25 °C	I_S	4.0	
	T_A = 25 °C		2.3[b,c]	
Single Pulse Avalanche Current	L = 0.1 mH	I_{AS}	20	
Avalanche Energy		E_{AS}	20	mJ
Maximum Power Dissipation	T_C = 25 °C	P_D	4.45	W
	T_C = 70 °C		2.85	
	T_A = 25 °C		2.50[b,c]	
	T_A = 70 °C		1.6[b,c]	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[b,d]	t ≤ 10 sec	R_{thJA}	36	50	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	22	28	

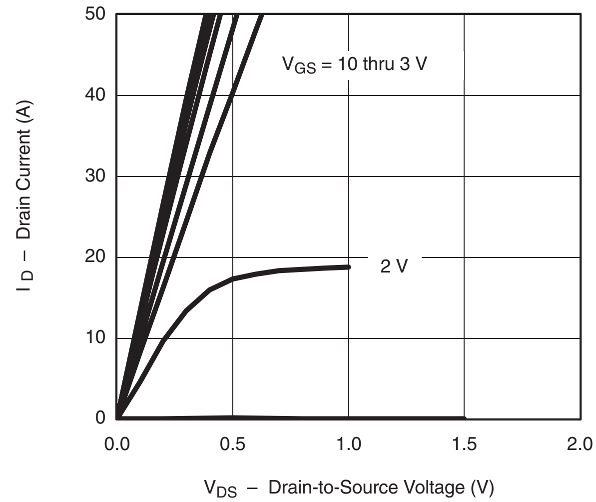
Notes:
a. Based on T_C = 25 °C.
b. Surface Mounted on 1" x 1" FR4 board.
c. t = 10 sec.
d. Maximum under Steady State conditions is 90 °C/W.

SPECIFICATIONS T_J = 25 °C, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Min**	**Typ**	**Max**	**Unit**
Static						
Drain-Source Breakdown Voltage	V_{DS}	V_{GS}= 0 V, I_D = 250 µA	30			V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	I_D = 250 µA		30		mV/°C
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			4.5		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 250 µA	0.6		1.5	V
		$V_{DS} = V_{GS}$, I_D = 5 mA		1.1		
Gate-Source Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 12 V			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 30 V, V_{GS} = 0 V			1	µA
		V_{DS} = 30 V, V_{GS} = 0 V, T_J = 55 °C			10	
On-State Drain Current[a]	$I_{D(on)}$	$V_{DS} \geq$ 5 V, V_{GS} = 10 V	30			A
Drain-Source On-State Resistance[a]	$r_{DS(on)}$	V_{GS} = 10 V, I_D = 16 A		0.0078	0.0094	Ω
		V_{GS} = 4.5 V, I_D = 9.5 A		0.0092	0.0115	
Forward Transconductance[a]	g_{fs}	V_{DS} = 15 V, I_D = 16 A		45		S
Dynamic[b]						
Input Capacitance	C_{iss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz		2080		pF
Output Capacitance	C_{oss}			340		
Reverse Transfer Capacitance	C_{rss}			135		
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 10 V, I_D = 11 A		30	45	nC
		V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 11 A		14	21	
Gate-Source Charge	Q_{gs}			3		
Gate-Drain Charge	Q_{gd}			2.8		
Gate Resistance	R_g	f = 1 MHz	0.2	0.55	0.9	Ω
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 1.87 Ω $I_D \cong$ 8 A, V_{GEN} = 4.5 V, R_g = 1 Ω		15	25	ns
Rise Time	t_r			60	100	
Turn-Off Delay Time	$t_{d(off)}$			28	45	
Fall Time	t_f			9	15	
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = 15 V, R_L = 1.87 Ω $I_D \cong$ 8 A, V_{GEN} = 10 V, R_g = 1 Ω		12	20	
Rise Time	t_r			12	20	
Turn-Off Delay Time	$t_{d(off)}$			45	70	
Fall Time	t_f			11	18	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	T_C = 25 °C			4	A
Pulse Diode Forward Current[a]	I_{SM}				50	
Body Diode Voltage	V_{SD}	I_S = 2.3 A		0.70	1.1	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = 9.5 A, di/dt = 100 A/µs, T_J = 25 °C		30	45	ns
Body Diode Reverse Recovery Charge	Q_{rr}			26	40	nC
Reverse Recovery Fall Time	t_a			16		ns
Reverse Recovery Rise Time	t_b			14		

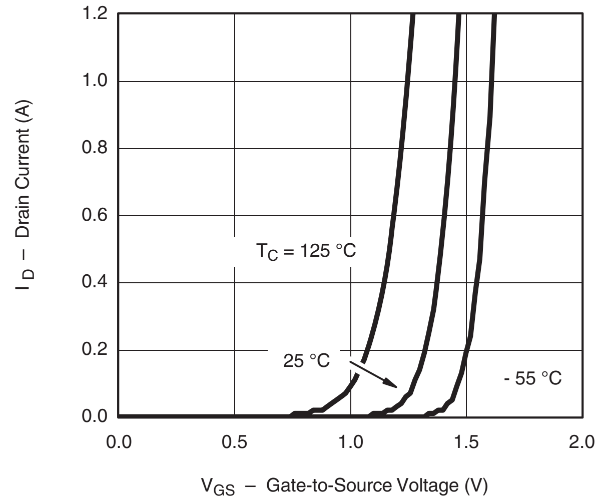
Notes:
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

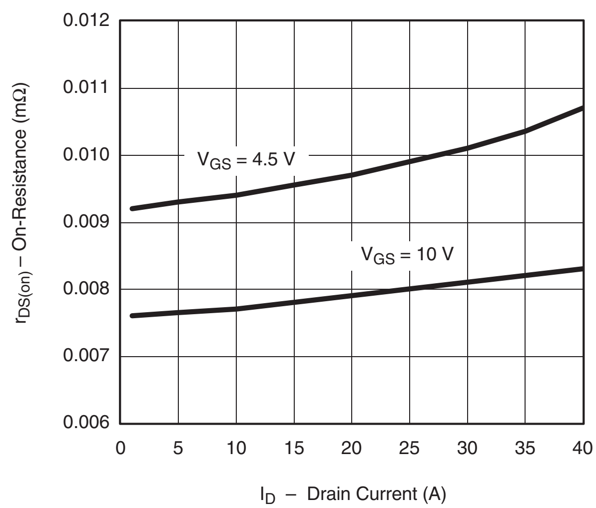
TYPICAL CHARACTERISTICS 25 °C unless noted



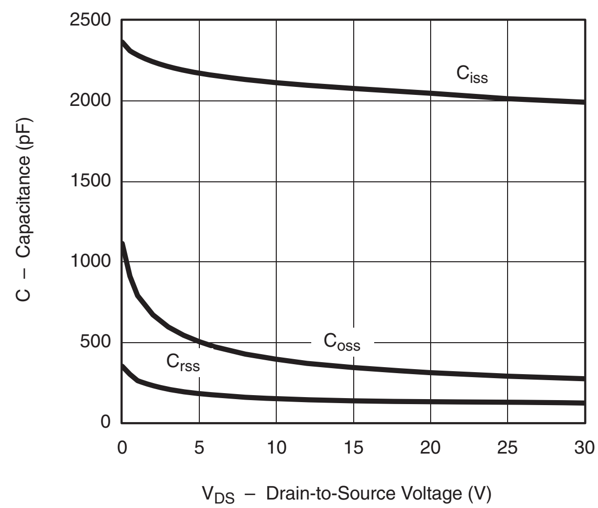
Output Characteristics



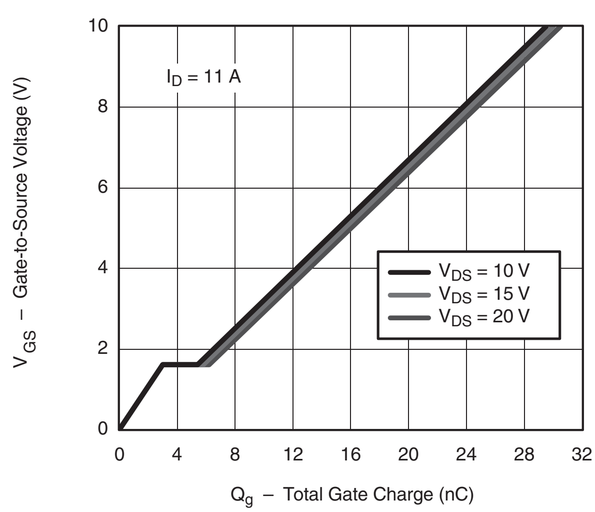
Transfer Characteristics



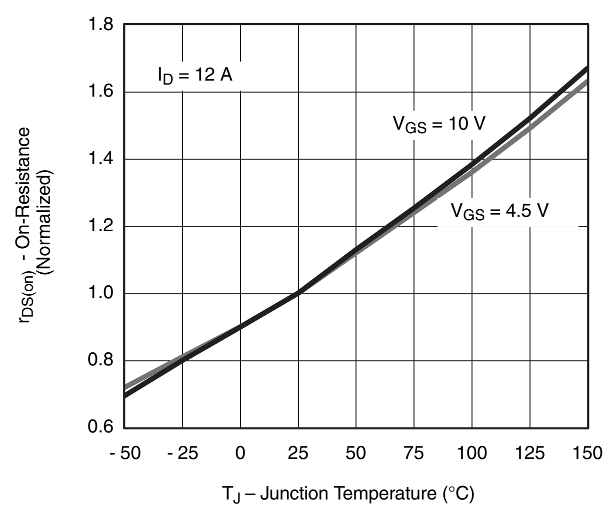
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

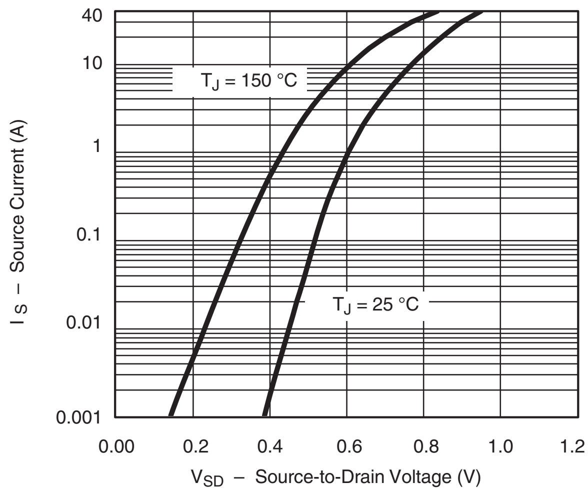


Gate Charge

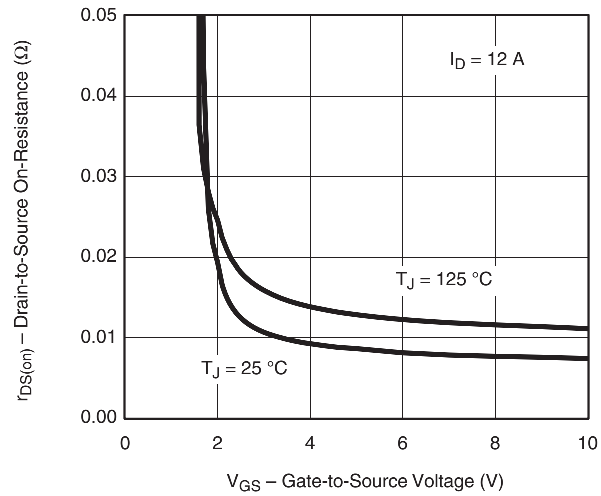


On-Resistance vs. Junction Temperature

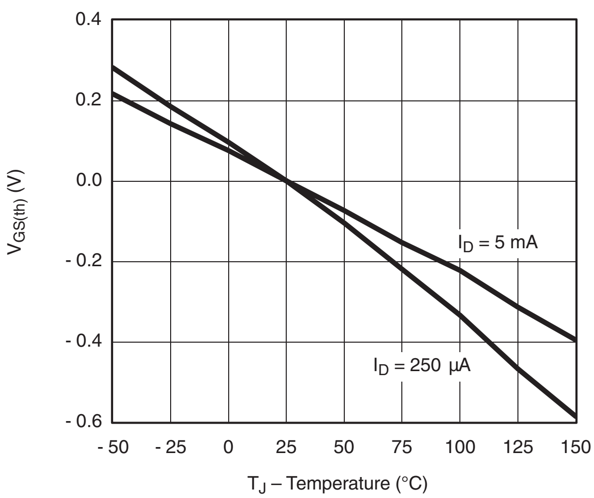
TYPICAL CHARACTERISTICS 25 °C unless noted



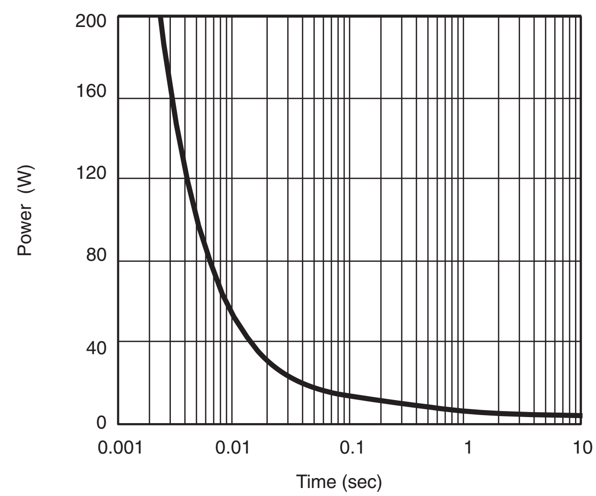
Source-Drain Diode Forward Voltage



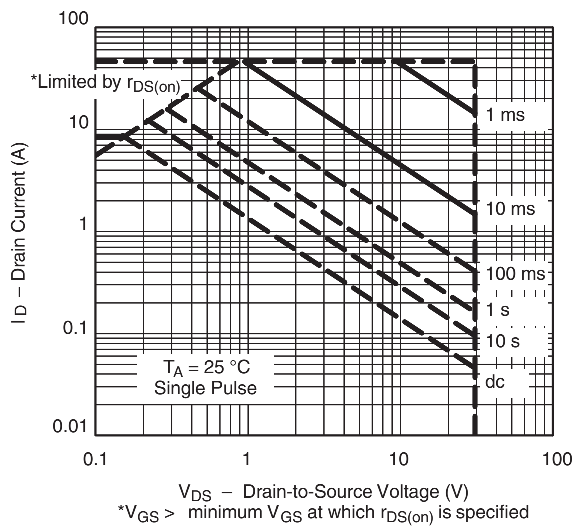
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

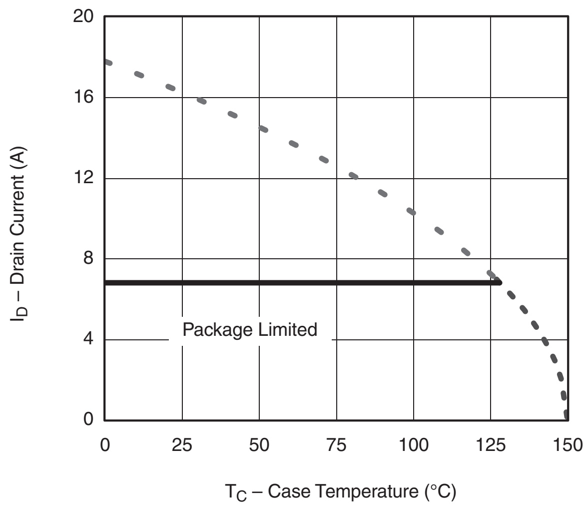


Single Pulse Power, Junction-to-Ambient

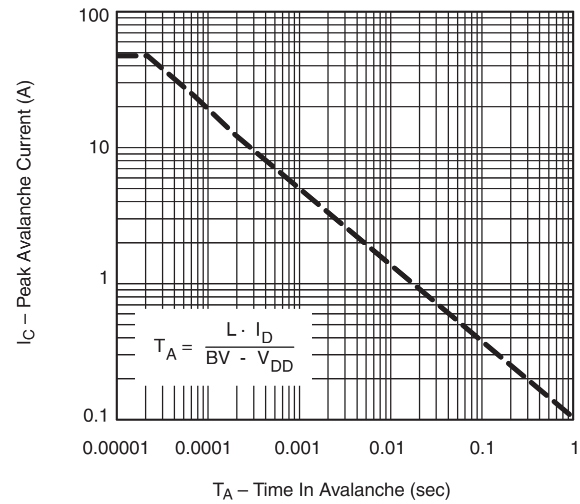


Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS 25 °C unless noted



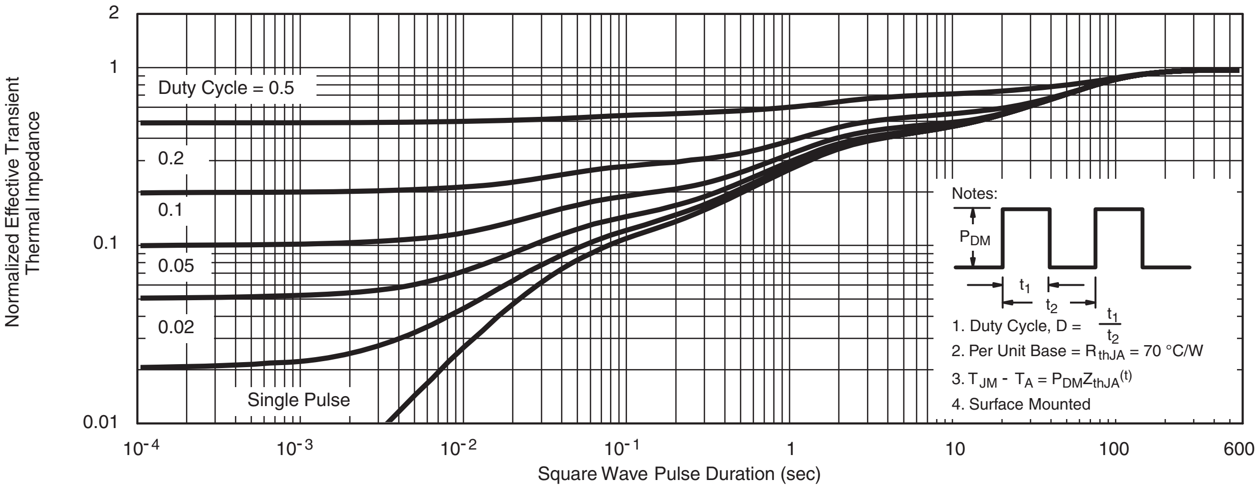
Current Derating*



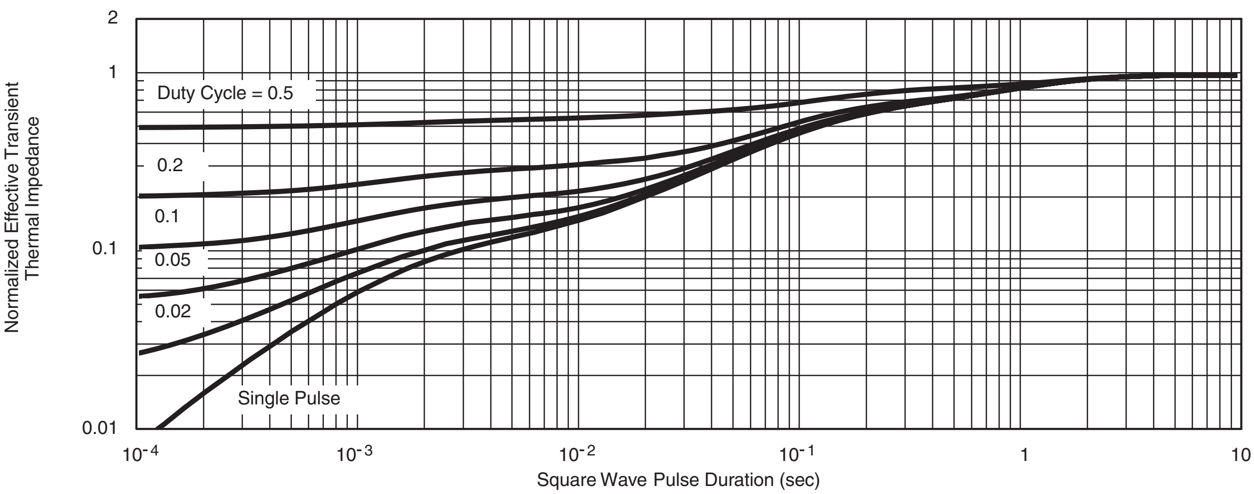
Single Pulse Avalanche Capability

*The power dissipation P_D is based on $T_{J(max)}$ = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C unless noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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