TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

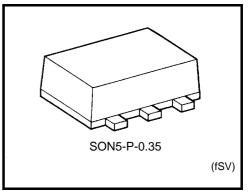
TC7SH14FS

SCHMITT INVERTER

Features

High speed: t_{pd} = 5.5 ns (typ.) at V_{CC} = 5 V Low power dissipation: $I_{CC} = 2 \ \mu A \ (max)$ at Ta = 25°C High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min) 5.5V tolerant input.

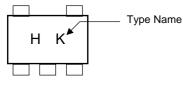
Wide operating voltage range: V_{CC} (opr) = 2~5.5 V

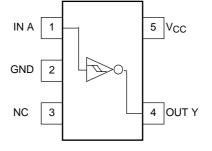


Marking (top view)

Pin Assignment

Weight : 0.001 g (Typ.)





Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	VIN	-0.5~7.0	V
DC output voltage	VOUT	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65~150	°C

Logic Diagram



А	Y
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Truth Table

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Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0~5.5	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Circuit		Test				Ta = 25°C			Ta = -40~85°C		
			Test Condition V _{CC} (V)			Тур.	Max	Min	Max	Unit	
Positive					3.0	_		2.20	_	2.20	
Threshold	V_{P}		—		4.5	—		3.15		3.15	V
voltage					5.5	_		3.85		3.85	
Negative					3.0	0.90		_	0.90		
Threshold	VN		—			1.35			1.35		V
voltage					5.5	1.65		_	1.65		
TT					3.0	0.30		1.20	0.30	1.20	
Hysteresis voltage	VH				4.5	0.40		1.40	0.30	1.40	V
voltage					5.5	0.30	—	1.60	0.30	1.60	
			VIN = VIL	I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
High-level V _{OH} output voltage					3.0	2.9	3.0	_	2.9		
	V _{OH}	—			4.5	4.4	4.5	_	4.4	_	
				$I_{OH} = -4 \text{ mA}$	3.0	2.58			2.48		
				I _{OH} = -8 mA	4.5	3.94			3.80		
			V _{IN} = VIH	I _{OL} = 50 μA	2.0	_	0.0	0.1	—	0.1	V
Low-level output Voltage					3.0		0.0	0.1	_	0.1	
	V _{OL}	V _{OL} —			4.5		0.0	0.1	_	0.1	
			I _{OL} = 4 mA	3.0			0.36	_	0.44		
				I _{OL} = 8 mA	4.5			0.36	_	0.44	
Input leakage current	I _{IN}	_	V _{IN} = 5.5 V or GND		0~ 5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc		$V_{IN} = V_{CC}$ or GND		5.5	_	_	2.0	_	20.0	μΑ

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbo	Symbol	Test	٦	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
	Symbol	Circuit		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Unit
1 1				22102	15		8.3	12.8	1.0	15.0	ns
	tpLH			3.3 ± 0.3	50		10.8	16.3	1.0	18.5	
	t _{pHL}	_		5.0 ± 0.5	15		5.5	8.6	1.0	10.0	
					50		7.0	10.6	1.0	12.0	
Input capacitance	C _{IN}			_			4	10		10	рF
Power dissipation capacitance	C _{PD}	_			(Note)		14		_		pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

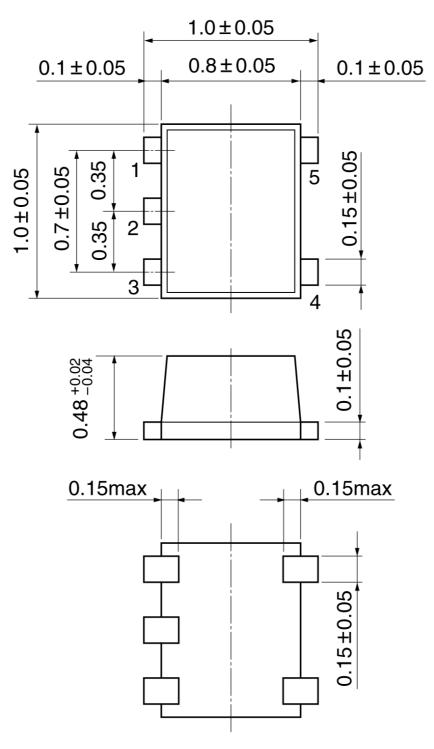
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.35

Unit:mm



Weight: 0.001 g (typ.)

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Handbook" etc..

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