TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

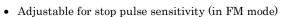
TA2022AFNG

1.5V AM/FM IF + MPX

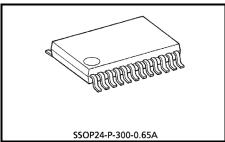
TA2022AFNG is the AM / FM IF + MPX system IC, which is developed for headphone radio in 1.5V use. This is included many functions and this can be used for digital tuning system with IF counter.

Features

- Suitable for combination with digital tuning system which is included IF counter.
 - Built-in AM / FM IF count output for IF counter of digital tuning system.
 FM: 10.7MHz
 AM: 450kHz

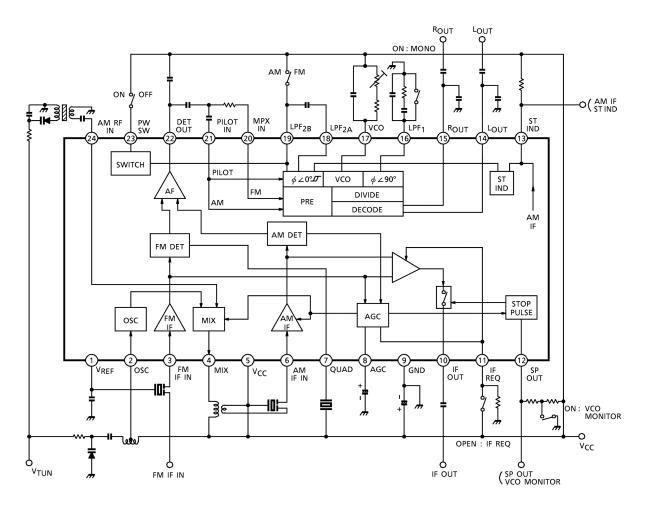


- For adopting ceramic discriminator, it is not necessary to adjust the FM QUAD detector circuit.
- Built-in AM IF output for AM stereo
- MPX input for main signal is independent of that for pilot signal. (MPX input for pilot signal is shared with AM input)
- MPX output is high-impedance in power off mode.
- Built-in power switch
- Low supply current (VCC = 1.2V, Ta = 25°C) FM mode: ICC2 = 5.5mA (typ.) AM mode: ICC3 = 3.7mA (typ.)
- Operating supply voltage range (Ta = 25°C) V_{CC} (opr) = 0.95~2.2V
- Stereo operating supply voltage range (Ta = 25°C) VCC (opr) = 1.0~2.2V



Weight: 0.14 g (typ.)

BLOCK DIAGRAM



Terminal Explanation Terminal Voltage: Typical Terminal Voltage at no Signal with The Test Circuit 1 ($V_{CC} = 1.2V$, Ta = 25°C)

٦	Terminal	Function	Internal Circuit	Terr Volta	ninal ae (V)
No.	Name			AM	FM
1	V _{REF}	Referential voltage Bypass terminal for AM RF and FM IF amplifier		0.9	0.9
2	OSC	AM OSC		1.2	1.2
4	MIX	Output of AM MIX		1.2	1.2
3	FM IF in	Imput of FM IF signal • Input impedance 330Ω (typ.)		0.9	0.9
5	V _{CC}	_	_	1.2	1.2
6	AM IF IN	Imput of AM IF signal • Input impedance 3kΩ (typ.)		1.2	1.2
7	QUAD	FM QUAD detector		1.2	1.2

٦	Terminal	Function	Internal Circuit	Tern Voltag	
No.	Name			AM	FM
8	AGC	 AM mode AGC time constant is determined by external capacitor. FM mode The sudden change of stop pulse sensitivity is held in by external capacitor. 		_	_
9	GND		_	0	0
10	IF OUT	Output of IF count signal In case of IF request mode, IF count signal for IF counter is taken from this terminal. FM: V _{IF (FM)} = 80mV _{p-p} (typ.) AM: V _{IF (AM)} = 100mV _{p-p} (typ.)		_	_
11	IF REQ	IF request switch V_{CC} : Receive mode Open: IF request mode FM stop pulse sensitivity can be changed by connecting R_{SEN} in IF request mode. R_{SEN} should be smaller than 100k Ω	75mV 75mV	_	_
12	SP OUT	Output of stop pulse VCO frequency can be monitored, in case that this terminal is connected with GND through a resistor ($R_{VCO} \doteq 100 k\Omega$). ON : VCO monitor OPEN : SP OUT	VCO + SP + m m	_	_
13	ST IND	 Stereo indicator terminal This terminal shared with output of modulated AM IF signal. AM IF output: 6mV_{rms} (R_{IF} = 3kΩ, typ.) R_{IF} should be smaller than 3kΩ. In case that R_{IF} > 3kΩ, internal biasing circuit doesn't operate normally in AM mode. 		_	_

-	Ferminal	Function	Internal Circuit	Tern Voltag	ninal ge (V)
No.	Name			AM	FM
14 15	L _{OUT}	Output of stereo signal These terminals are high– impedance in power off mode.		0.5	0.5
16	LPF1	LPF terminar for phase detector	ON : FM MONO		
17	vco	VCO The temperature characteristic of free running frequency is +350ppm / °C. Temperature coefficient: SH		_	1.2
18	LPF _{2A}	LPF terminal for synchronous detector		_	_
19	LPF _{2B}	LPF terminal for synchronous detector and AM / FM switch V _{CC} : AM mode OPEN: FM mode		1.2	_
21	PILOT IN	Input of FM stereo pilot signal and AM signal			0.1

٦	Ferminal	Function	Internal Circuit	Tern Voltag	ninal ge (V)	
No.	Name	A				
20	MPX IN	Input of FM stereo main signal		_	0.1	
22	DET OUT	Detector output circuit • Output impedance (typ.) AM: 10kΩ FM: 1kΩ		0.6	0.6	
23	PW SW	Power switch V _{CC} : IC on OPEN/GND: IC off		1.2	1.2	
24	AM RF IN	Input of AM RF signal • Input impedance: 13kΩ (typ., at no signal)		0.9	0	

Application Note

1. Power switch

It is necessary to connect an external pull-down resistor with the terminal of PW SW (pin (23)), in case that this IC is turned on due to external noise etc.

2. Mode switch

The terminal of AM / FM changeover is pin(19).

In controlling the $\rm AM$ / FM mode with voltage, it is applied as follows.

VCC : AM mode

OPEN: FM mode

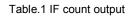
In FM mode, care should be taken to eliminate influence due to external noise etc, because this terminal is opened.

The leak current flows through this terminal, in the case that the terminal is connected to V_{CC} line independently, even through this IC is off mode (the terminal of PW SW (pin(23)) is off mode).

3. IF count output

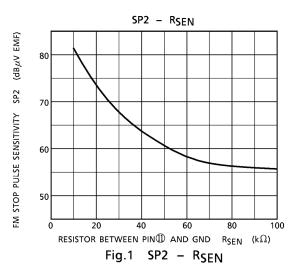
IF count signal can be taken from IF output terminal (pin(10)), when IF request terminal (pin(11)) is connected with GND through R_{SEN} and stop pulse sensitivity is on.

	Switch C	Condition	Output	Output	Output
	Pin(11)	Pin(19)	Frequency	Voltage (typ.)	Impedance (typ.)
FM	OPEN	OPEN	10.7MHz	80mV _{p-p}	2 kΩ
AM	(R _{SEN} ≤ 100kΩ)	V _{CC}	450kHz	100mV _{p-p}	2 132



4. FM stop pulse sensitivity

FM stop pulse sensitivity can be changed by connecting an external resistor RSEN between IF request terminal (pin(11)) and GND. The gain of FM IF amplifier is controlled by RSEN, and FM stop pulse sensitivity is changed. It is advised that RSEN is $100k\Omega$ or less.



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5. MPX IN

This IC can attenuate only FM stereo main signal level. Because MPX input for main signal (pin(20)) is independent of that for pilot signal (pin(21), this terminal is shared with AM input). Internal circuit of MPX in terminal (pin(20)) is shown in fig.2. FM stereo main signal level can be attenuated by connecting an external resistor RX. Because FM stereo main signal level is attenuated by internal resistor (14k Ω and 10k Ω) and resistor RX. And it is applied to an emitter of transistor Q.

6. AM tweets

It is necessary to connect the capacitor Ct (about 330pF) between V_{CC} terminal (pin(6)) and DET OUT (pin(22)), in case that AM tweets occurs by pattern layout. (Fig.3) This Ct can also functions as phase compensation of MPX stage.

7. AM AGC

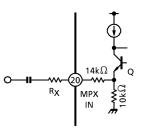
It is necessary to connect a capacitor C (about 68pF) between V_{CC} terminal (pin(8)) and AM IF IN terminal (pin(6)), in case that AGC circuit doesn't operate normally. Because second harmonic of local oscillator is applied AM IF terminal (pin(6)) by pettern layout.

In case that capacitor (C = 68pF) is connected, AM stop pulse sensitivity is delayed about 1dB.

Output DC voltage of AGC terminal (pin(8)) is $V_8 = 0.1V$ (typ., AM mode, at no signal).

8. Stereo indicator / AM IF output (for AM stereo)

• The terminal of pin(13) is built in stereo indicator function and modulated AM IF signal output. In AM mode, RIF (3k Ω or less) should be connected between V_{CC} and pin(13), or pin(13) should connected to V_{CC} directly. (Fig.5) In case that RIF >3k Ω or pin (13) is opened, this IC doesn't operate normally in AM mode. Because transistor Q₂ is saturated.



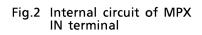




Fig.3 Countermeasures for AM tweets



Fig.4 AM trap circuit

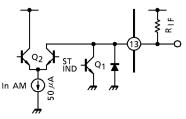
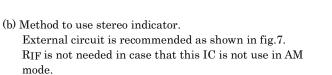
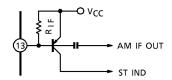


Fig.5 Internal circuit of pin⁽³⁾

- AM IF output V_{IF} (AM MOD) = 6m V_{rms} (typ. R_{IF} = 3k Ω) The applications of this terminal are explained as follow.
- (a) Method to use stereo indicator and AM IF output External circuit is recommended as shown in fig.6.



(c) Method to use AM IF output External circuit is recommended as shown in fig.8.



Method to use stereo Fig.6 indicator and AM IF output.

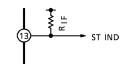


Fig.7 Method to use stereo indicator.

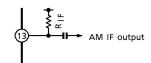


Fig.8 Method to use AM IF output.



Fig.9 In case that functions of this terminal are not used.

(d) In case that functions of this terminal are not used. External circuit is recommended as shown in fig.9. RIF is not needed in case that this IC is not used in AM mode.

	-	-
Characteristic	Symbol	Ratinę
Quarkunskana	V	4 5

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5	V
Stop pulse voltage	V _{SP}	4.5	V
Stop pulse current	I _{SP}	10	mA
Stereo indicator voltage	V _{ST}	4.5	V
Stereo indicator current	I _{ST}	10	mA
Power dissipation	P _D (Note)	500	mW
Operating temperature	T _{opr}	-25~75	°C
Storage temperature	T _{stg}	-55~150	°C

Note: Derated above Ta = 25°C in the proportion of 4mW / °C.

Electrical Characteristics

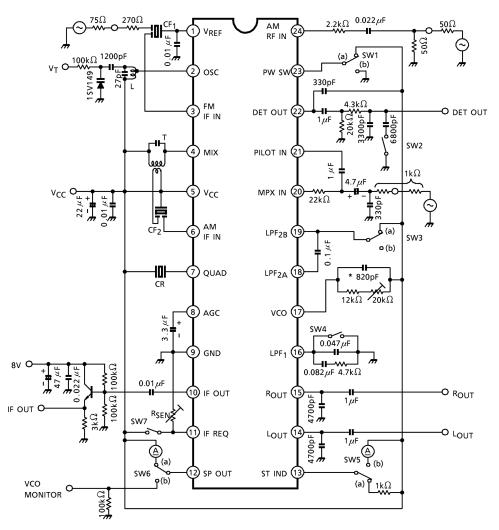
Unless Otherwise Specified

V_{CC} = 1.2V, Ta = 25°C, SW1: a, SW4: OPEN, SW5: a, SW6: a / b, SW7: ON FM IF: f = 10.7MHz, f_m = 1kHz, Δf = ±22.5kHz, V_{in} = 80dBµV, EMF, SW2: ON, SW3: b AM: f = 1MHz, f_m = 1kHz, MOD = 30%, V_{in} = 60dBµV, EMF, SW2: OPEN, SW3: a MPX: f_m = 1kHz, f_p = 19kHz, SW3:b

Characteristic		Symbol	Test Circuit	Test Condition	Min.	Тур.	Max.	Unit
		I _{CC1}		PW off, SW1: b	_	_	5	μA
Sup	oply current	I _{CC2}	1	FM mode, V _{in} = 0	-	5.5	7.5	mA
		I _{CC3}		AM mode, V _{in} = 0		3.7	5.5	
	Input limiting sensitivity	V _{in (lim)}		–3dB limiting	43	48	53	dBµV EMF
	Recovered output voltage 1	V _{OD1}			35	55	70	mV _{rms}
	Signal to noise ratio 1	S / N1			_	60	_	dB
≞	Total harmonic distortion 1	THD1	1		-	0.5	-	%
μR	AM rejection ratio	AMR	'	MOD = 30%	—	40	_	dB
	Stop pulse sensitivity 1	SP1		I ₁₂ = 0.5mA, SW6: a	50	55	60	dBµV
	Stop pulse sensitivity 2	SP2		I ₁₂ = 0.5mA, R _{SEN} = 39kΩ SW6: a, SW7: OPEN	_	64	_	EMF
	IF output voltage	V _{IF (FM)}		Δf = 0, SW7: OPEN	_	80	_	mV_{p-p}
	Gain	G _V		V _{in} = 26dBµV EMF	15	27	-	mV _{rms}
	Recovered output voltage 2	V _{OD2}			30	45	60	111 v rms
	Signal to noise ratio2	S / N2			_	38	-	dB
Σ	Total harmonic distortion 2	THD2	1		—	1.5	_	%
Α	Stop pulse sensitivitiy	SP3		I ₁₂ = 0.5mA, SW6: a	25	30	35	dBµV EMF
	IF output voltage	V _{IF (AM)}		MOD = 0, SW7: OPEN	_	100	_	mV_{p-p}
	Oscillator voltage	V _{osc}	2		30	55	_	mV _{rms}
	Oscillator stop voltage	V _{stop}			_	_	0.95	V

Characteristic		Symbol	Test Circuit	Test Co	ndition	Min.	Тур.	Max.	Unit	
	Voltage gain 1		G _{V (FM)}				-1.5	+0.5	+2.5	
	Voltage gain 2		G _{V (AM)}		V _{in} = 100mV _{rms}	(mono)	-0.5	+1.5	+3.5	dB
	Channel balance	;	СВ				-2	_	+2	
	Maximum comp input level	osite signal	V _{in (MAX)}		L + R =90%, P = THD = 3%	10%	_	220	_	mV _{rms}
		Mono	THD3		V _{in} = 100mV _{rms}	(mono)		0.2	0.5	
	Total harmonic distortion	Stereo	THD4		L + R =90mV _{rms}	, P = 10mV _{rms}		0.3	_	%
MPX		AM	THD5		V _{in} = 100mV _{rms}	V _{in} = 100mV _{rms}		0.2	_	
	Separation			1	L + R =90mV _{rms} P = 10mV _{rms}	f _m = 100Hz		36	—	
			SEP			^s f _m = 1kHz	25	35	_	dB
						f _m = 10kHz	_	34	_	
	Stereo indicator	On	ST (ON)		Pilot signal input I ₁₃ = 0.5mA, SW5: a		_	5.5	8	mV _{rms}
	sensitivity	Off	ST (OFF)				2	4	_	^{III v} rms
	Stereo indicator	hysteresis	V _H		To indicator turn turn on	To indicator turn off from turn on		1.5	_	mV _{rms}
	Capture range		CR		P = 10mV _{rms,} f _p = 19kHz		_	±7	_	%
	Signal noise rati	o 3	S / N3		V _{in} = 100mV _{rms} (mono)		_	65	_	dB
Dou	ver switch	n current	I ₂₃			SW1: c	5	_	_	μA
FUV)ff voltage	V ₂₃			SW1: d	0	_	0.3	V
AM	AM mode on current		I ₁₉	2	V _{CC} = 0.95V	SW3: c	50	_	_	μA
FM	FM mono mode on voltage V16		V ₁₆			SW4: ON		0.1	—	V
IF re	equest off voltage		V ₁₁			SW7: ON	0.9	_		V

TEST CIRCUIT 1



* Temperature coefficient : SH (-330ppm/°C)

CF1 : SFE10.7MA8 (MURATA Co., Ltd.)

CF2 : PFA450A (MURATA Co., Ltd.)

CR : CDA10.7MG50 (MURATA Co., Ltd.)

• For measurement of the THD4, Vin (Max.), and SEP the filter shown in Figure 10 is used. ATT ~: ATT $\geq 60dB$ (f = 19~100kHz)

Filter : 4501-054 (SUMIDA ELECTRIC Co., LTD) with insertion loss of 0.3dB

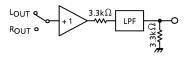


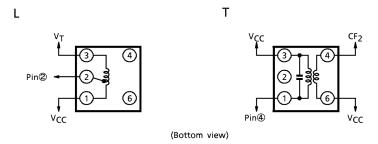
Fig.10 Filter circuit

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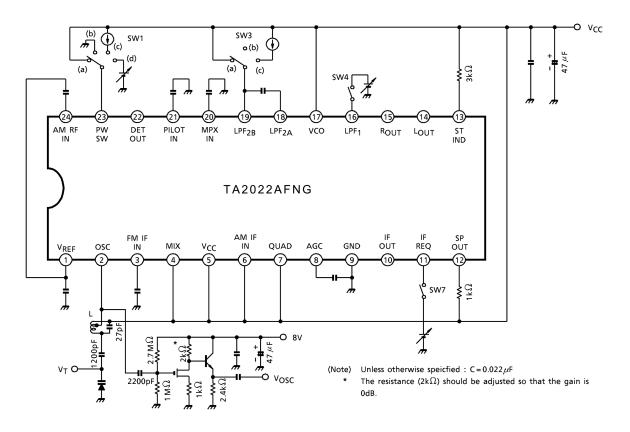
Coil Data

Cail	Test L		Co	Qo		Tu	rns		Wire	Reference
Coil	Frequency	(µH)	(pF)	QU	1–2	2–3	1–3	4–6	(mmø)	(coil no.)
L AM OSC	796kHz	100	_	85	13	55	_	_	0.06UEW	(S)4187–144
T AM IFT	450kHz	I	180	65	—	—	184	29	0.05UEW	(S)4161–242

(S): SUMIDA ELECTRIC Co., LTD.

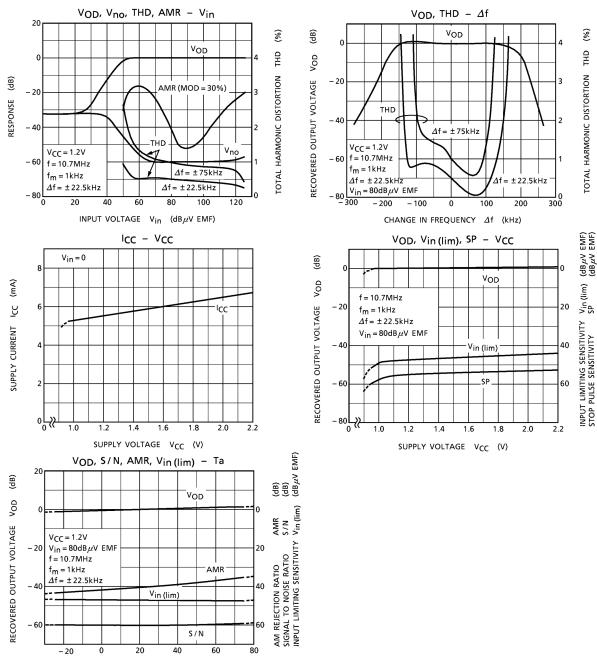


TEST CIRCUIT 2



Characteristic Curve

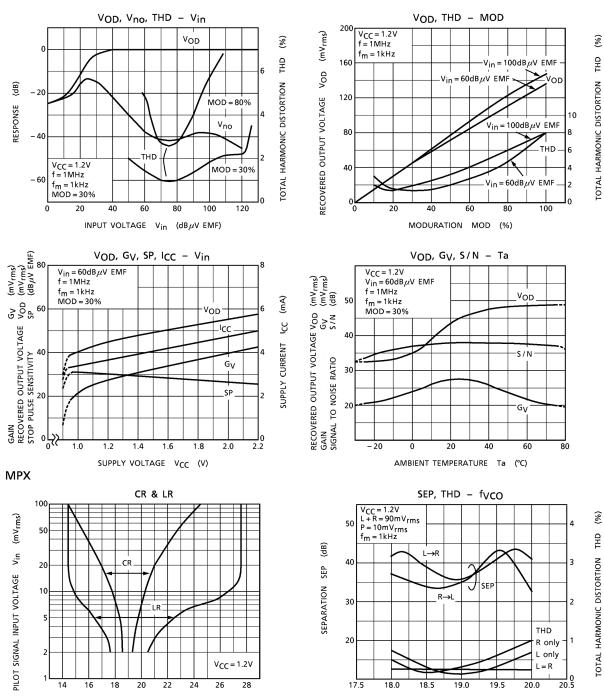
FM IF



AMBIENT TEMPERATURE Ta (°C)

<u>TOSHIBA</u>

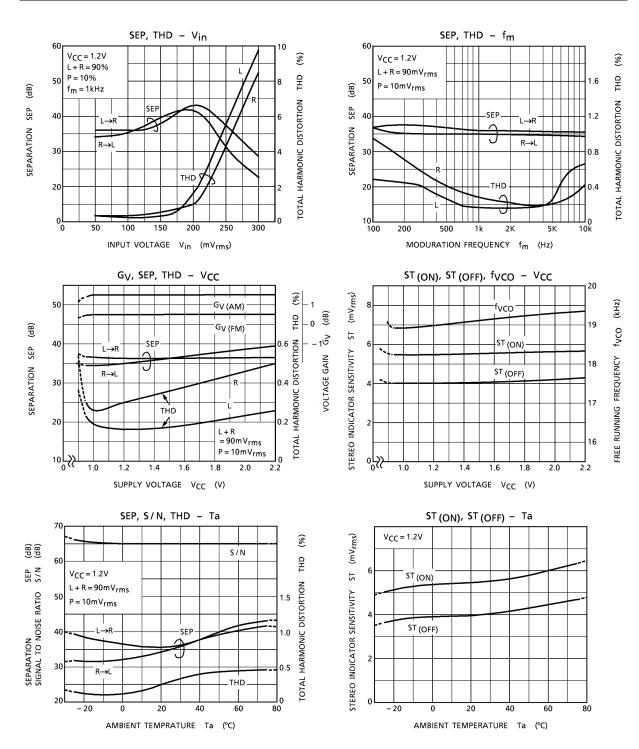
AM



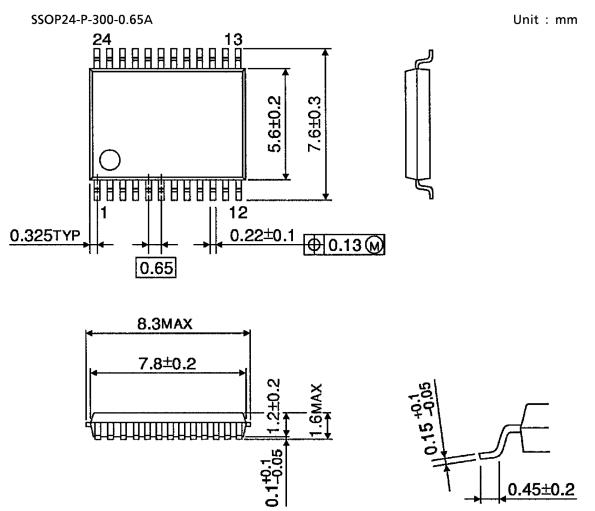
FREE RUNNING FREQUENCY fVCO (kHz)

FREE RUNNING FREQUENCY fVCO

(kHz)



Package Dimensions



Weight: 0.14g (typ.)

RESTRICTIONS ON PRODUCT USE

060116EBA

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About solderability, following conditions were confirmed

- Solderability
 - (1) Use of Sn-37Pb solder Bath
 - solder bath temperature = 230°C
 - · dipping time = 5 seconds
 - \cdot the number of times = once
 - use of R-type flux
 - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
 - solder bath temperature = 245°C
 - · dipping time = 5 seconds
 - \cdot the number of times = once
 - · use of R-type flux